

## Product Overview

### NB3N511: PLL Clock Multiplier, 4 MHz - 200 MHz, 3.3 V / 5.0 V

For complete documentation, see the data sheet.

The NB3N511 is a clock multiplier that will generate one of nine selectable output multiples of an input frequency via two 3 level select inputs (S0, S1). It accepts a standard fundamental mode crystal or an external reference clock signal. Phase Locked Loop (PLL) design techniques are used to produce a low jitter, TTL level clock output up to 200 MHz with a 50% duty cycle. An Output Enable (OE) pin is provided, and when asserted low, the clock output goes into tri-state (high impedance). The NB3N511 is commonly used in electronic systems as a cost efficient replacement for crystal oscillators

### Features

- Clock Output Frequencies up to 200 MHz
- Nine Selectable Multipliers of the Input Frequency
- Operating Range: VDD = 3.3 V +/-10% or 5.0 V +/-5%
- Low Jitter Output of 25 ps One Sigma (rms)
- Zero ppm Clock Multiplication Error
- 45% to 55% Output Duty Cycle
- TTL/CMOS Output with 25 mA TTL Level Drive
- Crystal Reference Input Range of 5 to 32 MHz
- Input Clock Frequency Range of 1 to 50 MHz
- OE, Output Enable with Tri State Output

For more features, see the data sheet

### Applications

- Networking and Telecommunications
- Consumer
- Industrial
- Medical

### End Products

- Servers
- Routers
- Set-Top Box

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