



Bill of Materials for the LV8498CTGEVB Evaluation Board

Designator	Quantity	Description	Value	Tolerance	Footprint	Manufacturer	Manufacturer Part Number	Substitution Allowed	Lead Free
C1	1	VCC Bypass	0.1µF, 50V	±10%	-	Murata	GRM188B31H104KA92*	Yes	Yes
*1)	1	Pull-up Resistor for terminal SDA	1k Ω, 1/10W	±5%	-	KOA	RK73B1JT**102J	Yes	Yes
*1)	1	Pull-up Resistor for terminal SCL	1k Ω, 1/10W	±5%	-	KOA	RK73B1JT**102J	Yes	Yes
*2)	1	Pull-up Resistor for terminal ENA	100k Ω, 1/10W	±5%	-	KOA	RK73B1JT**104J	Yes	Yes
IC1	1	Motor Driver	-	-	-	ON Semiconductor	LV8498CT	No	Yes
CNCT1	1	Pin Header straight Type	-	-	-	HIROSE	DF1-6P-2.5DS	Yes	Yes
TP1-TP5	5	Test Point	-	-	-	MAC8	ST-1-3	Yes	Yes

*1) It is the recommended part. If I2C Master device does not have pull-up resistors for SDA and SCL lines, mount on the space.

*2) It is the part we recommend. If the ENA pin is to be used with pull-up.