

ON Semiconductor

Is Now

onsemi™

To learn more about onsemi™, please visit our website at
www.onsemi.com

onsemi and **onsemi** and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi** product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner. Other names and brands may be claimed as the property of others.



ON Semiconductor®

Strata Enabled LDO EVK User Guide and Test Report

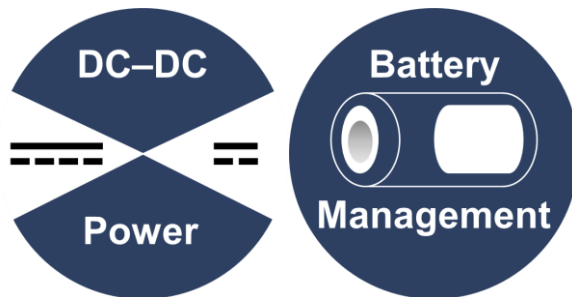


Table of Contents

INTRODUCTION.....	3
Features.....	3
Applications.....	3
USER GUIDE	4
Hardware Setup	4
User Interface	4
TEST REPORT	6
Thermals	6
NCV8163/NCP163 Transients	6
NCV8170/NCP170 Transients	8
NCP110 Transients.....	9
NCP115 Transients.....	11

Introduction

The Strata Enabled LDO EVK provides an easy to use evaluation kit within the Strata Development Environment for many series of XDFN package LDOs. Through Strata, the developer can access datasheets, BOMs, schematics, and other collateral they may need. This document will provide instructions on how to use the evaluation kits as well as provide all the measurement results for these LDOs in these kits.

Features

- 4 different LDO series to choose from, each with a range of voltage options
- V_{in} range from 1.1V to 5.5V
- Max load range from 150mA to 300mA
- V_{out} range from 0.6V to 5.3V
- Automotive options available

Applications

- DC-DC Power
- Battery Powered Devices
- Analog and RF
- Automotive

User Guide

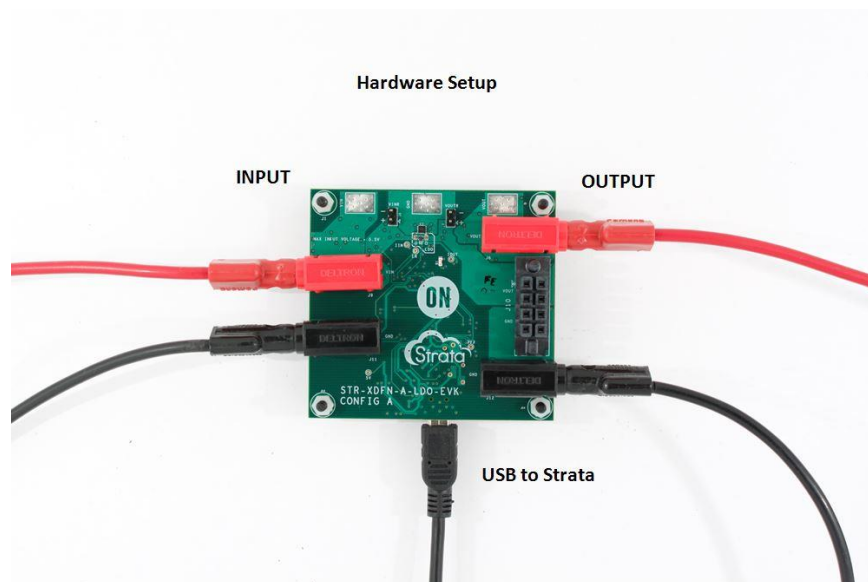
This section will explain how to use the Strata Enable LDO EVK in a step by step manner, and will cover both the hardware required as well as how to use the User Interface in Strata.

Hardware Setup

The hardware required for using the Strata Enabled LDO EVK are a computer (with Windows), a power supply, and a load. Follow the steps below.

1. Plug the power supply into the input of the LDO board using the banana plugs J9 and J11. Do not apply over 5.5V to the input because this will break the board. Depending on the LDO on the board, the UI will tell the user the minimal amount of voltage needed.
2. Connect the computer and the LDO using the mini USB connector J18 on the bottom of the board.
3. Plug the load into the output using the banana plugs J8 and J12.
4. There needs to be at least 100mV of dropout allowed at maximum load to regulate accurately to the desired output voltage.

A picture of the setup can be found below.



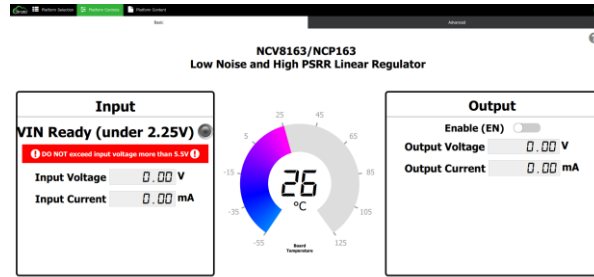
User Interface

The UI within the Strata app will allow the user to control the LDO and monitor its telemetry without needing other lab equipment or training to do so. The steps below cover what is in the UI.

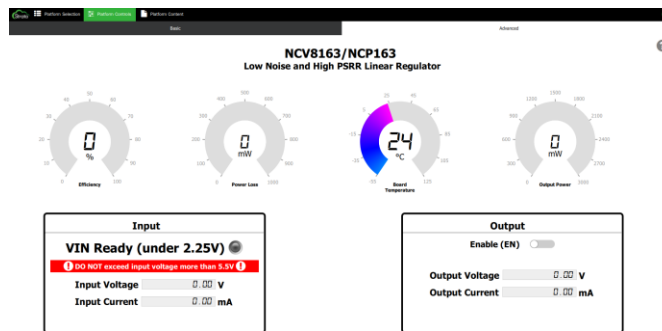
1. First, open the Strata app. Login and the home screen will appear.



- The app will automatically detect the device that is plugged in and will bring up the UI for the board that is plugged in.



- The view that comes up is the basic view, which offers basic telemetry and an enable switch for enabling/disabling the LDO.
- In the top right hand corner the user can switch to the Advanced view which is shown below. The Advanced view offers more telemetry for the user to monitor.



- The round button with a question mark in the top right corner is the Help button, and will show the user what everything on the UI is doing.
- To look at the collateral provided with the EVK, click on the “Platform Content” tab at the top of the screen.

Test Report

This section will report important results and measurements from testing the Strata Enabled LDO EVK.

Thermals

Thermal data for the board is shown below. The thermals were the same for all LDOs on the board since they are all in the same physical package. The worst case scenario is shown below, which is 5.5V_{in} and 1.2V_{out} at 250mA load.

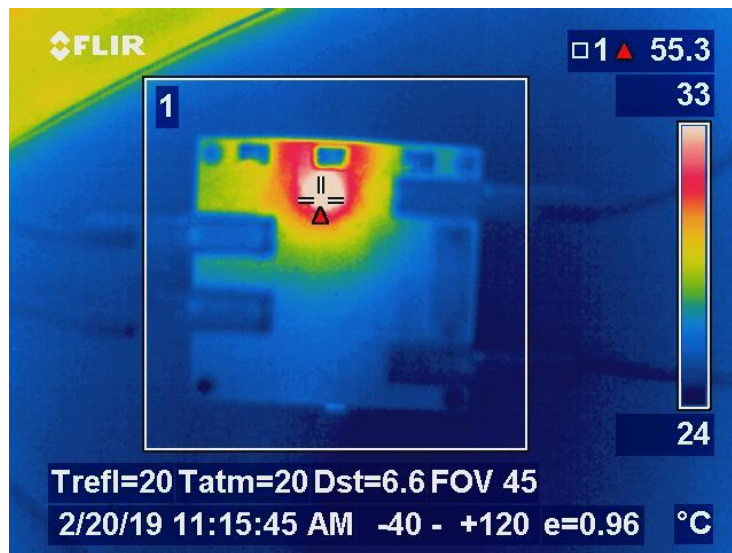


Figure 1: Thermal of LDO at 5.5V_{in}, 1.2V_{out}, and a load of 250mA.

NCV8163/NCP163 Transients

Transient data for the NCV8163/NCP163 LDO is included below.

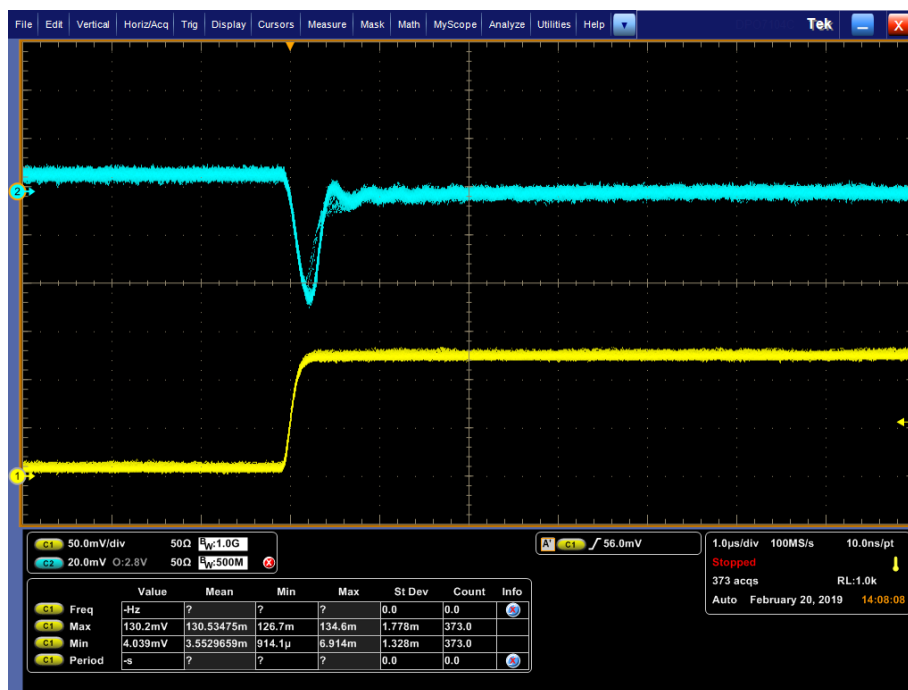


Figure 2: Load step from 0 to 250mA at 10kHz frequency.

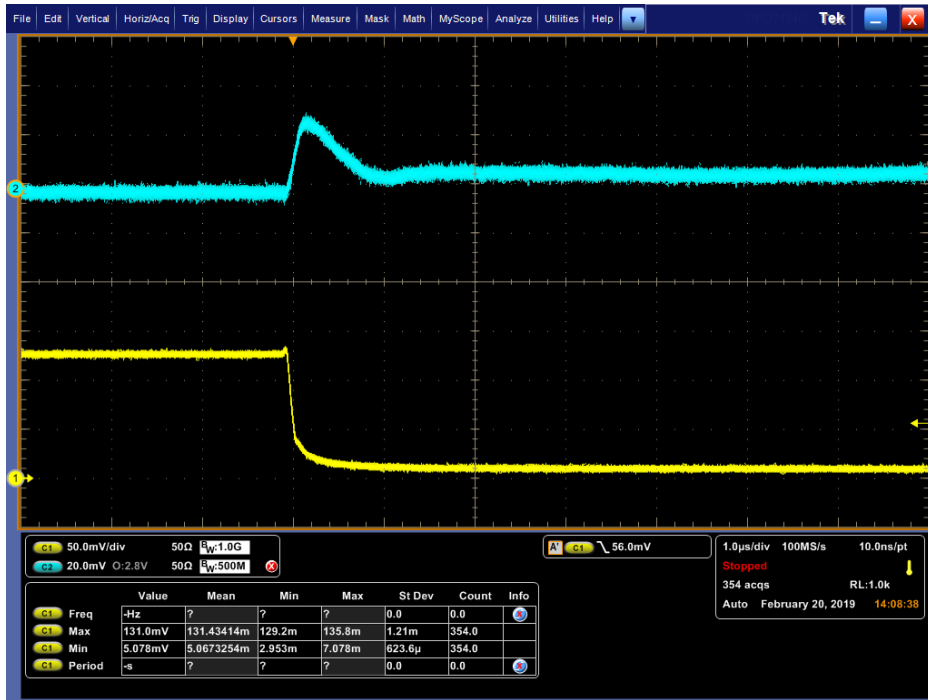


Figure 3: Load step from 250mA to 0A at 10kHz frequency.

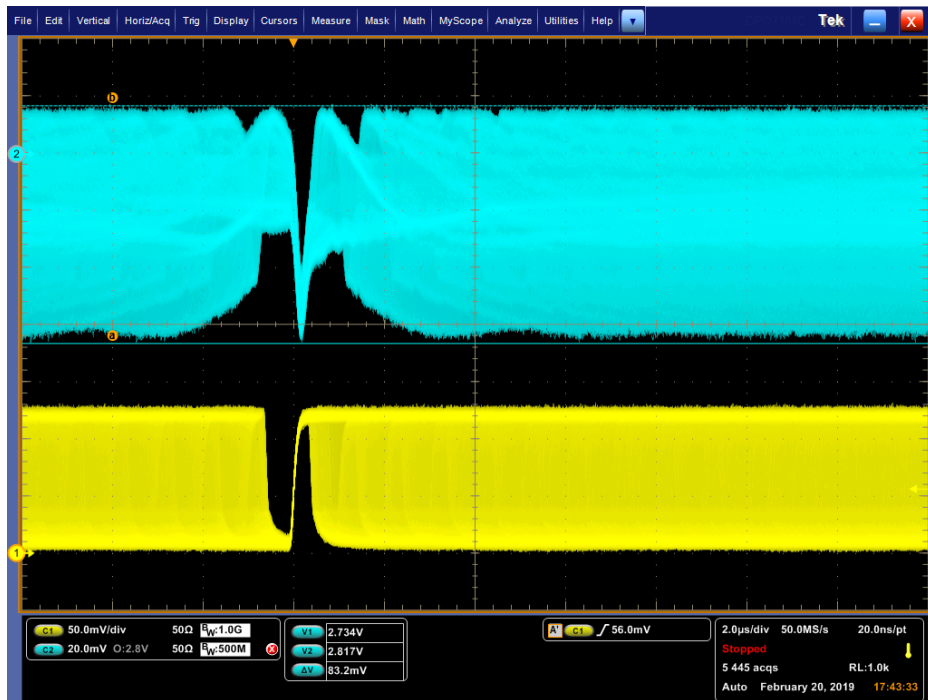


Figure 4: Transient sweep from 1kHz to 1MHz.

NCV8170/NCP170 Transients

Transient data for the NCV8170/NCP170 LDO is included below.

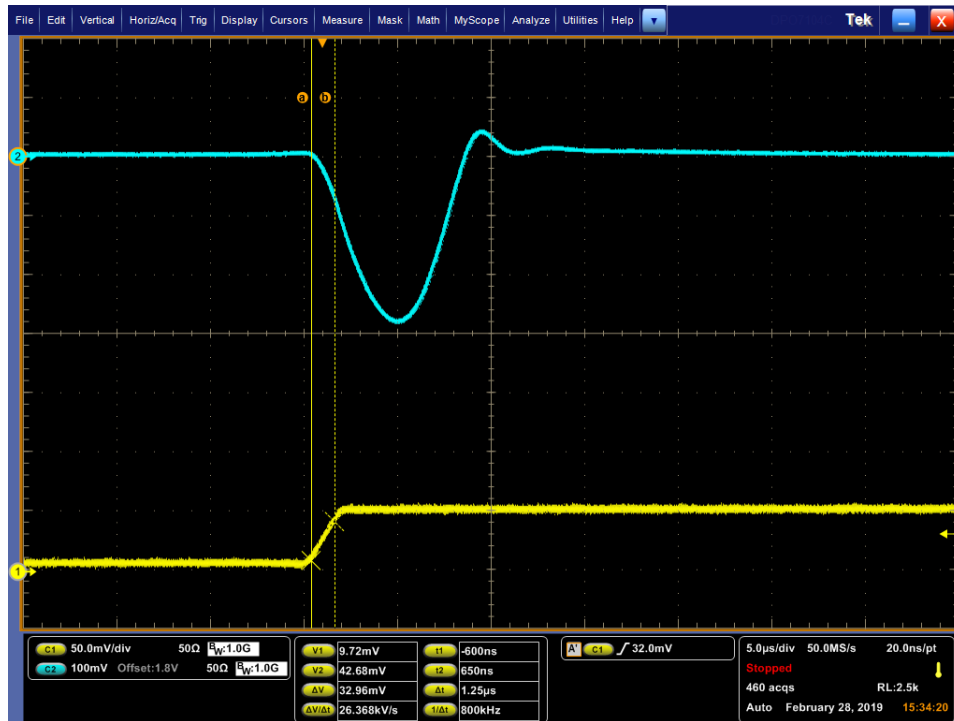


Figure 5: Load step from 0 to 125mA at 10kHz frequency.



Figure 6: Load step from 125mA to 0A at 10kHz frequency.

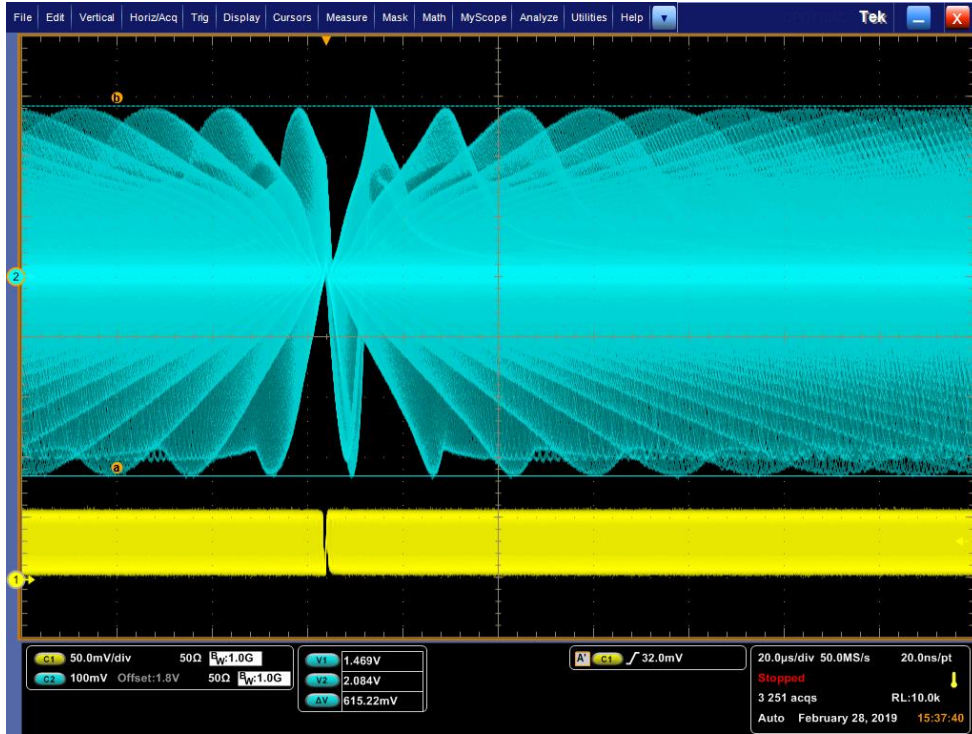


Figure 7: Transient Sweep from 1kHz to 1MHz.

NCP110 Transients

Transient data for the NCP110 LDO is included below.

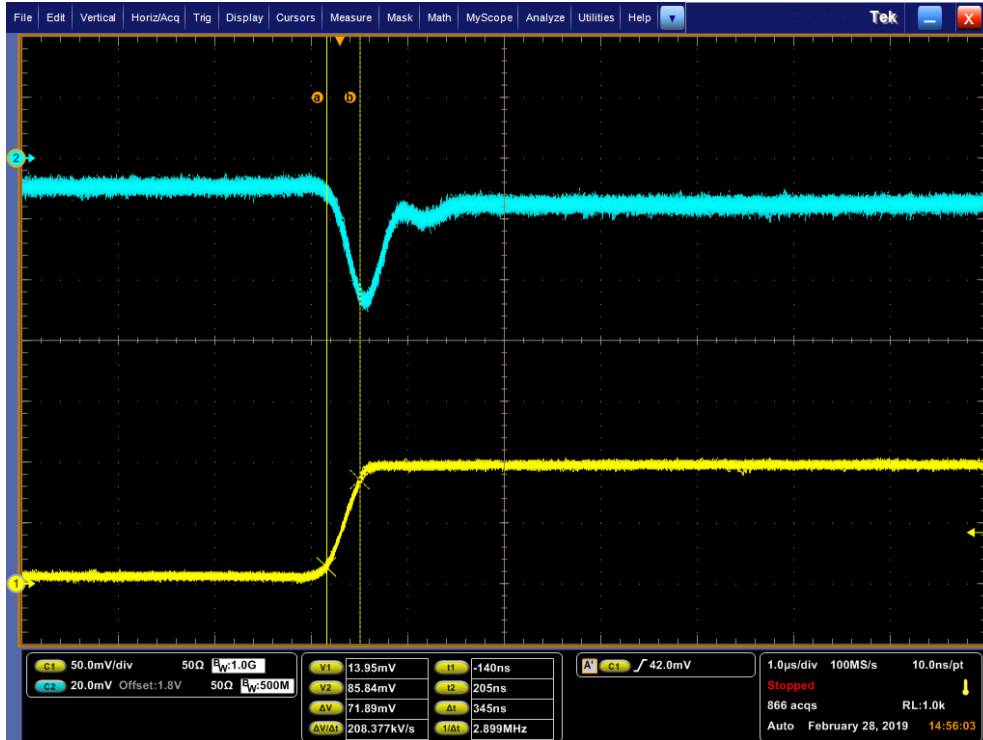


Figure 8: Load step from 0 to 200mA at 10kHz frequency.

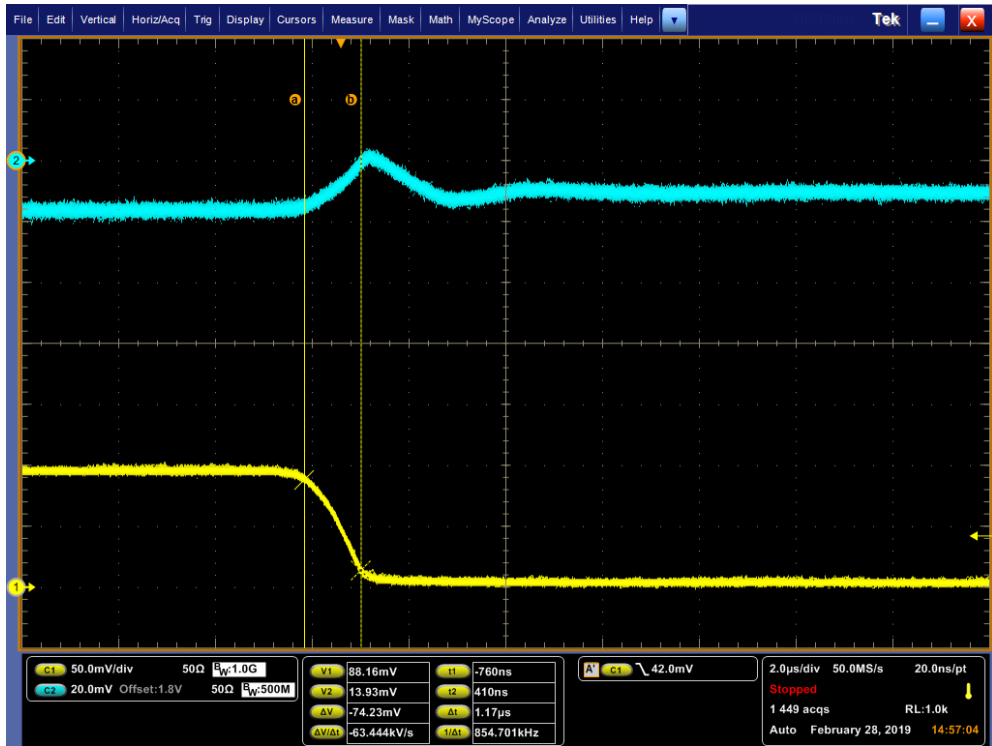


Figure 9: Load step from 200mA to 0A at 10kHz frequency.

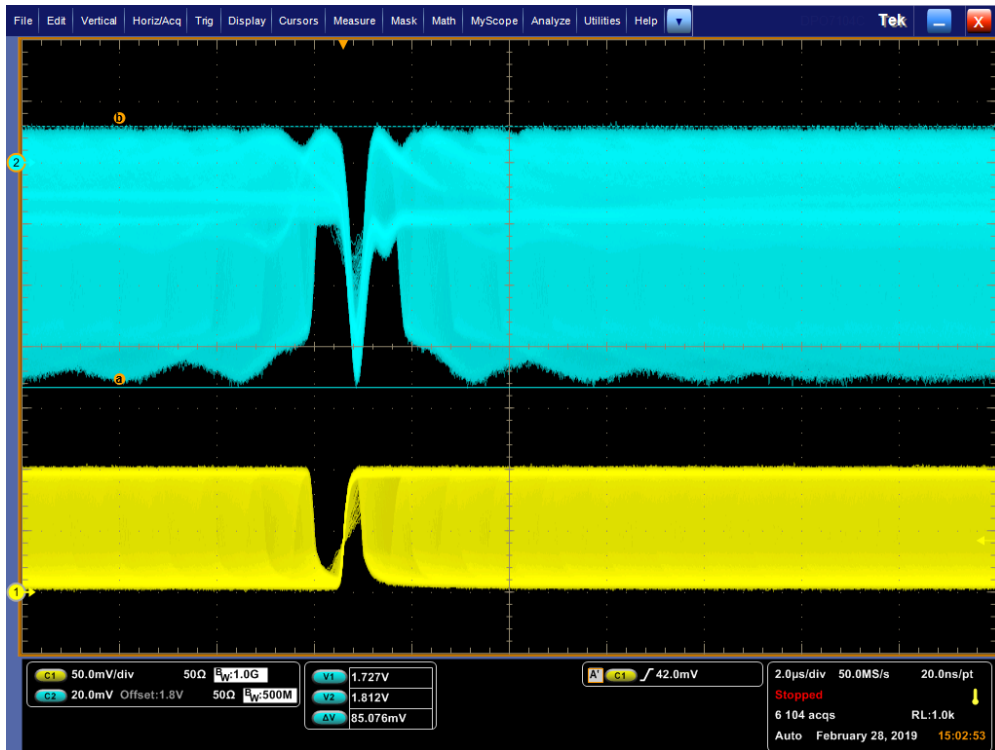


Figure 10: Transient sweep from 1kHz to 1MHz.

NCP115 Transients

Transient data for the NCP115 LDO is included below.

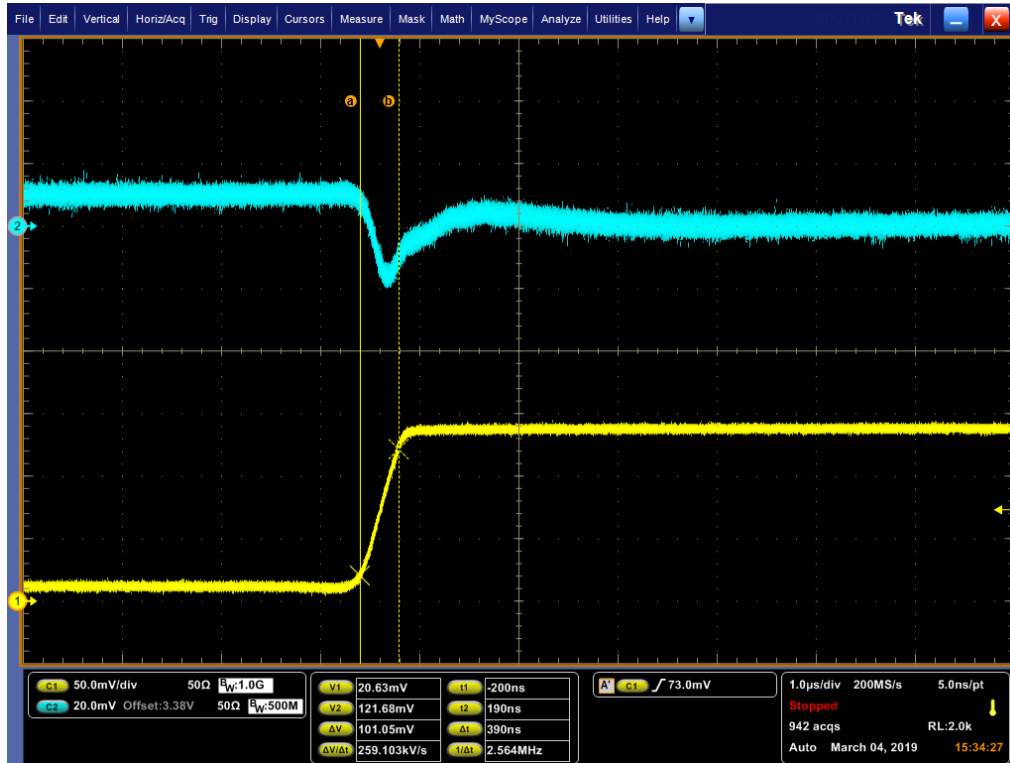


Figure 11: Load step from 0 to 300mA at 10kHz frequency.

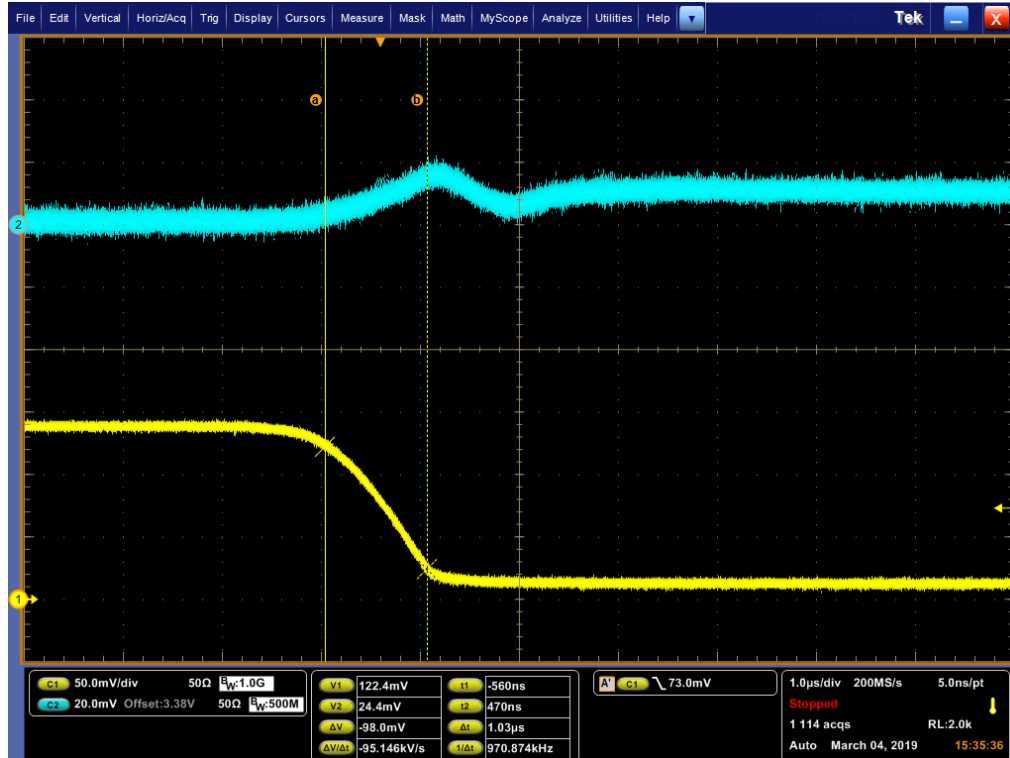


Figure 12: Load step from 300mA to 0A at 10kHz frequency.

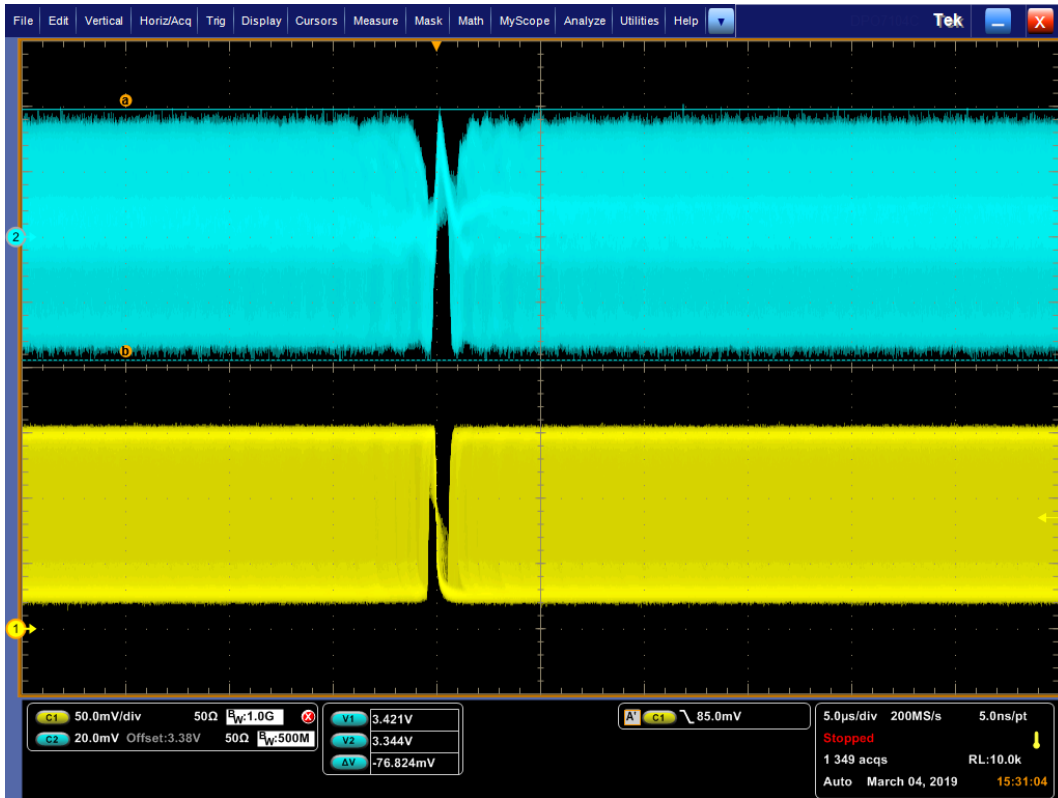


Figure 13: Transient sweep from 1kHz to 1MHz.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

The evaluation board/kit (research and development board/kit) (hereinafter the "board") is not a finished product and is as such not available for sale to consumers. The board is only intended for research, development, demonstration and evaluation purposes and should as such only be used in laboratory/development areas by persons with an engineering/technical training and familiar with the risks associated with handling electrical/mechanical components, systems and subsystems. This person assumes full responsibility/liability for proper and safe handling. Any other use, resale or redistribution for any other purpose is strictly prohibited.

The board is delivered "AS IS" and without warranty of any kind including, but not limited to, that the board is production-worthy, that the functions contained in the board will meet your requirements, or that the operation of the board will be uninterrupted or error free. ON Semiconductor expressly disclaims all warranties, express, implied or otherwise, including without limitation, warranties of fitness for a particular purpose and non-infringement of intellectual property rights.

ON Semiconductor reserves the right to make changes without further notice to any board.

You are responsible for determining whether the board will be suitable for your intended use or application or will achieve your intended results. Prior to using or distributing any systems that have been evaluated, designed or tested using the board, you agree to test and validate your design to confirm the functionality for your application. Any technical, applications or design information or advice, quality characterization, reliability data or other services provided by ON Semiconductor shall not constitute any representation or warranty by ON Semiconductor, and no additional obligations or liabilities shall arise from ON Semiconductor having provided such information or services.

The boards are not designed, intended, or authorized for use in life support systems, or any FDA Class 3 medical devices or medical devices with a similar or equivalent classification in a foreign jurisdiction, or any devices intended for implantation in the human body. Should you purchase or use the board for any such unintended or unauthorized application, you shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the board.

This evaluation board/kit does not fall within the scope of the European Union directives regarding electromagnetic compatibility, restricted substances (RoHS), recycling (WEEE), FCC, CE or UL, and may not meet the technical requirements of these or other related directives.

FCC WARNING – This evaluation board/kit is intended for use for engineering development, demonstration, or evaluation purposes only and is not considered by ON Semiconductor to be a finished end product fit for general consumer use. It may generate, use, or radiate radio frequency energy and has not been tested for compliance with the limits of computing devices pursuant to part 15 of FCC rules, which are designed to provide reasonable protection against radio frequency interference. Operation of this equipment may cause interference with radio communications, in which case the user shall be responsible, at its expense, to take whatever measures may be required to correct this interference.

ON Semiconductor does not convey any license under its patent rights nor the rights of others.

LIMITATIONS OF LIABILITY: ON Semiconductor shall not be liable for any special, consequential, incidental, indirect or punitive damages, including, but not limited to the costs of requalification, delay, loss of profits or goodwill, arising out of or in connection with the board, even if ON Semiconductor is advised of the possibility of such damages. In no event shall ON Semiconductor's aggregate liability from any obligation arising out of or in connection with the board, under any theory of liability, exceed the purchase price paid for the board, if any.

PUBLICATION ORDERING INFORMATION

LITERATURE FULLFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E.
32nd Pkwy, Aurora, Colorado 80011 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support:

800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910

ON Semiconductor Website:

<https://www.onsemi.com/>

Order Literature:

<https://www.onsemi.com/orderlit>

For additional information, please contact your local Sales Representative