



INITIAL PRODUCT/PROCESS CHANGE NOTIFICATION
Generic Copy

Issue Date: 29-Jan-2007

SUBJECT: ON Semiconductor Initial Product/Process Change Notification # 15715

TITLE: DUAL SOURCING OF SELECTED DEVICES AT ONPY1 IN SLOVAKIA

PROPOSED FIRST SHIP DATE: 31-May-2007

AFFECTED CHANGE CATEGORY: ON SEMICONDUCTOR WAFER FAB SITE

AFFECTED PRODUCT DIVISION: ANALOG PRODUCTS

FOR ANY QUESTIONS CONCERNING THIS NOTIFICATION:

Contact your local ON Semiconductor Sales Office or Alan Garlington <alan.garlington@onsemi.com>

NOTIFICATION TYPE:

Initial Product/Process Change Notification (IPCEN)

First change notification sent to customers. IPCENs are issued at least 120 days prior to implementation of the change. An IPCEN is advance notification about an upcoming change and contains general information regarding the change details and devices affected. It also contains the preliminary reliability qualification plan.

The completed qualification and characterization data will be included in the Final Product/Process Change Notification (FPCEN).

This IPCEN notification will be followed by a Final Product/Process Change Notification (FPCEN) at least 60 days prior to implementation of the change.

DESCRIPTION AND PURPOSE:

This is an initial PCN to notify customers of the intent to qualify and dual source specific devices currently being processed at the ON Semiconductor M1 wafer fab in the Czech Republic (ONCR) to the existing ON Semiconductor wafer fab in Piestany, Slovakia (ONPY1). The ONPY1 Wafer Fab will be used to provide additional capacity for the products specified below. The existing design database currently in use at ONCR will be transferred to ONPY1 with no change to the functional circuit design. New mask sets will be generated which will incorporate required process control and alignment patterns. Full electrical characterization and bench analysis will be performed on all devices transferred to ensure no change to device functionality or data sheet electrical specifications.

This is an initial PCN only. A Final PCN will be issued at least 60 days prior to actual use of the new Wafer Fab. Final PCN's will be issued as the specific devices are prepared for release in the new wafer facility and will contain specific dates for each device. Samples will be made available per the Final PCN schedule.



Initial Product/Process Change Notification # 15715

QUALIFICATION PLAN:

Full Temperature Electrical Distribution data for each device being qualified – Test over full temperature specifications per data sheet.

Reliability Qualification Vehicles: 1 lot each – LM317LBDR2G and MC7905BTG

Reliability testing as follows:

High Temperature Op Life	Ta = 125 C	168, 504 Hrs	80pcs, 1 lot
Preconditioning (PC) (SOIC part only) MSL1@260C			
AutoClave – PC Units	Ta = 121C; RH =100% PSIG = 15	96 Hrs	80pcs, 1 lot
Temp Cycle – PC Units	-65C to +150C	500 Cyc	80pcs, 1 lot
ESD – Human Body Model - 2000v min.			3pcs, 1 lot
ESD – Machine Model - 200v min.			3pcs, 1 lot
Latch Up			6pcs, 1 lot

AFFECTED DEVICE LIST:

PART

- LM317LBD
- LM317LBDG
- LM317LBDR2
- LM317LBDR2G
- LM317LBZ
- LM317LBZG
- LM317LBZRA
- LM317LBZRAG
- LM317LBZRP
- LM317LBZRPG
- LM317LD
- LM317LDG
- LM317LDR2
- LM317LDR2G
- LM317LZ
- LM317LZG
- LM317LZRA
- LM317LZRAG
- LM317LZRE
- LM317LZRM
- LM317LZRP
- LM317LZRPG
- MC78L05ABD
- MC78L05ABDG
- MC78L05ABDR2
- MC78L05ABDR2G



Initial Product/Process Change Notification # 15715

MC78L05ABP
MC78L05ABPG
MC78L05ABPRA
MC78L05ABPRAG
MC78L05ABPRE
MC78L05ABPREG
MC78L05ABPRM
MC78L05ABPRMG
MC78L05ACD
MC78L05ACDG
MC78L05ACDR2
MC78L05ACDR2G
MC78L05ACP
MC78L05ACPG
MC78L05ACPRA
MC78L05ACPRAG
MC78L05ACPRE
MC78L05ACPREG
MC78L05ACPRM
MC78L05ACPRMG
MC78L05ACPRP
MC78L05ACPRPG
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MC78L08ABDR2G
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MC78L08ABPRPG
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MC78L08ACDG
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MC78L08ACPRA
MC78L08ACPRAG
MC78L08ACPRE
MC78L08ACPREG
MC78L08ACPRP
MC78L08ACPRPG
MC78L09ABD
MC78L09ABDG
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MC78L09ABDR2G
MC78L09ABPRA
MC78L09ABPRAG
MC78L09ABPRP
MC78L09ABPRPG
MC78L09ACD
MC78L09ACDG
MC78L09ACDR2
MC78L09ACDR2G



Initial Product/Process Change Notification # 15715

MC78L09ACP
MC78L09ACPG
MC78L12ABD
MC78L12ABDG
MC78L12ABDR2
MC78L12ABDR2G
MC78L12ABP
MC78L12ABPG
MC78L12ABPRP
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MC78L12ACPRPG
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MC78L15ABDG
MC78L15ABDR2
MC78L15ABDR2G
MC78L15ABP
MC78L15ABPG
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MC78L15ABPRAG
MC78L15ABPRP
MC78L15ABPRPG
MC78L15ACD
MC78L15ACDG
MC78L15ACDR2
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MC78L18ACPRA
MC78L18ACPRAG
MC78L18ACPRM
MC78L18ACPRMG
MC78L18ACPRP
MC78L18ACPRPG
MC78L24ABP
MC78L24ABPG



Initial Product/Process Change Notification # 15715

MC78L24ACP
MC78L24ACPG
MC78L24ACPRA
MC78L24ACPRA
MC78L24ACPRAG
MC78L24ACPRP
MC78L24ACPRPG
MC7905.2CT
MC7905.2CTG
MC7905BD2T
MC7905BD2TG
MC7905BD2TR4
MC7905BD2TR4G
MC7905BT
MC7905BTG
MC7905CD2T
MC7905CD2TG
MC7905CD2TR4
MC7905CD2TR4G
MC7905CT
MC7905CTG
MC7906CD2T
MC7906CD2TG
MC7906CT
MC7906CTG
MC7908CD2T
MC7908CD2TG
MC7908CD2TR4
MC7908CD2TR4G
MC7908CT
MC7908CTG
MC7912BD2T
MC7912BD2TG
MC7912BD2TR4
MC7912BD2TR4G
MC7912BT
MC7912BTG
MC7912CD2T
MC7912CD2TG
MC7912CD2TR4
MC7912CD2TR4G
MC7912CT
MC7912CTG
MC7915BD2T
MC7915BD2TG
MC7915BT
MC7915BTG
MC7915CD2T
MC7915CD2TG
MC7915CD2TR4
MC7915CD2TR4G
MC7915CT
MC7915CTG
MC7918CT
MC7918CTG
MC7924BT
MC7924BTG



Initial Product/Process Change Notification # 15715

MC7924CD2T
MC7924CD2TG
MC7924CT
MC7924CTG
MC79M05BDT
MC79M05BDTG
MC79M05BDTRK
MC79M05BDTRKG
MC79M05BT
MC79M05BTG
MC79M05CDT
MC79M05CDTG
MC79M05CDTRK
MC79M05CDTRKG
MC79M05CT
MC79M05CTG
MC79M08BDT
MC79M08BDTG
MC79M08BDTRK
MC79M08BDTRKG
MC79M08BT
MC79M08BTG
MC79M08CDT
MC79M08CDTG
MC79M08CDTRK
MC79M08CDTRKG
MC79M08CT
MC79M08CTG
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MC79M15CDTG
MC79M15CDTRK
MC79M15CDTRKG
MC79M15CT
MC79M15CTG
TL431ACD
TL431ACDG
TL431ACDMR2
TL431ACDMR2G



Initial Product/Process Change Notification # 15715

TL431ACDR2
TL431ACDR2G
TL431ACLP
TL431ACLPG
TL431ACLPR
TL431ACLPRAG
TL431ACLPRAG
TL431ACLPRE
TL431ACLPREG
TL431ACLPRP
TL431ACLPRPG
TL431ACP
TL431ACPG
TL431AID
TL431AIDG
TL431AIDMR2
TL431AIDMR2G
TL431AIDR2
TL431AIDR2G
TL431AILP
TL431AILPG
TL431AILPRA
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TL431AILPRPG
TL431AIP
TL431AIPG