



PRODUCT BULLETIN
Generic Copy

20-JUN-2005

SUBJECT: ON Semiconductor Product Bulletin #14132

TITLE: MSL Rating Change SOIC 16 Wide Package

EFFECTIVE DATE: 20-Jun-2005

AFFECTED PRODUCT DIVISION: Analog Products

FOR ANY QUESTIONS CONCERNING THIS NOTIFICATION:

Contact your local ON Semiconductor sales office or Alan Garlington <RPR180@onsemi.com>

NOTIFICATION TYPE:

ON Semiconductor considers this change approved unless specific conditions of acceptance are provided in writing. To do so, contact your local ON Semiconductor sales office.

DESCRIPTION AND PURPOSE:

Several devices in the SOIC 16 Wide package have recently been qualified at the MSL1 - 260 C level. 3 Production lots passed MSL1 - 260 C Preconditioning and testing. Results:

Pre-Conditioned Auto Clave - 121C/100%RH/15 psig	0/240 at 96 hrs
Pre-Conditioned Temp Cycle - -65 to +150C	0/270 at 500 cyc
Pre-Conditioned HAST - 130C/85%RH	0/240 at 96 hrs

These devices were previously qualified at MSL3 which required the use of Dry Packaging techniques. These devices will no longer require the Dry Packing procedure and will be shipped using standard ON Semi materials and packaging.

AFFECTED DEVICE LIST (WITHOUT SPECIALS)

PART

NCV51411PWR2

NCV8501PDW100

NCV8501PDW100R2

NCV8501PDW25

NCV8501PDW25R2

NCV8501PDW33

NCV8501PDW33R2



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- NCV8501PDW50
- NCV8501PDW50R2
- NCV8501PDW80
- NCV8501PDW80R2
- NCV8501PDWADJ
- NCV8501PDWADJR2
- NCV8502PDW100
- NCV8502PDW100R2
- NCV8502PDW25
- NCV8502PDW25R2
- NCV8502PDW33
- NCV8502PDW33R2
- NCV8502PDW50
- NCV8502PDW50R2
- NCV8502PDW80
- NCV8502PDW80R2
- NCV8502PDWADJ
- NCV8502PDWADJR2
- NCV8503PW25
- NCV8503PW25R2
- NCV8503PW33
- NCV8503PW33R2
- NCV8503PW50
- NCV8503PW50R2
- NCV8503PWADJ
- NCV8503PWADJR2
- NCV8504PW25
- NCV8504PW25R2
- NCV8504PW33
- NCV8504PW33R2
- NCV8504PW50
- NCV8504PW50R2
- NCV8504PWADJ
- NCV8504PWADJR2
- NCV8509PDW18
- NCV8509PDW18R2
- NCV8509PDW25
- NCV8509PDW25R2



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NCV8509PDW26
NCV8509PDW26R2
NCV8820PW
NCV8820PWR2