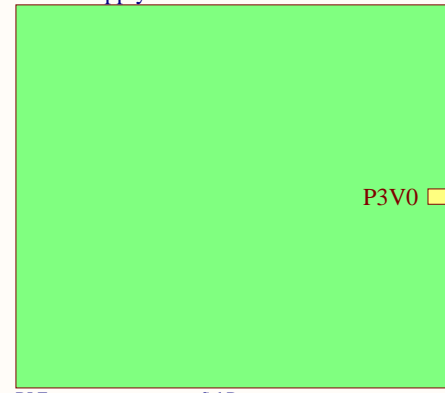


Power supply



BLE_power_management.SchDoc

BLE signals



BLE_signals.SchDoc

Sensors



Sensors.SchDoc

SENSORS_ON

SPI_IF

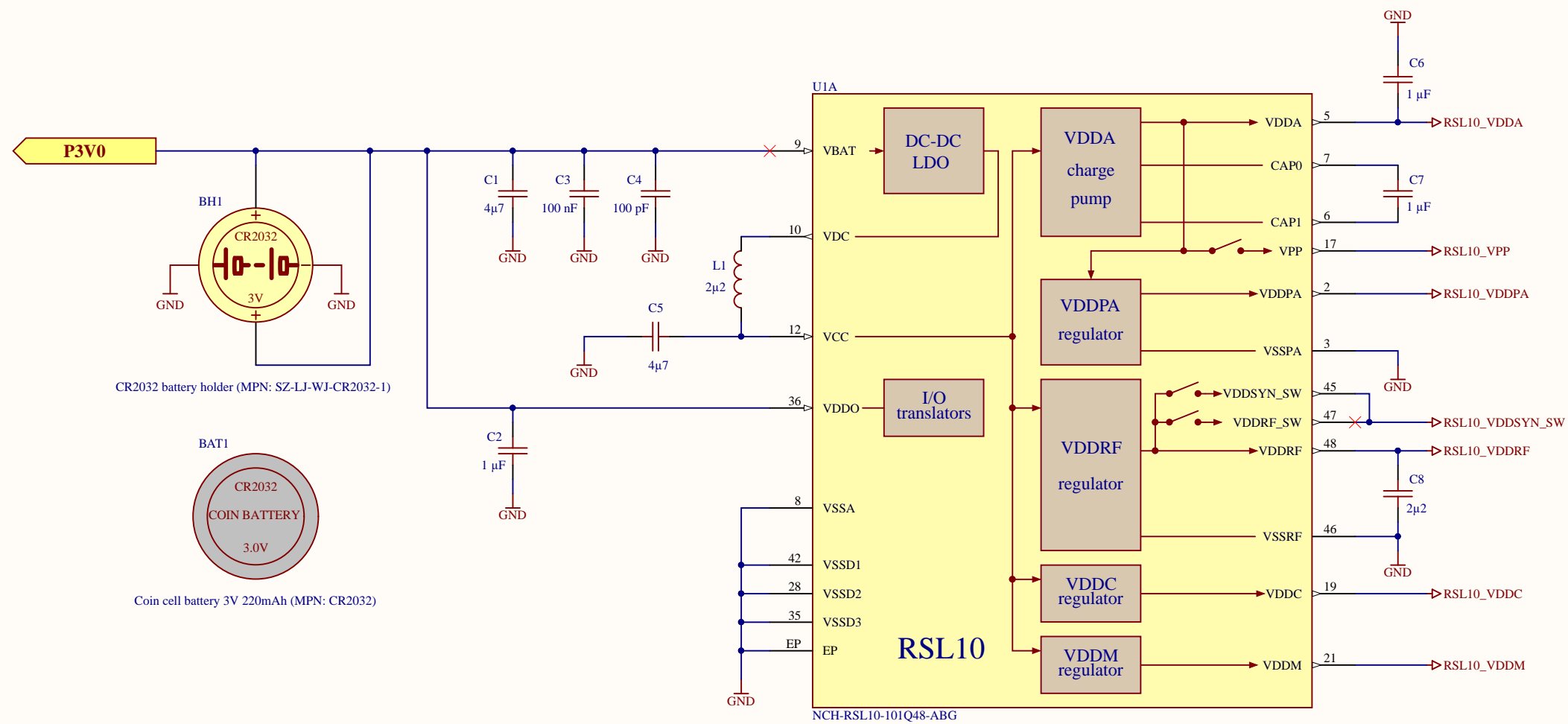
IRQ

SENSORS_ON

SPI_IF

IRQ

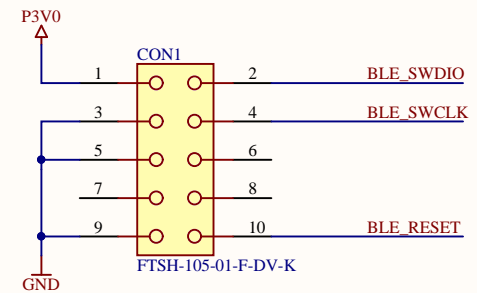
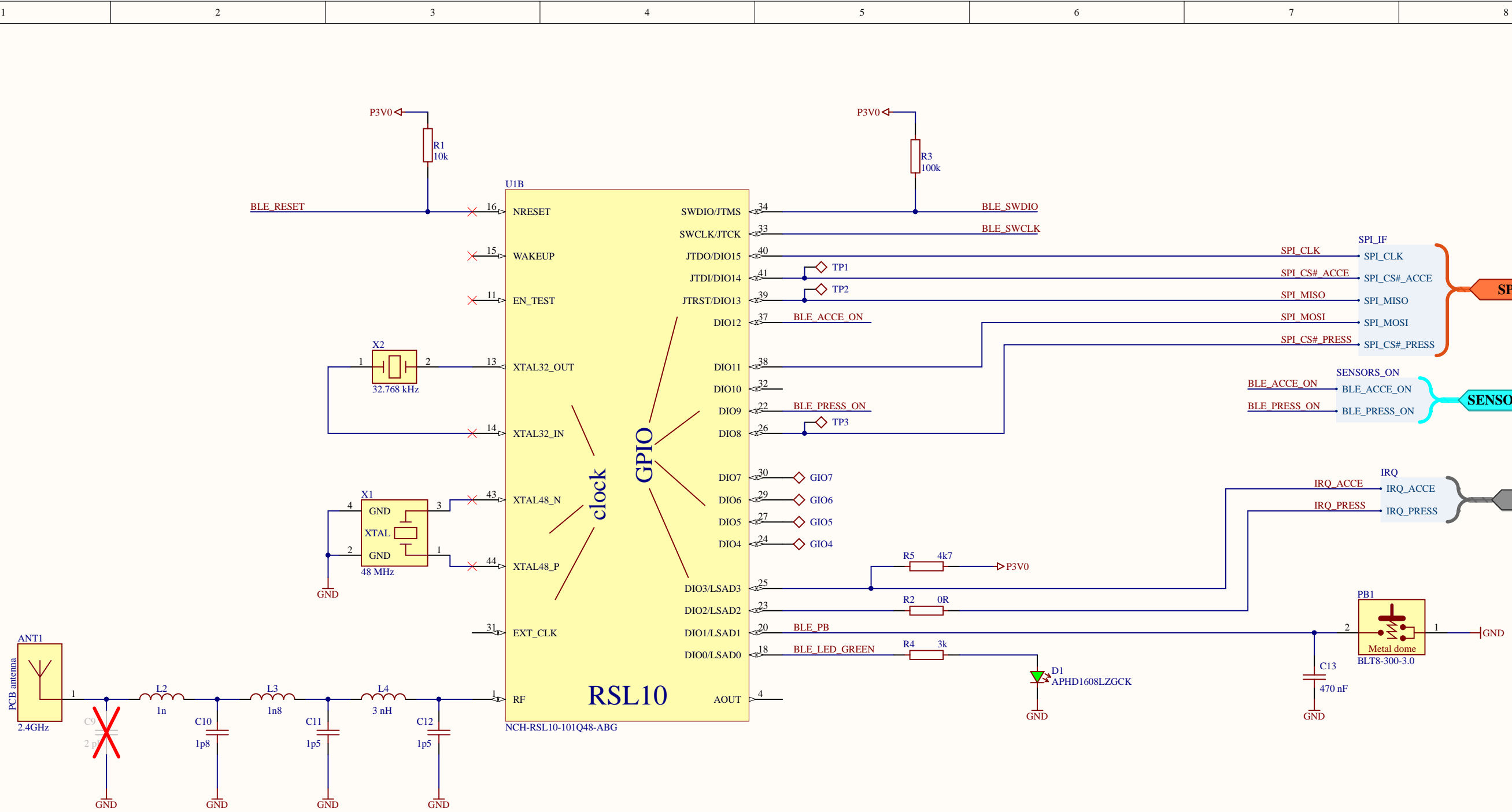
SECO-RSL10-TAG-GEVB		Assembly variant:	State:
Block diagram		standard_board	released
Revision: 0.2	Repository revision: Not in version control		ON Semiconductor Solution Engineering Center Piešťany
Engineer: Tomas Duris	26. Jan 2020 18:16		
File: Block_diagram.SchDoc	1/4		



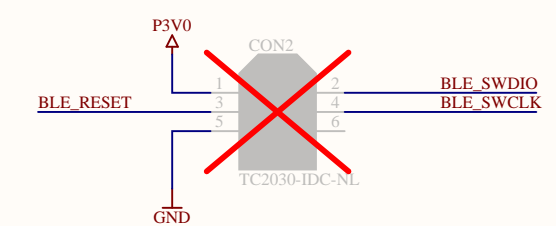
Note: There is no physical negative pad/pin connection to ground (GND) on the battery holder BH1. The BH1 is connected to GND via the metal dome push button PB1. Please see further details on page 3/4 and the PCB layout discussing the PB1 connection to circuitry.

SECO-RSL10-TAG-GEVB		Assembly variant:	State:
BLE power supply		standard_board	released
Revision: 0.2	Repository revision: Not in version control		ON Semiconductor Solution Engineering Center Piešťany
Engineer: Tomas Duris	26. Jan 2020 18:16		
File: BLE_power_management.SchDoc	2/4		





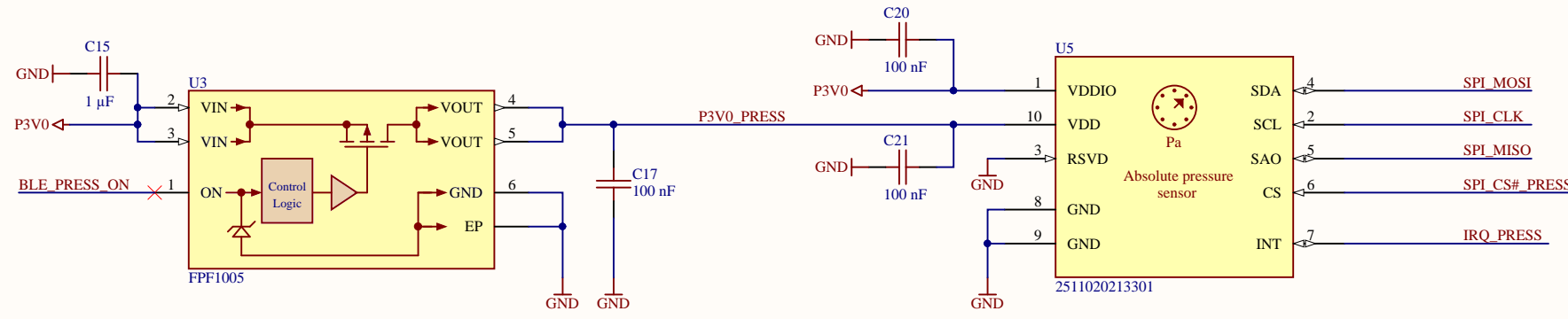
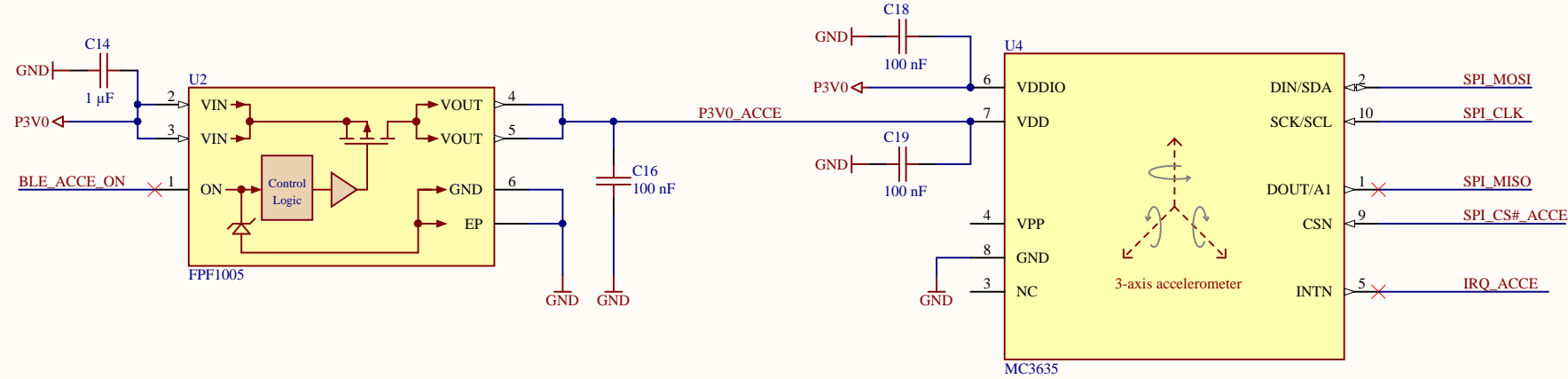
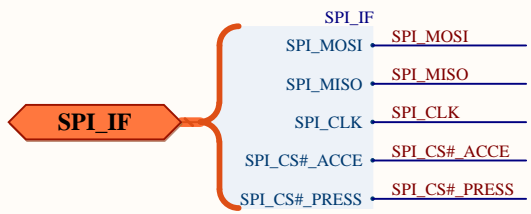
RSL10 SW debug



RSL10 SW fast programming

SECO-RSL10-TAG-GEVB		Assembly variant:	State:
BLE signals+SW debug		standard_board	released
Revision: 0.2	Repository revision: Not in version control	ON Semiconductor Solution Engineering Center Piešťany	
Engineer: Tomas Duris	26. Jan 2020 18:16		
File: BLE_signals.SchDoc	3/4		





SECO-RSL10-TAG-GEVB		Assembly variant:	State:
Sensors		standard_board	released
Revision: 0.2	Repository revision: Not in version control		ON Semiconductor Solution Engineering Center Piešťany
Engineer: Tomas Duris	26. Jan 2020 18:16		
File: Sensors.SchDoc	4/4		

