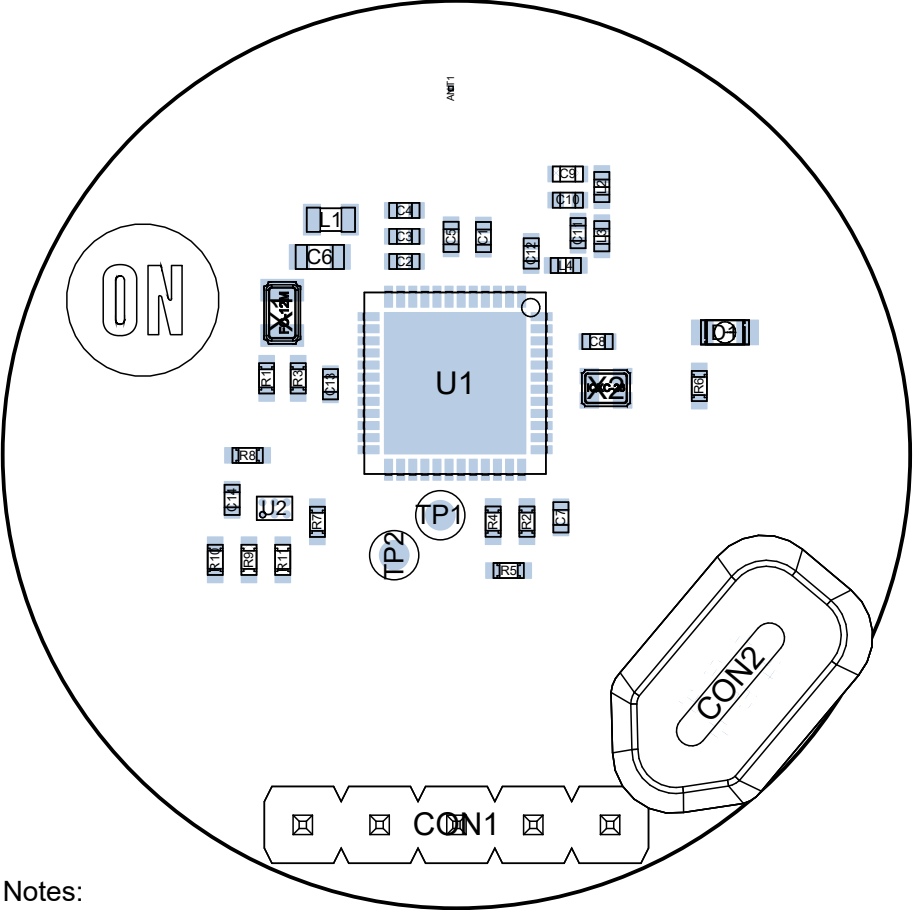


Bill Of Materials

Line #	Designator	Comment	Quantity
1	ANT1	2.4GHz	1
4	C1, C5, C7	1 μ F	3
5	C2	4 μ 7	1
6	C3, C14	100 nF	2
7	C4	100 pF	1
8	C6	4 μ 7	1
9	C8	2 μ 2	1
10	C10	1p8	1
11	C11, C12	1p5	2
12	C13	470 nF	1
13	D1	APHD1608LCGCK	1
14	L1	2 μ 2	1
15	L2	1n	1
16	L3	1n8	1
17	L4	3 nH	1
19	R1, R7, R8	10k	3
20	R2	100k	1
21	R3, R4, R5	68	3
22	R6	560	1
23	R10	0R	1
24	U1	NCH-RSL10-101Q48-ABG	1
25	U2	N34TS108C6EXT5G	1
26	X1	32.768 kHz 20ppm	1
27	X2	48 MHz	1

View from Top side (Scale =4:1)



Notes:

- 1

Only components present in this assembly variant are listed in BoM table on the left
- 2

Components not present in this assembly variant are crossed out by red line

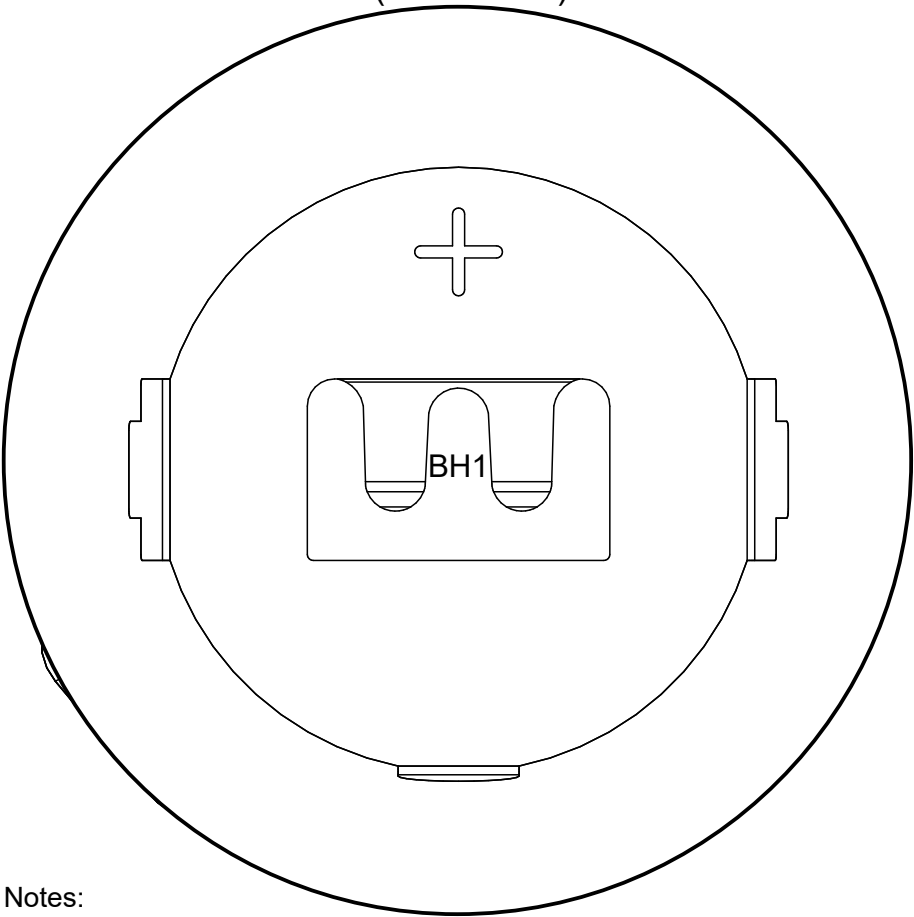
RSL10-COIN-GEVB		Revision: 0.1	State: released
Variant name: standard_board		Assembly document	Sheet 1 / 3
Engineer: Tomas Duris	Date: 16. Jan 2019		
PCB File: RSL10_COIN_GEV.B.PcbDoc		ON Semiconductor Solution Engineering Center Piestany	
Repository revision: No repository control			



Bill Of Materials


Line #	Designator	Comment	Quantity
3	BH1	CR2032 battery holder	1
18	PB1	BLT8-300-3.0	1

View from Bottom side (Scale =4:1)



Notes:

- 3 Only components present in this assembly variant are listed in BoM table on the left
- 4 Components not present in this assembly variant are crossed out by red line

RSL10-COIN-GEVB		Revision: 0.1	State: released
Variant name: standard_board		Assembly document	Sheet 2 / 3
Engineer: Tomas Duris	Date: 16. Jan 2019		
PCB File: RSL10_COIN_GEV.B.PcbDoc		ON Semiconductor Solution Engineering Center Piestany	
Repository revision: No repository control			



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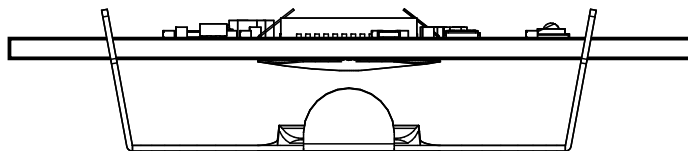
4

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A

A

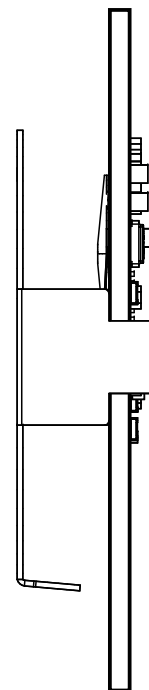
View from Top side (Scale = 3:1)



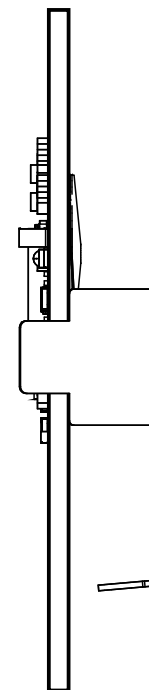
B

B

View from Left side (Scale = 3:1)



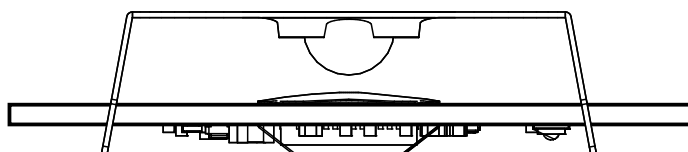
View from Right side (Scale = 3:1)



C

C

View from Back side (Scale = 3:1)



D

D

RSL10-COIN-GEVBRevision:
0.1State:
released

Variant name: standard_board

Assembly
documentSheet
3 / 3

PCB File: RSL10_COIN_GEBV.PcbDoc

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Repository revision: No repository control

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