



UP = No FET, Resistor in series with FB to VP  
 Down = FET with FB resistors to VP.  
 Physical board layout left= Down & right=UP

- Jumpers 1-8, Enable the customer to put their own LEDs between VP and OUTx.
- Jumper 9, Switches pull up in and out for DIAG.
- Jumper 10, Allows easy connection to one supply for Tail and STOP.
- Jumper 11, RSTOP programming node.
- Jumper 12, RTAIL programming node.
- Jumper 13, Boost Regulator connection node.
- Jumper 14, Loss of ground testing and NCV3065 testing.
- Jumper 15, in series with VP pin.

Title		
NCV7680_demoboard_new_pinout_rev_B		
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A	<Doc>	C
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