

Electrical Rules Check Report

Class	Document	Message
		Successful Compile for MULTI-SENSE-GEVB.PrjPcb

Design Rules Verification Report

Filename : C:\Users\Public\Documents\Altium\Projects\IoT_IDK_Multisensor_EVB_19x44_

Warnings 0
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=8mil) (All),(All)	0
Clearance Constraint (Gap=5mil) (InComponentClass('Clearance_Q3_Q4_Q5_Q6_Q8_Q9_Q10')),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint (All)	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=4mil) (Max=80mil) (Preferred=30mil) (All)	0
Power Plane Connect Rule(Direct Connect)(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4)	0
Hole Size Constraint (Min=1mil) (Max=130mil) (All)	0
Pads and Vias to follow the Drill pairs settings	0
Hole To Hole Clearance (Gap=10mil) (All),(All)	0
Minimum Solder Mask Sliver (Gap=1.3mil) (IsPad And HasFootprint('IC_ON_WQFN24_485BG')),(((Name Like '*-1') or	0
Minimum Solder Mask Sliver (Gap=2.9mil) (All),(All)	0
Silk To Solder Mask (Clearance=4mil) (IsPad),(All)	0
Silk to Silk (Clearance=8mil) (All),(All)	0
Net Antennae (Tolerance=0mil) (All)	0
Component Clearance Constraint (Horizontal Gap = 10mil, Vertical Gap = 10mil) (All),(All)	0
Height Constraint (Min=0mil) (Max=1000mil) (Preferred=500mil) (All)	0
Silk primitive without silk layer	0
Total	0