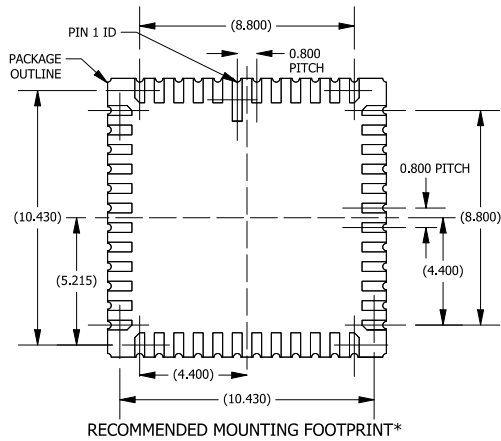
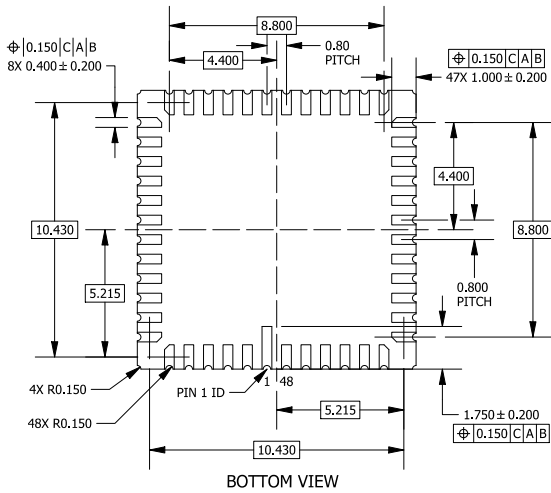
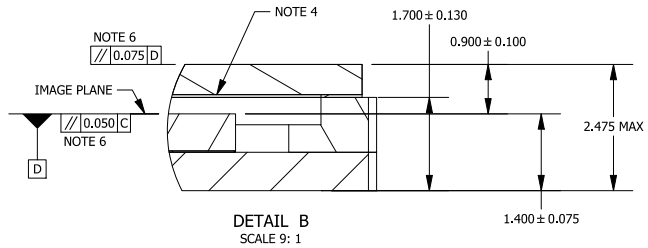
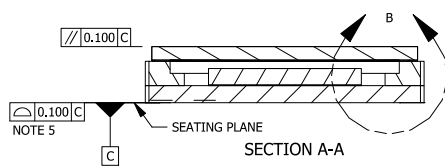
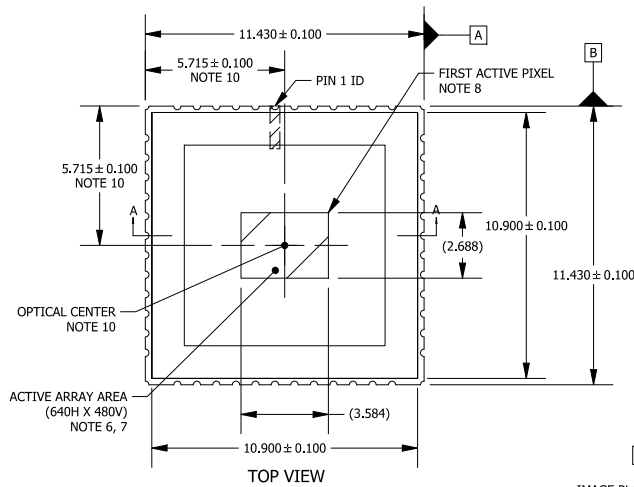




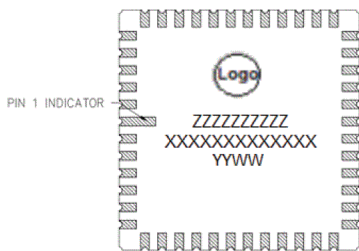
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DATE 25 MAR 2020



*FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

MARKING DIAGRAM*



ZZZ = Lot Traceability
XXXX = Specific Device Code
YY = Year
WW = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS [mm].
3. GLASS: 0.550 THICKNESS; REFRACTIVE INDEX = 1.52.
4. AIR GAP BETWEEN GLASS AND PIXEL ARRAY: 0.350 THICKNESS.
5. COPLANARITY APPLIES TO THE PLATED LAND PADS.
6. PARALLELISM APPLIES ONLY TO THE ACTIVE ARRAY.
7. MAXIMUM ROTATION OF ACTIVE ARRAY RELATIVE TO DATUMS A AND B IS ± 1°.
8. REFER TO THE DEVICE DATA SHEET FOR TOTAL PIXEL ARRAY DEFINITIONS.
9. PACKAGE CENTER (X, Y) = (0.000, 0.000).
10. OPTICAL CENTER RELATIVE TO PACKAGE CENTER (X, Y) = (0.000, 0.000).

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