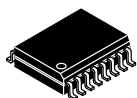


# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

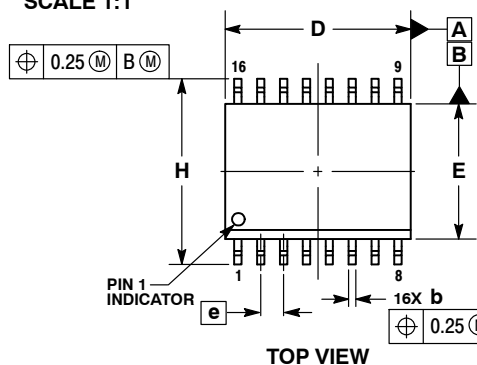
ON Semiconductor®



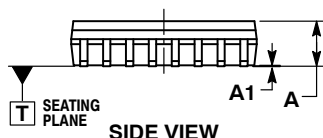
SCALE 1:1

## SOIC-16 WB, EP CASE 751DW ISSUE A

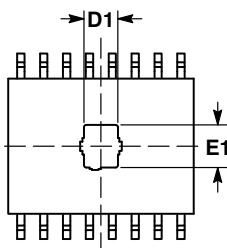
DATE 23 OCT 2015



TOP VIEW

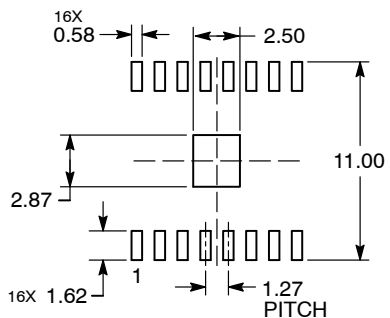


SIDE VIEW

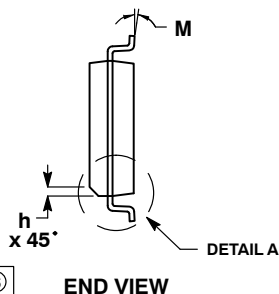


BOTTOM VIEW

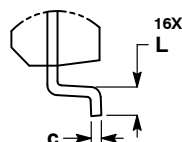
### RECOMMENDED SOLDERING FOOTPRINT



DIMENSIONS: MILLIMETERS



END VIEW



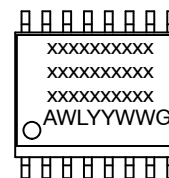
DETAIL A

#### NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. DAMBAR PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF b DIMENSION AT MAXIMUM MATERIAL CONDITION.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSIONS.
5. MAXIMUM MOLD PROTRUSION OR FLASH TO BE 0.15 PER SIDE.

DIM	MILLIMETERS	
	MIN	MAX
A	2.35	2.65
A1	0.00	0.10
b	0.35	0.49
c	0.25	0.32
D	10.15	10.45
D1	1.79	2.00
E	7.40	7.60
E1	2.27	2.47
e	1.27 BSC	
H	10.05	10.55
h	0.53 REF	
L	0.50	0.90
M	0°	7°

### GENERIC MARKING DIAGRAM\*



- xxx = Specific Device Code
- A = Assembly Location
- WL = Wafer Lot
- YY = Year
- WW = Work Week
- G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.

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