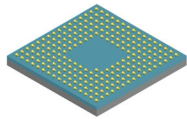


# MECHANICAL CASE OUTLINE

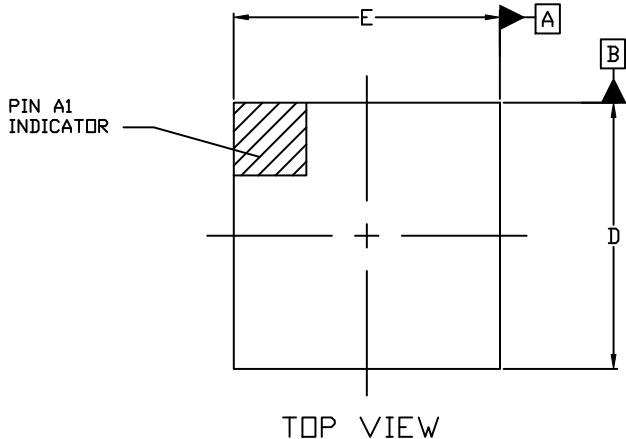
## PACKAGE DIMENSIONS

ON Semiconductor®



LFBGA221 11x11 / FBGA221J  
CASE 566DJ  
ISSUE A

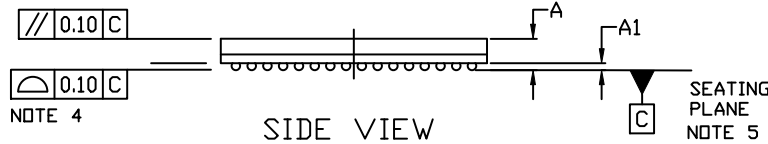
DATE 06 MAY 2020



NOTES:

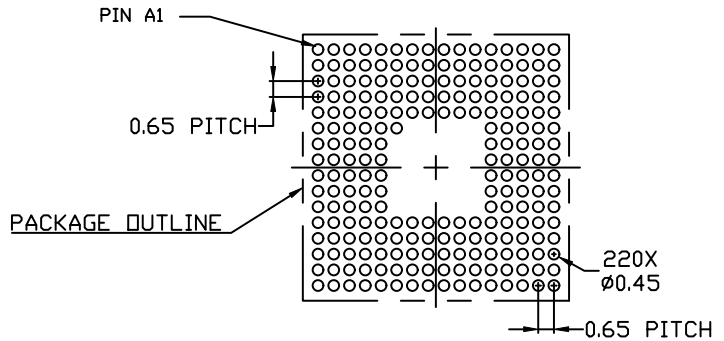
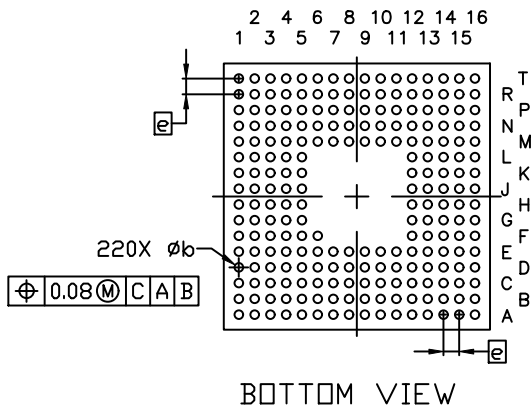
1. DIMENSIONING AND TOLERANCING PER. ASME Y14.5M, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION  $b$  IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER PARALLEL TO DATUM C.
4. COPLANARITY APPLIES TO THE SPHERICAL CROWNS OF THE SOLDER BALLS.
5. DATUM C, THE SEATING PLANE, IS DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	1.20	1.30	1.40
A1	0.22	0.27	0.32
b	0.30	0.35	0.40
D	10.90	11.00	11.10
E	10.90	11.00	11.10
e	0.65 BSC		



NOTE 4

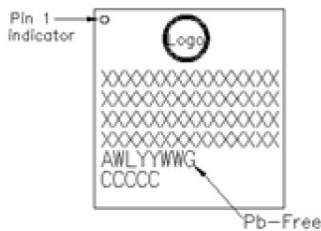
SEATING PLANE NOTE 5



RECOMMENDED MOUNTING FOOTPRINT\*

\* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SLDERRM/D.

GENERIC MARKING DIAGRAM\*



XXXX = Specific Device Code  
A = Assembly Location  
WL = Wafer Lot  
YY = Year  
WW = Work Week  
G = Pb-Free Package  
CCC = Country of Origin

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	LFBGA221 11X11 / FBGA221J	PAGE 1 OF 1

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