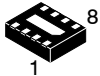


MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

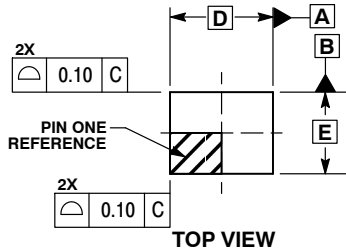
ON Semiconductor®



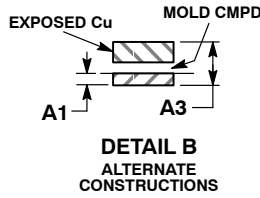
SCALE 4:1

UDFN8, 1.7x1.35, 0.4P
CASE 517BC-01
ISSUE O

DATE 17 NOV 2009



TOP VIEW

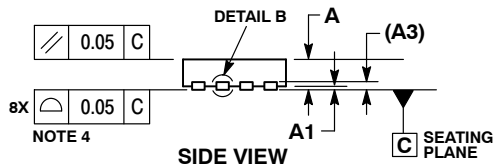


DETAIL B
ALTERNATE
CONSTRUCTIONS

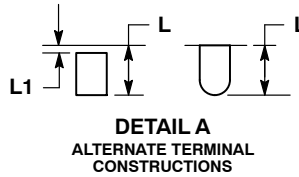
NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.25 mm FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

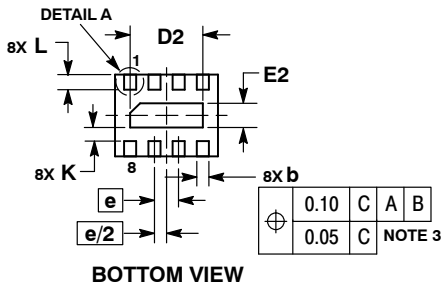
MILLIMETERS		
DIM	MIN	MAX
A	0.45	0.55
A1	0.00	0.05
A3	0.13	REF
b	0.15	0.25
D	1.70	BSC
D2	1.10	1.30
E	1.35	BSC
E2	0.30	0.50
e	0.40	BSC
K	0.15	---
L	0.20	0.30
L1	---	0.05



SIDE VIEW

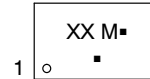


DETAIL A
ALTERNATE TERMINAL
CONSTRUCTIONS



BOTTOM VIEW

GENERIC MARKING DIAGRAM*

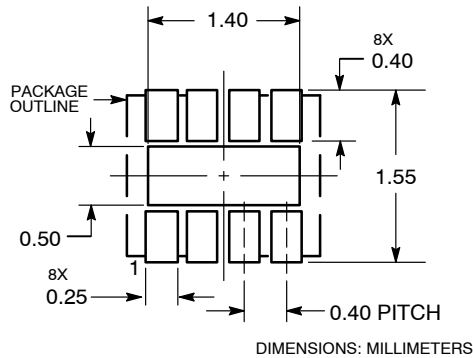


- XX = Specific Device Code
- M = Date Code
- = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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DESCRIPTION:	8 PIN UDFN, 1.7X1.35, 0.4P	PAGE 1 OF 1

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