

# MECHANICAL CASE OUTLINE

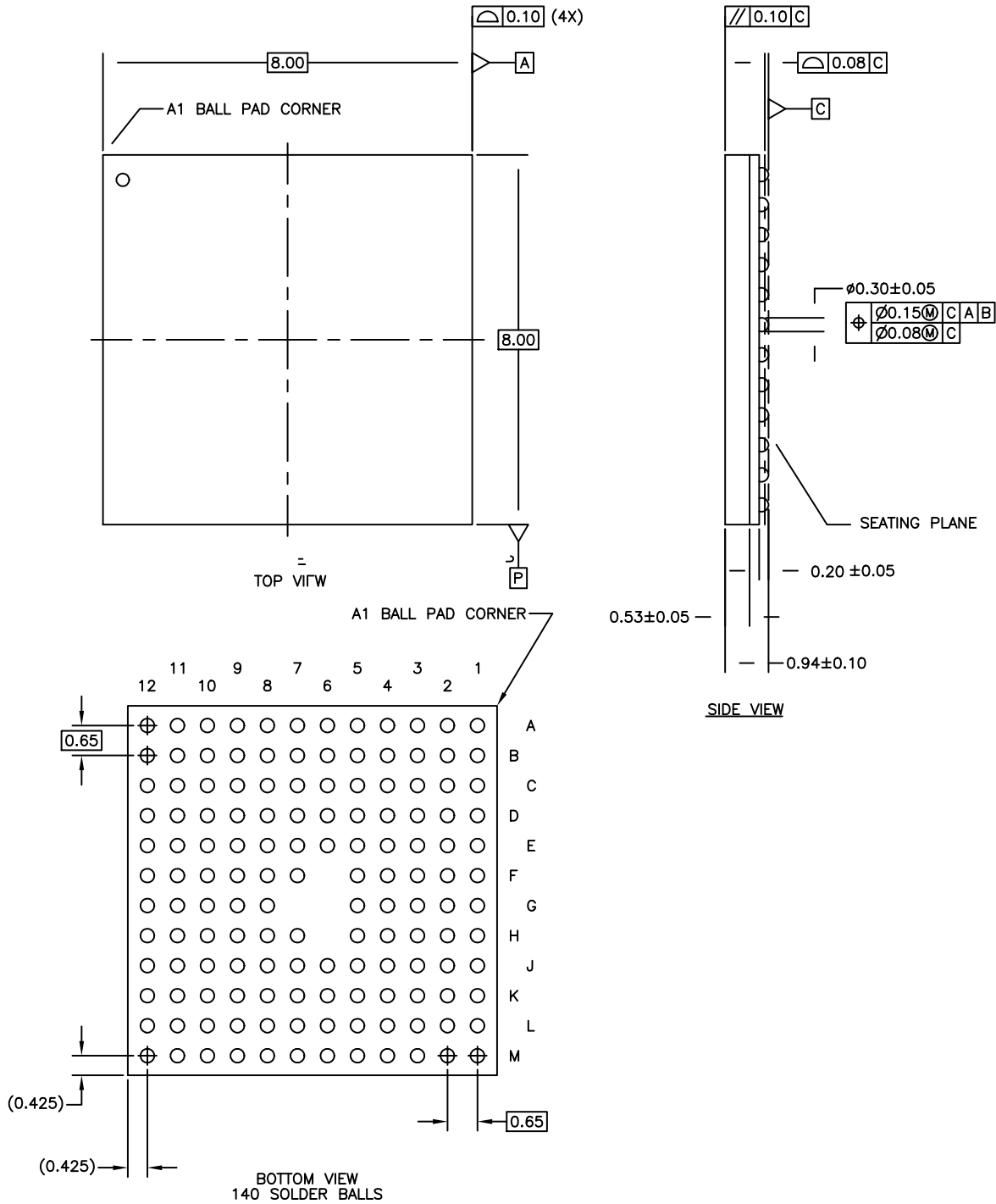
## PACKAGE DIMENSIONS

ON Semiconductor®



VFBGA 140 8x8  
CASE 138AE-01  
ISSUE O

DATE 29 JUN 2010



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<b>DESCRIPTION:</b>	<b>VFBGA 140, 8X8</b>	<b>PAGE 1 OF 2</b>

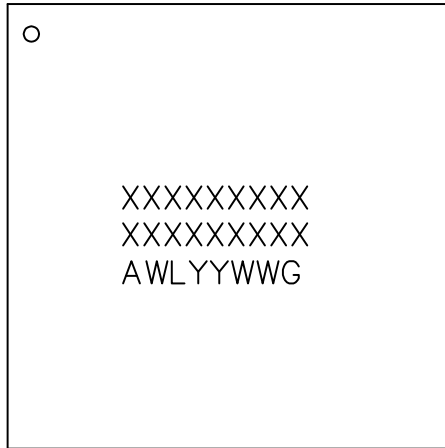
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W/B	BALL	W/B	BALL	W/B	BALL	W/B	BALL	W/B	BALL	W/B	BALL	W/B	BALL
1	B2	22	K1	43	L6	64	L11	85	F10	106	E8	127	A3
2	C2	23	J2	44	M7	65	L12	86	D12	107	B9	128	E3
3	B1	24	K2	45	K6	66	K10	87	E10	108	C8	129	C3
4	C1	25	G4	46	J5	67	K11	88	D10	109	D8	130	A2
5	D3	26	J3	47	J6	68	K12	89	E11	110	E7	131	B3
6	D2	27	H4	48	J7	69	J12	90	B12	111	A5, B5	132	A1
7	D1	28	K3	49	J8	70	J11	91	C12	112	A7, B7		
8	E1	29	L1	50	K7	71	H7	92	D11	113	D7		
9	E2	30	M1	51	L7	72	H12	93	C11	114	F7		
10	F1	31	L2	52	M8	73	H11	94	A12	115	C6, C7		
11	F2	32	M2	53	M9	74	H9	95	B11	116	E6		
12	E4	33	L3	54	L8	75	G12	96	C10	117	D6		
13	F4	34	M3	55	M10	76	H10	97	A11	118	A6, B6		
14	F3	35	K4	56	K8	77	G11	98	B10	119	A5, B5		
15	G1	36	J4	57	J9	78	H8	99	A10	120	E5		
16	G2	37	L4	58	K9	79	G9	100	A9	121	A4		
17	H1	38	L5	59	L9	80	G10	101	F9	122	C5		
18	J1	39	K5	60	J10	81	F12	102	E9	123	D5		
19	G3	40	M4	61	M11	82	E12	103	C9	124	D4		
20	H3	41	M5	62	L10	83	G8	104	D9	125	B4		
21	H2	42	M6	63	M12	84	F11	105	F8	126	C4		

GENERIC MARKING DIAGRAM



XXXXXXXXXX = Device Code  
A = Assembly Site  
WL = Wafer Lot  
YY = Year  
WW = Work Week  
G = Pb-Free Package

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