

Hyperfast Diode

75 A, 1200 V

RHRG75120

Description

The RHRG75120 is a hyperfast diode with soft recovery characteristics. It has the half recovery time of ultrafast diodes and is silicon nitride passivated ionimplanted epitaxial planar construction.

These devices are intended to be used as freewheeling / clamping diodes and diodes in a variety of switching power supplies and other power switching applications. Their low stored charge and hyperfast soft recovery minimize ringing and electrical noise in many power switching circuits reducing power loss in the switching transistors.

Features

- Hyperfast Recovery, $t_{rr} = 100$ ns (@ $I_F = 75$ A)
- Max Forward Voltage, $V_F = 3.2$ V (@ $T_C = 25^\circ\text{C}$)
- 1200 V Reverse Voltage and High Reliability
- Avalanche Energy Rated
- This Device is Pb-Free and is RoHS Compliant

Applications

- Switching Power Supplies
- Power Switching Circuits
- General Purpose

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

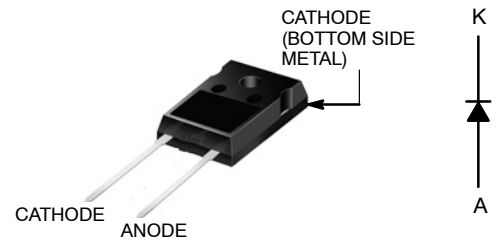
Symbol	Rating	Value	Unit
V_{RRM}	Peak Repetitive Reverse Voltage	1200	V
V_{RWM}	Working Peak Reverse Voltage	1200	V
V_R	DC Blocking Voltage	1200	V
$I_{F(AV)}$	Average Rectified Forward Current @ $T_C = 42^\circ\text{C}$	75	A
I_{FRM}	Repetitive Peak Surge Current (Square Wave, 20 kHz)	150	A
I_{FSM}	Non-Repetitive Peak Surge Current (Halfwave, 1 Phase, 60 Hz)	500	A
P_D	Maximum Power Dissipation	190	W
E_{AVL}	Avalanche Energy (See Figures 7 and 8)	50	mJ
T_J, T_{STG}	Operating and Storage Temperature	-65 to +175	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

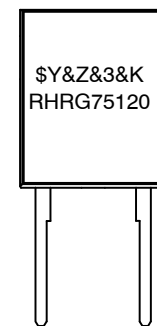
Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Maximum Thermal Resistance, Junction to Case	0.8	$^\circ\text{C}/\text{W}$

PIN ASSIGNMENT



TO-247-2LD
CASE 340CL

MARKING DIAGRAM



\$Y = onsemi Logo
 &Z = Assembly Plant Code
 &3 = Numeric Date Code
 &K = Lot Code
 RHRG75120 = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

RHRG75120

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V _F	Instantaneous Forward Voltage (Pulse Width = 300 μs, Duty Cycle = 2%)	I _F = 75 A I _F = 75 A, T _C = 150°C	– –	– –	3.2 2.6	V
I _R	Instantaneous Reverse Current	V _R = 1200 V V _R = 1200 V, T _C = 150°C	– –	– –	250 2	μA mA
T _{rr}	Reverse Recovery Time (See Figure 6), Summation of t _a + t _b	I _F = 1 A, dI _F /dt = 100 A/μs I _F = 75 A, dI _F /dt = 100 A/μs	– –	– –	85 100	ns
t _a	Time to Reach Peak Reverse Current (See Figure 6)	I _F = 75 A, dI _F /dt = 100 A/μs	–	60	–	ns
t _b	Time from Peak I _{RM} to Projected Zero Crossing of I _{RM} Based on a Straight Line from Peak I _{RM} through 25% of I _{RM} (See Figure 6)	I _F = 75 A, dI _F /dt = 100 A/μs	–	25	–	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

PACKAGE MARKING AND ORDERING INFORMATION

Device	Device Marking	Package	Shipping
RHRG75120	RHRG75120	TO-247-2L	450 / Tube

TYPICAL PERFORMANCE CURVES

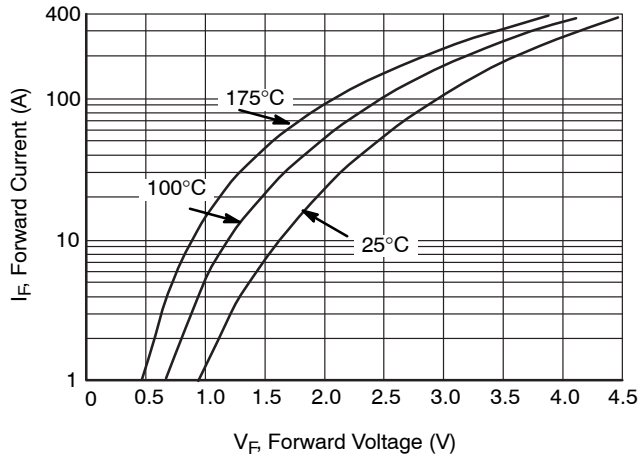


Figure 1. Forward Current vs. Forward Voltage

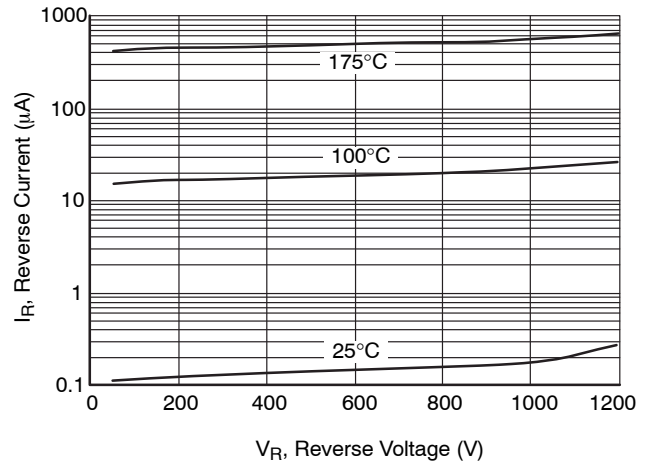


Figure 2. Reverse Current vs. Reverse Voltage

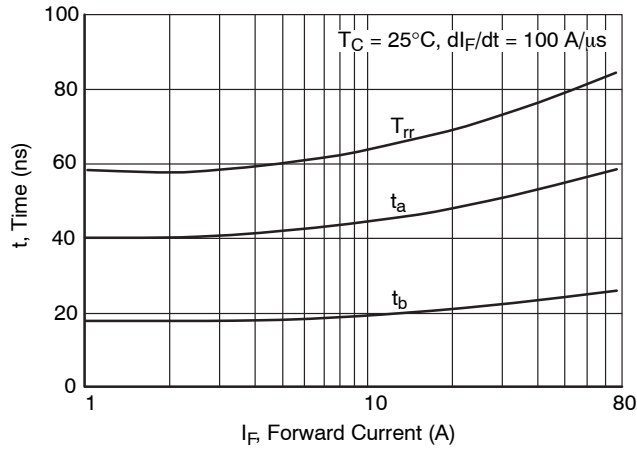


Figure 3. T_{rr} , t_a and t_b Curves vs. Forward Current

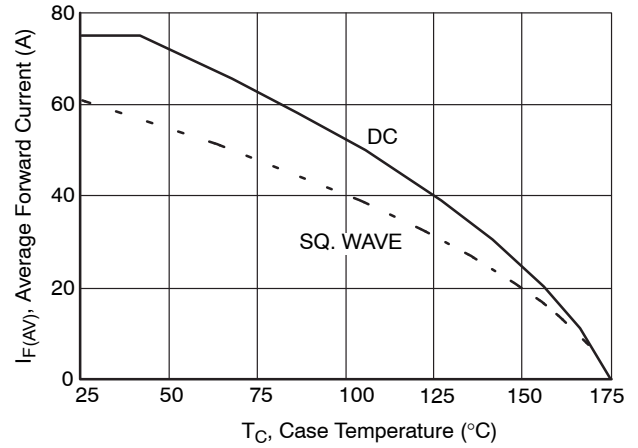


Figure 4. Current Derating Curve

TEST CIRCUITS AND WAVEFORMS

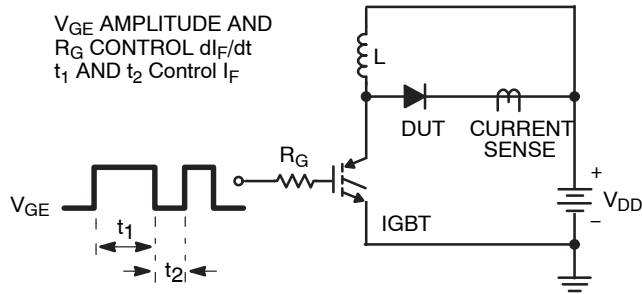
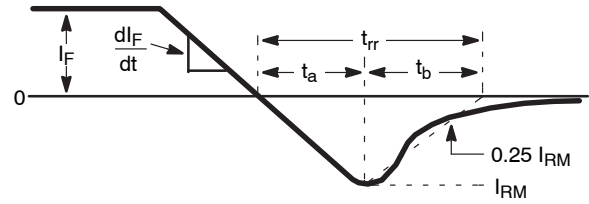
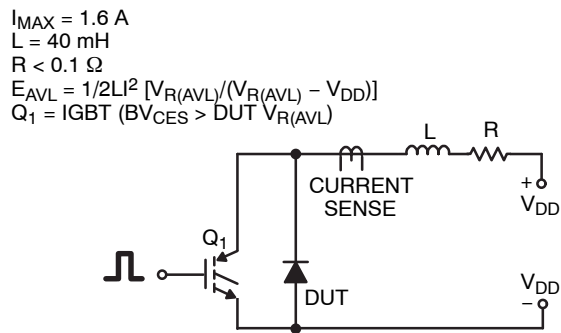
Figure 5. T_{rr} Test CircuitFigure 6. T_{rr} Waveforms and Definitions

Figure 7. Avalanche Energy Test Circuit

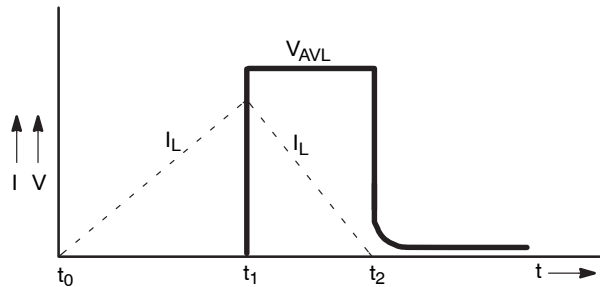
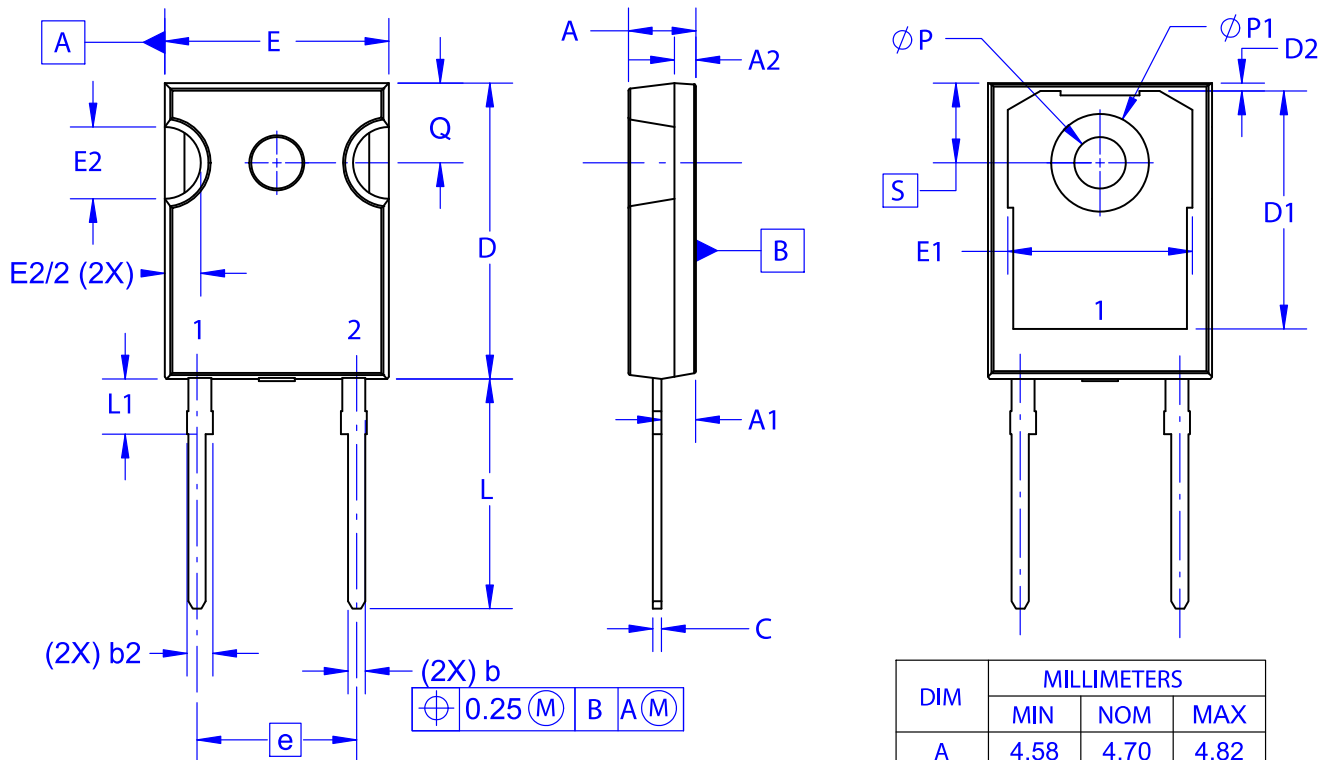


Figure 8. Avalanche Current and Voltage Waveforms

TO-247-2LD
CASE 340CL
ISSUE A

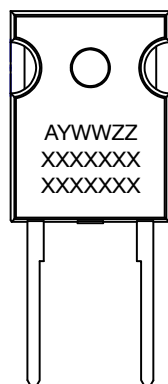
DATE 03 DEC 2019



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- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC
MARKING DIAGRAM*



XXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
ZZ = Assembly Lot Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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