# NUP4102XV6

## **6-Pin Bi-Directional Quad TVS Array**

This 6–Pin bi–directional transient suppressor array is designed for applications requiring transient overvoltage protection capability. It is intended for use in transient voltage and ESD sensitive equipment such as computers, printers, cell phones, medical equipment, and other applications. Its integrated design provides bi–directional protection for four separate lines using a single SOT–563 package. This device is ideal for situations where board space is a premium.

#### Features

- Bi-directional Protection for Four Lines in a Single SOT-563 Package
- Peak Power Dissipation 75 W (8x20 µsec Waveform)
- Low Leakage Current (100 nA @ 12 V)
- Low Capacitance (< 15 pF)
- Provides ESD Protection for JEDEC Standards JESD22 – Machine Model = Class C
  - Human Body Model = Class 3B
- Provides ESD Protection for IEC 61000–4–2, 15 kV (Air), 8 kV (Contact)

#### **Mechanical Characteristics**

- Void Free, Transfer–Molded, Thermosetting Plastic Case
- Corrosion Resistant Finish, Easily Solderable
- Package Designed for Optimal Automated Board Assembly
- Small Package Size for High Density Applications

#### Applications

- GSM Handsets and Accessories
- Other Telephone Sets
- Computers / Printers / Set-Top Boxes

#### MAXIMUM RATINGS (T<sub>J</sub>=25°C, unless otherwise specified)

Rating	Symbol	Value	Unit
Peak Power Dissipation 8x20 μsec Double Exponential Waveform, (Note 1)	P <sub>PK</sub>	75	W
Operating Junction Temperature Range	ТJ	-40 to 125	°C
Storage Temperature Range	T <sub>STG</sub>	–55 to 150	°C
Lead Solder Temperature – Maximum (10 sec)	TL	260	°C
Human Body Model (HBM) Machine Model (MM) IEC 61000–4–2 Air (ESD) IEC 61000–4–2 Contact (ESD)	ESD	16 0.4 30 30	kV

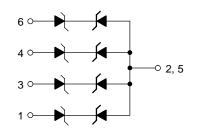
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. Non-repetitive current pulse per Figure 3.



### **ON Semiconductor®**

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SOT-563 CASE 463A PLASTIC

#### MARKING DIAGRAM



- RP = Device Marking
- M = One Digit Date Code
- = Pb–Free Package
- (Note: Microdot may be in either location)

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NUP4102XV6T1G	SOT-563 (Pb-Free)	4000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

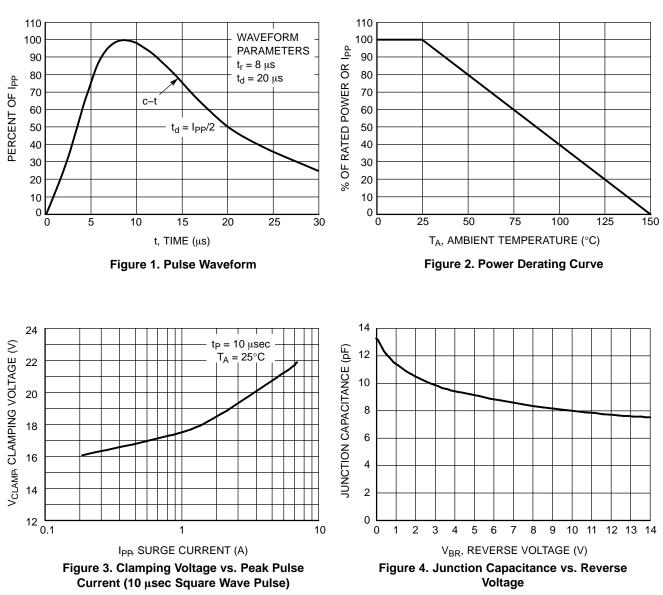
## NUP4102XV6

#### ELECTRICAL CHARACTERISTICS (TJ=25°C, unless otherwise specified)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Reverse Working Voltage	(Note 2)	V <sub>RWM</sub>			12	V
Breakdown Voltage	I <sub>T</sub> = 1 mA, (Note 3)	V <sub>BR</sub>	13.6		17.8	V
Reverse Leakage Current	V <sub>RWM</sub> = 12 V	I <sub>R</sub>		10	100	nA
Clamping Voltage	I <sub>PP</sub> = 3 A, (8x20 μsec Waveform)	V <sub>C</sub>			25	V
Maximum Peak Pulse Current	8x20 µsec waveform	I <sub>PP</sub>			3.0	А
Capacitance	V <sub>R</sub> = 0 V, f=1 MHz (Line to GND)	Cj		13	15	pF

TVS devices are normally selected according to the working peak reverse voltage (V<sub>RWM</sub>), which should be equal or greater than the DC or continuous peak operating voltage level.

3.  $V_{BR}$  is measured at pulse test current I<sub>T</sub>; Pulse Width 1 ms.



#### **TYPICAL PERFORMANCE CURVES**

 $(T_J = 25^{\circ}C \text{ unless otherwise specified})$ 

#### **MECHANICAL CASE OUTLINE** PACKAGE DIMENSIONS



# **ONSEM**

SOT-563-6 1.60x1.20x0.55, 0.50P CASE 463A								
			ISSUE J				-	
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	DA	А —		. Г		M	ILLIMETE	RS
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DESCRIPTION:	SOT-563-6 1.	60x1.20x0.55,	0.50P					PAGE 1 OF 1

SOT-563-6 1.60x1.20x0.55, 0.50P

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