# MOSFET – Power, Single, N-Channel, SO-8FL 25 V, 334 A

#### **Features**

- Integrated Schottky Diode
- Optimized Design to Minimize Conduction and Switching Losses
- Optimized Package to Minimize Parasitic Inductances
- Optimized material for improved thermal performance
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

## **Applications**

- High Performance DC-DC Converters
- System Voltage Rails
- Netcom, Telecom
- Servers & Point of Load

## MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise stated)

Parameter	Symbol	Value	Units
Drain-to-Source Voltage	$V_{DSS}$	25	V
Gate-to-Source Voltage	$V_{GS}$	±20	V
Continuous Drain Current $R_{\theta JA}$ ( $T_A$ = 25°C, Note 1)	Ι <sub>D</sub>	54	Α
Power Dissipation $R_{\theta JA}$ ( $T_A = 25^{\circ}C$ , Note 1)	P <sub>D</sub>	3.2	W
Continuous Drain Current $R_{\theta JC}$ ( $T_C = 25^{\circ}C$ , Note 1)	Ι <sub>D</sub>	334	Α
Power Dissipation $R_{\theta JC}$ ( $T_C = 25^{\circ}C$ , Note 1)	P <sub>D</sub>	125	W
Pulsed Drain Current (t <sub>p</sub> = 10 μs)	I <sub>DM</sub>	568	Α
Single Pulse Drain-to-Source Avalanche Energy (Note 1) ( $I_L$ = 57 $A_{pk}$ , $L$ = 0.3 mH)	E <sub>AS</sub>	487	mJ
Drain to Source dV/dt	dV/dt	7	V/ns
Maximum Junction Temperature	T <sub>J(max)</sub>	150	°C
Storage Temperature Range	T <sub>STG</sub>	–55 to 150	°C
Lead Temperature Soldering Reflow (SMD Styles Only), Pb-Free Versions (Note 2)	T <sub>SLD</sub>	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- Values based on copper area of 645 mm<sup>2</sup> (or 1 in<sup>2</sup>) of 2 oz copper thickness and FR4 PCB substrate.
- For more information, please refer to our Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.
- 3. This is the absolute maximum rating. Parts are 100% UIS tested at  $T_J$  = 25°C,  $V_{GS}$  = 10 V,  $I_L$  = 37 A,  $E_{AS}$  = 205 mJ.



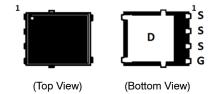
## ON Semiconductor®

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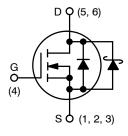
V <sub>GS</sub>	MAX R <sub>DS(on)</sub>	TYP Q <sub>GTOT</sub>
4.5 V	1.0 m $\Omega$	37.8 nC
10 V	0.7 m $\Omega$	82 nC

#### **PIN CONNECTIONS**

SO8-FL (5 x 6 mm)



## **N-CHANNEL MOSFET**



## **ORDERING INFORMATION**

See detailed ordering, marking and shipping information on page 7 of this data sheet.

## **THERMALCHARACTERISTICS**

Parameter	Symbol	Max	Units
Thermal Resistance, Junction-to-Ambient (Note 1 and 4) Junction-to-Case (Note 1 and 4)	$R_{ heta JA} \ R_{ heta JC}$	38.9 1.0	°C/W

<sup>4.</sup> Thermal Resistance  $R_{\theta JA}$  and  $R_{\theta JC}$  as defined in JESD51-3.

## **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA		25			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /	I <sub>D</sub> = 10 mA reference to 25°C			16		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 20 V	T <sub>J</sub> = 25°C			500	μΑ
Gate-to-Source Leakage Current	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS}$	= +20 V			+100	nA
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}, I_D = 250 \mu A$		1.2		2.1	V
Negative Threshold Temperature Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>	I <sub>D</sub> = 10 mA referer	ice to 25°C		3.7		mV/°C
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 30 A		0.56	0.7	
		V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 30 A		0.79	1	mΩ
Forward Transconductance	9FS	V <sub>DS</sub> = 12 V, I <sub>D</sub>	= 20 A		101		S
CHARGES, CAPACITANCES & GATE RESIS	STANCE						
Input Capacitance	C <sub>ISS</sub>				5538		pF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V, f = 1 MHz	z, V <sub>DS</sub> = 12 V		3416		
Reverse Transfer Capacitance	C <sub>RSS</sub>	1 3			175.3		1
Total Gate Charge	Q <sub>G(TOT)</sub>				37.8		
Threshold Gate Charge	Q <sub>G(TH)</sub>				2.3		1
Gate-to-Source Charge	Q <sub>GS</sub>	$V_{GS} = 4.5 \text{ V}, V_{DS} = 12 \text{ V}; I_D = 30 \text{ A}$			11.8		nC
Gate-to-Drain Charge	$Q_{GD}$				8		1
Total Gate Charge	Q <sub>G(TOT)</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 12 V; I <sub>D</sub> = 30 A			82		nC
Gate Resistance	$R_{G}$	T <sub>A</sub> = 25°0	C		1.3	2	Ω
SWITCHING CHARACTERISTICS, V <sub>GS</sub> = 4.5	<b>V</b> (Note 5)				•		•
Turn-On Delay Time	t <sub>d(ON)</sub>				16.9		
Rise Time	t <sub>r</sub>	$V_{GS} = 4.5 \text{ V}, V_{DD} = 12 \text{ V}, I_{D} = 15 \text{ A},$ $R_{G} = 3.0 \Omega$			42.3		1
Turn-Off Delay Time	t <sub>d(OFF)</sub>				46.3		ns
Fall Time	t <sub>f</sub>				30.9		┥ !
SWITCHING CHARACTERISTICS, V <sub>GS</sub> = 10	V (Note 5)						
Turn-On Delay Time	t <sub>d(ON)</sub>				10.9		
Rise Time	t <sub>r</sub>	Voc = 11.5 V Vo	n = 12 V		33.2		1
Turn-Off Delay Time	t <sub>d(OFF)</sub>	$V_{GS}$ = 11.5 V, $V_{DD}$ = 12 V, $I_{D}$ = 15 A, $R_{G}$ = 3.0 $\Omega$			58.3		ns
Fall Time	t <sub>f</sub>				23.3		
DRAIN-SOURCE DIODE CHARACTERISTIC	s				•		
Forward Diode Voltage	$V_{SD}$	V <sub>GS</sub> = 0 V,	T <sub>J</sub> = 25°C		0.35	0.6	T
		$I_S = 2.0 \text{ A}$	T <sub>J</sub> = 125°C		0.27		V
Reverse Recovery Time	t <sub>RR</sub>	$V_{GS} = 0 \text{ V, dIS/dt} = 100 \text{ A/}\mu\text{s,}$ $I_{S} = 30 \text{ A}$			66.7		1
Charge Time	t <sub>a</sub>				33.1		ns
Discharge Time	t <sub>b</sub>				33.6		1
Reverse Recovery Charge	Q <sub>RR</sub>				90		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

5. Pulse Test: pulse width  $\leq 300~\mu s$ , duty cycle  $\leq 2\%$ .

6. Switching characteristics are independent of operating junction temperatures.

## **TYPICAL CHARACTERISTICS**

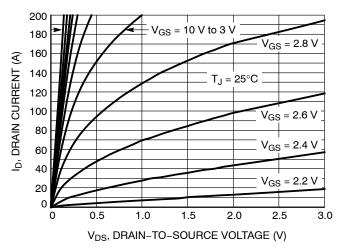


Figure 1. On-Region Characteristics

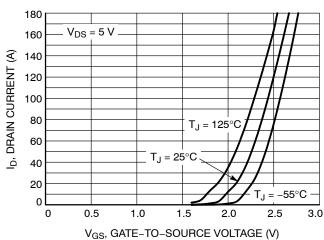


Figure 2. Transfer Characteristics

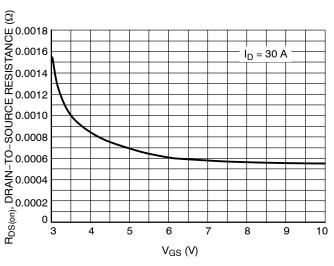


Figure 3. On-Resistance vs. V<sub>GS</sub>

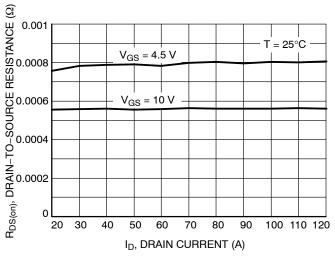


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

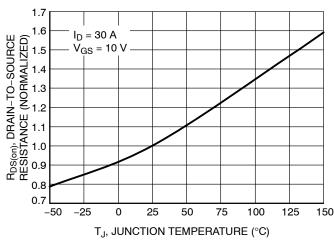


Figure 5. On–Resistance Variation with Temperature

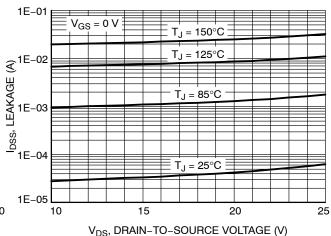


Figure 6. Drain-to-Source Leakage Current vs. Voltage

## TYPICAL CHARACTERISTICS

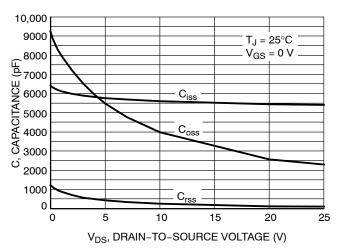


Figure 7. Capacitance Variation

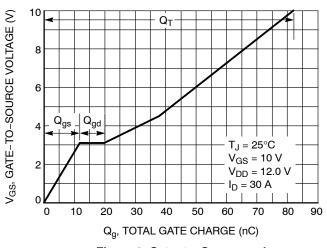


Figure 8. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

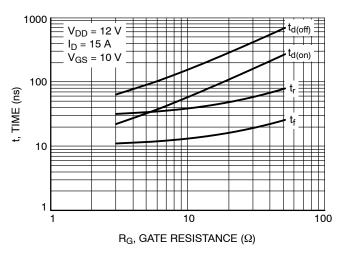


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

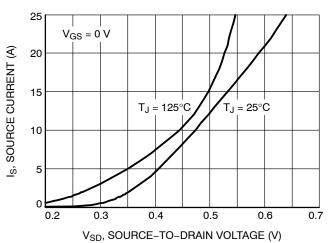


Figure 10. Diode Forward Voltage vs. Current

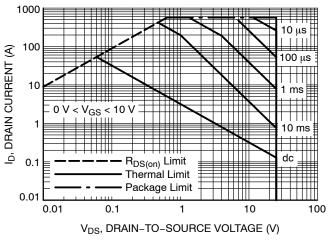


Figure 11. Maximum Rated Forward Biased Safe Operating Area

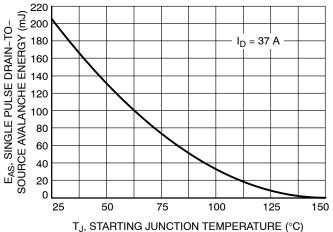


Figure 12. Maximum Avalanche Energy vs. Starting Junction Temperature

## **TYPICAL CHARACTERISTICS**

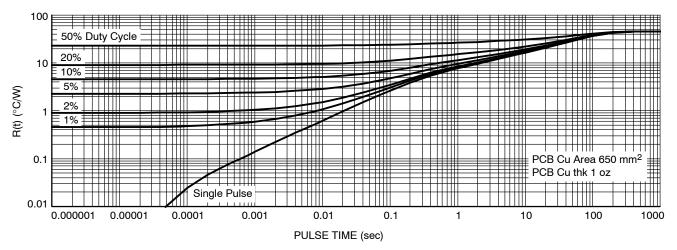


Figure 13. Thermal Characteristics

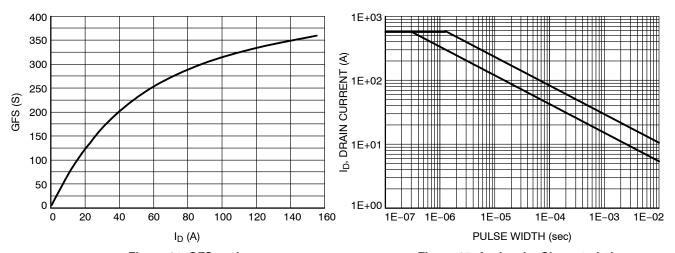


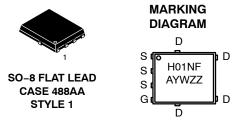
Figure 14. GFS vs. I<sub>D</sub>

Figure 15. Avalanche Characteristics

## **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NTMFS4H01NFT1G	SO8-FL (Pb-Free)	1500 / Tape & Reel
NTMFS4H01NFT3G	SO8-FL (Pb-Free)	5000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



A = Assembly Location

Y = Year

W = Work Week
ZZ = Lot Traceability

2 X





DFN5 5x6, 1.27P (SO-8FL) CASE 488AA ISSUE N

**DATE 25 JUN 2018** 

#### NOTES:

- DIMENSIONING AND TOLERANCING PER
- ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETER. DIMENSION D1 AND E1 DO NOT INCLUDE
- MOLD FLASH PROTRUSIONS OR GATE BURRS

	MILLIMETERS			
DIM	MIN	NOM	MAX	
Α	0.90	1.00	1.10	
A1	0.00		0.05	
b	0.33	0.41	0.51	
С	0.23	0.28	0.33	
D	5.00	5.15	5.30	
D1	4.70	4.90	5.10	
D2	3.80	4.00	4.20	
E	6.00	6.15	6.30	
E1	5.70	5.90	6.10	
E2	3.45	3.65	3.85	
е	1.27 BSC			
G	0.51	0.575	0.71	
K	1.20	1.35	1.50	
L	0.51	0.575	0.71	
L1	0.125 REF			
М	3.00	3.40	3.80	
θ	0 °		12 °	

## **GENERIC MARKING DIAGRAM\***



XXXXXX = Specific Device Code

= Lot Traceability

= Assembly Location Α

Υ = Year W = Work Week

ZZ

3.200

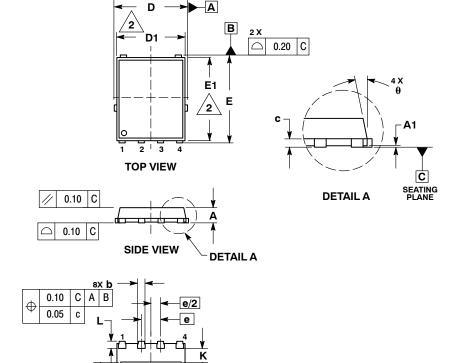
1.330

1.270 **PITCH** 

**DIMENSIONS: MILLIMETERS** 

4.530

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ", may or may not be present. Some products may not follow the Generic Marking.



2X

0.495

2X

0.475

2X 0.905

**A** 

0.965

1.000

4X 0.750 →

0.20 C

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

RECOMMENDED

**SOLDERING FOOTPRINT\*** 

2X

1.530

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**E2** 

D2

**BOTTOM VIEW** 

STYLE 2:

PIN 1. ANODE 2. ANODE 3. ANODE 4. NO CONNECT

5. CATHODE

G

PIN 5

(EXPOSED PAD)

STYLE 1:

PIN 1. SOURCE 2. SOURCE 3. SOURCE

4. GATE

5. DRAIN

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