# **IGBT**

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop (FS) Trench construction, and provides superior performance in demanding switching applications, offering both low on-state voltage and minimal switching loss. The IGBT is well suited for resonant or soft switching applications. Incorporated into the device is a rugged co-packaged free wheeling diode with a low forward voltage.

#### **Features**

- Low Saturation Voltage using Trench with Field Stop Technology
- Low Switching Loss Reduces System Power Dissipation
- Optimized for Low Case Temperature in IH Cooker Application
- Low Gate Charge
- These are Pb-Free Devices

#### **Typical Applications**

- Inductive Heating
- Consumer Appliances
- Soft Switching

#### **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-emitter voltage	$V_{CES}$	1200	V
Collector current @ Tc = 25°C @ Tc = 100°C	I <sub>C</sub>	60 30	Α
Pulsed collector current, T <sub>pulse</sub> limited by T <sub>Jmax</sub>	I <sub>CM</sub>	200	Α
Diode forward current @ Tc = 25°C @ Tc = 100°C	I <sub>F</sub>	60 30	Α
Diode pulsed current, T <sub>pulse</sub> limited by T <sub>Jmax</sub>	I <sub>FM</sub>	200	Α
Gate-emitter voltage	$V_{GE}$	±20	V
Power Dissipation @ Tc = 25°C @ Tc = 100°C	P <sub>D</sub>	192 77	W
Operating junction temperature range	TJ	-55 to +150	°C
Storage temperature range	T <sub>stg</sub>	-55 to +150	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T <sub>SLD</sub>	260	°C

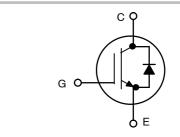
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

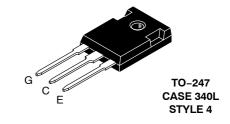


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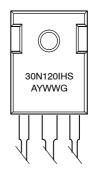
http://onsemi.com

30 A, 1200 V V<sub>CEsat</sub> = 2.00 V E<sub>off</sub> = 1.0 mJ





#### **MARKING DIAGRAM**



A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

#### **ORDERING INFORMATION**

Device	Package	Shipping
NGTB30N120IHSWG	TO-247 (Pb-Free)	30 Units / Rail

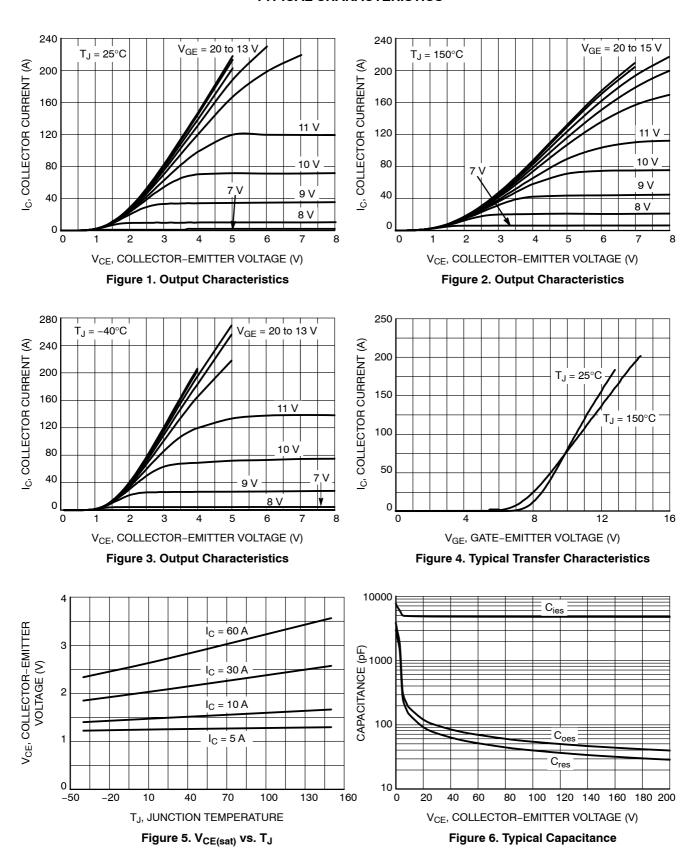
#### THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{ hetaJC}$	0.65	°C/W
Thermal resistance junction-to-case, for Diode	$R_{ hetaJC}$	2.0	°C/W
Thermal resistance junction-to-ambient	$R_{ hetaJA}$	40	°C/W

# **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC						
Collector-emitter breakdown voltage, gate-emitter short-circuited	$V_{GE} = 0 \text{ V, I}_{C} = 500 \mu\text{A}$	V <sub>(BR)CES</sub>	1200	_	-	V
Collector-emitter saturation voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 30 A V <sub>GE</sub> = 15 V, I <sub>C</sub> = 30 A, T <sub>J</sub> = 150°C	V <sub>CEsat</sub>	-	2.0 2.6	2.4	٧
Gate-emitter threshold voltage	$V_{GE} = V_{CE}, I_{C} = 250 \mu A$	V <sub>GE(th)</sub>	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V, T <sub>J =</sub> 150°C	I <sub>CES</sub>	-	_ _	0.5 2.0	mA
Gate leakage current, collector-emitter short-circuited	V <sub>GE</sub> = 20 V, V <sub>CE</sub> = 0 V	I <sub>GES</sub>	-	_	100	nA
DYNAMIC CHARACTERISTIC						
Input capacitance		C <sub>ies</sub>	-	5300	-	pF
Output capacitance	$V_{CE} = 20 \text{ V}, V_{GE} = 0 \text{ V}, f = 1 \text{ MHz}$	C <sub>oes</sub>	-	125	-	
Reverse transfer capacitance	]	C <sub>res</sub>	_	95	-	
Gate charge total		$Q_g$	-	220	-	nC
Gate to emitter charge	V <sub>CE</sub> = 600 V, I <sub>C</sub> = 30 A, V <sub>GE</sub> = 15 V	$Q_{ge}$	_	42	-	
Gate to collector charge		$Q_{gc}$	_	95	-	
SWITCHING CHARACTERISTIC, INDUCT	TIVE LOAD					
Turn-off delay time	T <sub>J</sub> = 25°C	t <sub>d(off)</sub>	-	210	-	ns
Fall time	$V_{CC} = 600 \text{ V}, I_{C} = 30 \text{ A}$ $R_{a} = 10 \Omega$	t <sub>f</sub>	_	140	-	
Turn-off switching loss	V <sub>GE</sub> = 0 V/ 15V	E <sub>off</sub>	_	1.0	-	mJ
Turn-off delay time	T <sub>J</sub> = 125°C	t <sub>d(off)</sub>	_	215	-	ns
Fall time	$V_{CC} = 600 \text{ V}, I_{C} = 30 \text{ A}$ $R_{g} = 10 \Omega$	t <sub>f</sub>	_	175	-	
Turn-off switching loss	V <sub>GE</sub> = 0 V/ 15V	E <sub>off</sub>	-	1.8	-	mJ
DIODE CHARACTERISTIC					_	_
Forward voltage	V <sub>GE</sub> = 0 V, I <sub>F</sub> = 30 A V <sub>GE</sub> = 0 V, I <sub>F</sub> = 30 A, T <sub>J</sub> = 150°C	$V_{F}$	- -	1.8 2.0	2.0	V

#### **TYPICAL CHARACTERISTICS**



#### **TYPICAL CHARACTERISTICS**

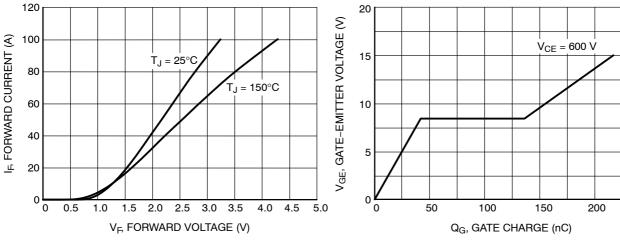


Figure 7. Diode Forward Characteristics

Figure 8. Typical Gate Charge

250

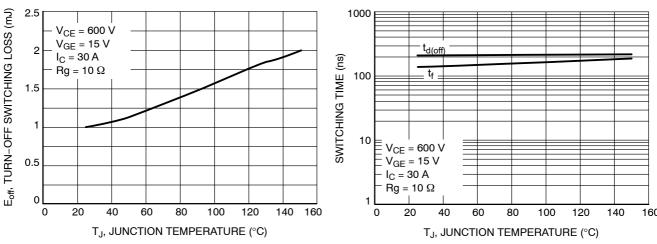


Figure 9. Energy Loss vs. Temperature

Figure 10. Switching Time vs. Temperature

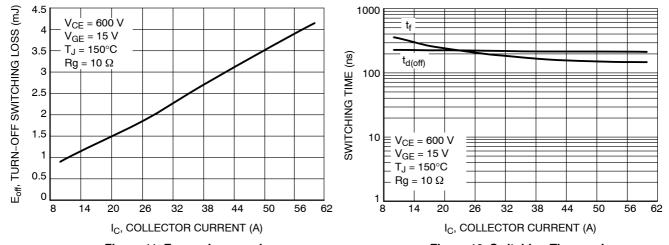
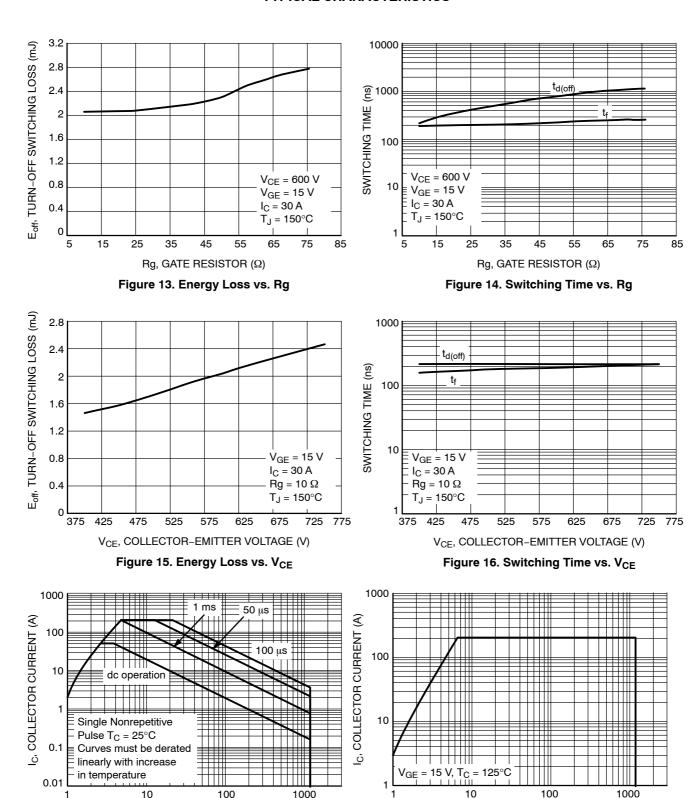


Figure 11. Energy Loss vs. I<sub>C</sub>

Figure 12. Switching Time vs.  $I_{\mathbb{C}}$ 

#### TYPICAL CHARACTERISTICS



V<sub>CE</sub>, COLLECTOR-EMITTER VOLTAGE (V) Figure 17. Safe Operating Area

V<sub>CE</sub>, COLLECTOR-EMITTER VOLTAGE (V)

Figure 18. Reverse Bias Safe Operating Area

#### TYPICAL CHARACTERISTICS

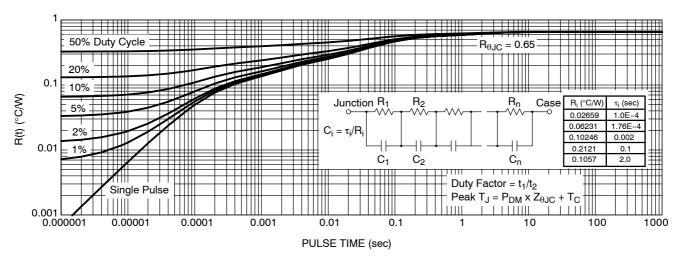


Figure 19. IGBT Transient Thermal Impedance

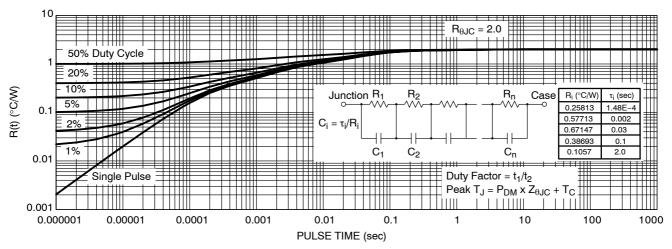


Figure 20. Diode Transient Thermal Impedance

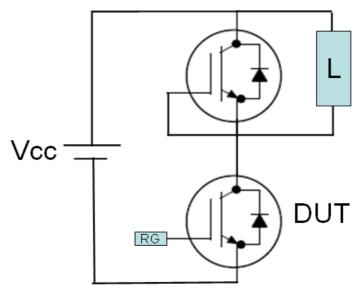


Figure 21. Test Circuit for Switching Characteristics

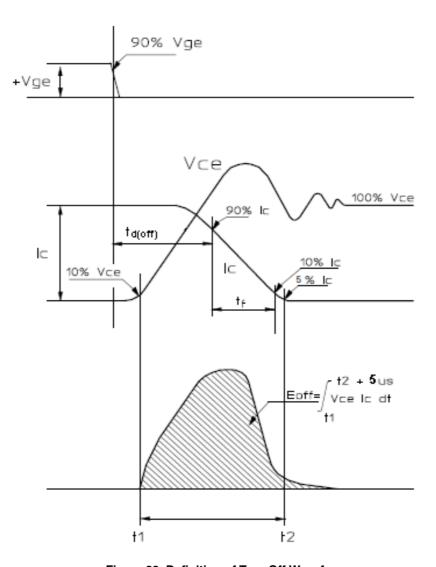
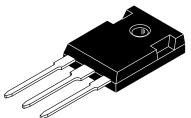


Figure 22. Definition of Turn Off Waveform





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**DATE 06 OCT 2021** 

#### NOTES

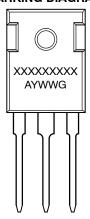
- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: MILLIMETER

	MILLIMETERS		INC	HES
DIM	MIN.	MAX.	MIN.	MAX.
Α	20.32	21.08	0.800	0.830
В	15.75	16.26	0.620	0.640
С	4.70	5.30	0.185	0.209
D	1.00	1.40	0.040	0.055
Ε	1.90	2.60	0.075	0.102
F	1.65	2.13	0.065	0.084
G	5.45 BSC		0.215 BSC	
Н	1.50	2.49	0.059	0.098
J	0.40	0.80	0.016	0.031
К	19.81	20.83	0.780	0.820
L	5.40	6.20	0.212	0.244
N	4.32	5.49	0.170	0.216
Р		4.50		0.177
Q	3.55	3.65	0.140	0.144
U	6.15	6.15 BSC		BSC
W	2.87	3.12	0.113	0.123

# 

**⊕** 0.25 (0.010)**W** Y AS

# GENERIC MARKING DIAGRAM\*



PIN 1. MAIN TERMINAL 1 2. MAIN TERMINAL 2

3. GATE 4. MAIN TERMINAL 2 XXXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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STYLE 5: PIN 1. CATHODE

2. ANODE

3. GATE 4. ANODE

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