



## SMALL SIGNAL SIMULATION FOR THE DISCRETE INDUCTOR SEPIC NCV887100SEPGVB DEMO BOARD

Duty\_Ratio, CTRL, VFB are NCV8871 internal nodes for feedback loop analysis. This is a lossy small-signal model adapted from Chris Basso's published PWMCM\_L lossy PWM switch model. DIO introduces losses from the boost diode. MOSFET  $r_{DS(on)}$  and current sense resistor ( $R_{sense}$ ) losses are included.

Title		
NCV887100 SEPIC EVALUATION BOARD (NCV887100SEPGVB)		
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