The NCP717 is 300 mA LDO that provides the engineer with a very stable, accurate voltage with very low noise suitable for space constrained, noise sensitive applications. In order to optimize performance for battery operated portable applications, the NCP717 employs the dynamic quiescent current adjustment for very low IQ consumption at no−load.

Features
• Operating Input Voltage Range: 1.8 V to 5.5 V
• Available in Fixed Voltage Options: 0.8 V to 5 V
  Contact Factory for Other Voltage Options
• Very Low Quiescent Current of Typ. 25 μA
• Very Low Noise: 22 μVRMS from 100 Hz to 100 kHz
• Low Dropout: 175 mV Typical at 300 mA
• ±2% Accuracy Over Load/Line/Temperature
• High Power Supply Ripple Rejection: 70 dB at 1 kHz
• Thermal Shutdown and Current Limit Protections
• Stable with a 1 μF Ceramic Output Capacitor
• Available in XDFN 1.0 x 1.0 mm Package
• These are Pb−Free Devices

Typical Applications
• PDAs, Mobile phones, GPS, Smartphones
• Wireless Handsets, Wireless LAN, Bluetooth®, Zigbee®
• Portable Medical Equipment
• Other Battery Powered Applications

Figure 1. Typical Application Schematic
Figure 2. Simplified Schematic Block Diagram

PIN FUNCTION DESCRIPTION

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Pin Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>OUT</td>
<td>Regulated output voltage pin. A small ceramic capacitor with minimum value of 1 μF is needed from this pin to ground to assure stability.</td>
</tr>
<tr>
<td>2</td>
<td>GND</td>
<td>Power supply ground.</td>
</tr>
<tr>
<td>3</td>
<td>EN</td>
<td>Driving EN over 0.9 V turns on the regulator. Driving EN below 0.4 V puts the regulator into shutdown mode.</td>
</tr>
<tr>
<td>4</td>
<td>IN</td>
<td>Input pin. A small capacitor is needed from this pin to ground to assure stability.</td>
</tr>
<tr>
<td>−</td>
<td>EPAD</td>
<td>Exposed pad should be connected directly to the GND pin. Soldered to a large ground copper plane allows for effective heat removal.</td>
</tr>
</tbody>
</table>

ABSOLUTE MAXIMUM RATINGS

<table>
<thead>
<tr>
<th>Rating</th>
<th>Symbol</th>
<th>Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Voltage (Note 1)</td>
<td>VIN</td>
<td>−0.3 V to 6 V</td>
<td>V</td>
</tr>
<tr>
<td>Output Voltage</td>
<td>VOUT</td>
<td>−0.3 V to VIN + 0.3 V</td>
<td>V</td>
</tr>
<tr>
<td>Enable Input</td>
<td>VEN</td>
<td>−0.3 V to VIN + 0.3 V</td>
<td>V</td>
</tr>
<tr>
<td>Output Short Circuit Duration</td>
<td>tsc</td>
<td>∞</td>
<td>s</td>
</tr>
<tr>
<td>Maximum Junction Temperature</td>
<td>TJ(MAX)</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>TSTG</td>
<td>−55 to 150</td>
<td>°C</td>
</tr>
<tr>
<td>ESD Capability, Human Body Model (Note 2)</td>
<td>ESDHBM</td>
<td>2000</td>
<td>V</td>
</tr>
<tr>
<td>ESD Capability, Machine Model (Note 2)</td>
<td>ESADM</td>
<td>2000</td>
<td>V</td>
</tr>
</tbody>
</table>

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Refer to ELECTRICAL CHARACTERISTICS and APPLICATION INFORMATION for Safe Operating Area.
2. This device series incorporates ESD protection and is tested by the following methods:
   - ESD Human Body Model tested per EIA/JESD22−A114,
   - ESD Machine Model tested per EIA/JESD22−A115,
   - Latchup Current Maximum Rating tested per JEDEC standard: JESD78.

THERMAL CHARACTERISTICS

<table>
<thead>
<tr>
<th>Rating</th>
<th>Symbol</th>
<th>Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Thermal Characteristics, XDFN4 1x1 mm</td>
<td>RΘJA</td>
<td>250</td>
<td>°C/W</td>
</tr>
<tr>
<td>Thermal Resistance, Junction−to−Air</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
# ELECTRICAL CHARACTERISTICS

−40°C ≤ TJ ≤ 125°C; VIN = VOUT(NOM) + 0.5 V or 2.3 V, whichever is greater; IOUT = 10 mA, CIN = COUT = 1 μF, unless otherwise noted. VEN = 0.9 V. Typical values are at TJ = +25°C. Min./Max. are for TJ = −40°C and TJ = +125°C respectively (Note 3).

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Symbol</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating Input Voltage</td>
<td></td>
<td>VIN</td>
<td>1.8</td>
<td>5.5</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Output Voltage Accuracy</td>
<td>2.3 V ≤ VIN ≤ 4.2 V, IOUT = 0 − 300 mA</td>
<td>VOUT(nom)</td>
<td>&lt; 1.5 V</td>
<td>VOUT</td>
<td>−30</td>
<td>+30 mV</td>
</tr>
<tr>
<td></td>
<td>VOUT(NOM) + 0.5 V or 2.3 V ≤ VIN ≤ 5.5 V, IOUT = 0 − 300 mA</td>
<td>VOUT</td>
<td>−2</td>
<td>+2</td>
<td>%</td>
<td></td>
</tr>
<tr>
<td>Line Regulation</td>
<td>VOUT + 0.5 V or 2.3 V ≤ VIN ≤ 5.5 V, IOUT = 10 mA</td>
<td>RegLINE</td>
<td>400</td>
<td>μV/V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Load Regulation</td>
<td>IOUT = 0 mA to 300 mA</td>
<td>RegLOAD</td>
<td>12</td>
<td>μV/mA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Load Transient</td>
<td>IOUT = 1 mA to 300 mA or 300 mA to 1 mA in 1 μs, COUT = 1 μF</td>
<td>TranLOAD</td>
<td>95</td>
<td>mV</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Dropout Voltage (Note 4)</td>
<td>IOUT = 300 mA</td>
<td>VOUT = 2.5 V</td>
<td>190</td>
<td>350</td>
<td>mV</td>
<td></td>
</tr>
<tr>
<td></td>
<td>VOUT = 2.8 V</td>
<td>175</td>
<td>280</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>VOUT = 2.85 V</td>
<td>175</td>
<td>265</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>VOUT = 3.0 V</td>
<td>170</td>
<td>250</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>VOUT = 3.1 V</td>
<td>165</td>
<td>235</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>VOUT = 3.2 V</td>
<td>165</td>
<td>235</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>VOUT = 3.3 V</td>
<td>155</td>
<td>230</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Output Current Limit</td>
<td>VOUT = 90% VOUT(nom)</td>
<td>ICL</td>
<td>379</td>
<td>500</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>Ground Current</td>
<td>IOUT = 0 mA</td>
<td>IO</td>
<td>25</td>
<td>35</td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td></td>
<td>IOUT = 2 mA</td>
<td>IIGND</td>
<td>105</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>IOUT = 300 mA</td>
<td>IIGND</td>
<td>250</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Shutdown Current</td>
<td>VEN ≤ 0.4 V, VIN = 5.5 V</td>
<td>IDIS</td>
<td>0.01</td>
<td>1</td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td>EN Pin Threshold Voltage</td>
<td>VEN Voltage increasing</td>
<td>VEN_HI</td>
<td>0.9</td>
<td></td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>High Threshold</td>
<td>VEN Voltage decreasing</td>
<td>VEN_LO</td>
<td>0.4</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Low Threshold</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EN Pin Input Current</td>
<td>VEN = 5.5 V</td>
<td>IEN</td>
<td>180</td>
<td>500</td>
<td>nA</td>
<td></td>
</tr>
<tr>
<td>Turn−on Time</td>
<td>COUT = 1.0 μF, From assertion of VEN to 98% VOUT(nom)</td>
<td>ION</td>
<td>200</td>
<td></td>
<td>μs</td>
<td></td>
</tr>
<tr>
<td>Power Supply Rejection Ratio</td>
<td>VIN = 3.6 V, VOUT = 3.1 V, IOUT = 150 mA</td>
<td>f = 100 Hz</td>
<td>PSRR</td>
<td>58</td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>f = 1 kHz</td>
<td></td>
<td>70</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>f = 10 kHz</td>
<td></td>
<td>55</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Output Noise Voltage</td>
<td>VIN = 3.6 V, VOUT = 3.1 V, IOUT = 300 mA</td>
<td>VN</td>
<td>22</td>
<td></td>
<td>μVrms</td>
<td></td>
</tr>
<tr>
<td>Thermal Shutdown Temperature</td>
<td>Temperature increasing from TJ = +25°C</td>
<td>TSD</td>
<td>160</td>
<td></td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td>Thermal Shutdown Hysteresis</td>
<td>Temperature falling from TSD</td>
<td>TSDH</td>
<td>20</td>
<td></td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td>Active Output Discharge Resistance</td>
<td>VEN &lt; 0.4 V</td>
<td>RDIS</td>
<td>1.2</td>
<td></td>
<td>kΩ</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Version A</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Version C</td>
<td></td>
<td>120</td>
<td></td>
<td>Ω</td>
<td></td>
</tr>
</tbody>
</table>
Figure 1. Output Voltage vs. Temperature

$V_{OUT} = 1.2\,V$

$JUNCTION\,TEMPERATURE\,(°C)$

$OUTPUT\,VOLTAGE\,(V)$

$I_{OUT} = 10\,mA$

$I_{OUT} = 300\,mA$

$C_{IN} = C_{OUT} = 1\,\mu F$

$V_{IN} = 2.3\,V$

$V_{OUT\,(NOM)} = 1.2\,V$

Figure 1. Output Voltage vs. Temperature

$V_{OUT} = 1.5\,V$

$JUNCTION\,TEMPERATURE\,(°C)$

$OUTPUT\,VOLTAGE\,(V)$

$I_{OUT} = 10\,mA$

$I_{OUT} = 300\,mA$

$C_{IN} = C_{OUT} = 1\,\mu F$

$V_{IN} = 2.3\,V$

$V_{OUT\,(NOM)} = 1.5\,V$

Figure 2. Output Voltage vs. Temperature

$V_{OUT} = 1.85\,V$

$JUNCTION\,TEMPERATURE\,(°C)$

$OUTPUT\,VOLTAGE\,(V)$

$I_{OUT} = 10\,mA$

$I_{OUT} = 300\,mA$

$C_{IN} = C_{OUT} = 1\,\mu F$

$V_{IN} = 2.3\,V$

$V_{OUT\,(NOM)} = 1.8\,V$

Figure 3. Output Voltage vs. Temperature

$V_{OUT} = 2.85\,V$

$JUNCTION\,TEMPERATURE\,(°C)$

$OUTPUT\,VOLTAGE\,(V)$

$I_{OUT} = 10\,mA$

$I_{OUT} = 300\,mA$

$C_{IN} = C_{OUT} = 1\,\mu F$

$V_{IN} = 3.35\,V$

$V_{OUT\,(NOM)} = 2.85\,V$

Figure 4. Output Voltage vs. Temperature

$V_{OUT} = 3.0\,V$

$JUNCTION\,TEMPERATURE\,(°C)$

$OUTPUT\,VOLTAGE\,(V)$

$I_{OUT} = 10\,mA$

$I_{OUT} = 300\,mA$

$C_{IN} = C_{OUT} = 1\,\mu F$

$V_{IN} = 3.5\,V$

$V_{OUT\,(NOM)} = 3.0\,V$

Figure 5. Quiescent Current vs. Temperature

$V_{OUT} = 1.5\,V$

$JUNCTION\,TEMPERATURE\,(°C)$

$OUTPUT\,VOLTAGE\,(V)$

$QUIESCENT\,CURRENT\,(A)$

$I_{OUT} = 0\,mA$

$T_{A} = 125°\,C$

$T_{A} = 25°\,C$

$T_{A} = -40°\,C$

$C_{IN} = C_{OUT} = 1\,\mu F$

$V_{IN} = 3.5\,V$

$V_{OUT\,(NOM)} = 1.5\,V$
Figure 12. Output Voltage vs. Input Voltage
\( V_{OUT} = 1.8 \text{ V} \)

\( C_{IN} = C_{OUT} = 1 \text{ } \mu\text{F} \)
\( I_{OUT} = 0 \text{ mA} \)
\( V_{OUT(NOM)} = 1.8 \text{ V} \)

\( T_A = 125^\circ \text{C} \)
\( T_A = 25^\circ \text{C} \)
\( T_A = -40^\circ \text{C} \)

Figure 13. Output Voltage vs. Input Voltage
\( V_{OUT} = 2.8 \text{ V} \)

\( C_{IN} = C_{OUT} = 1 \text{ } \mu\text{F} \)
\( I_{OUT} = 0 \text{ mA} \)
\( V_{OUT(NOM)} = 2.8 \text{ V} \)

\( T_A = 125^\circ \text{C} \)
\( T_A = 25^\circ \text{C} \)
\( T_A = -40^\circ \text{C} \)

Figure 14. Output Voltage vs. Input Voltage
\( V_{OUT} = 3.0 \text{ V} \)

\( C_{IN} = C_{OUT} = 1 \text{ } \mu\text{F} \)
\( I_{OUT} = 0 \text{ mA} \)
\( V_{OUT(NOM)} = 3.0 \text{ V} \)

\( T_A = 125^\circ \text{C} \)
\( T_A = 25^\circ \text{C} \)
\( T_A = -40^\circ \text{C} \)

Figure 15. Output Voltage vs. Input Voltage
\( V_{OUT} = 3.1 \text{ V} \)

\( C_{IN} = C_{OUT} = 1 \text{ } \mu\text{F} \)
\( I_{OUT} = 0 \text{ mA} \)
\( V_{OUT(NOM)} = 3.1 \text{ V} \)

\( T_A = 125^\circ \text{C} \)
\( T_A = 25^\circ \text{C} \)
\( T_A = -40^\circ \text{C} \)

Figure 16. Output Voltage vs. Input Voltage
\( V_{OUT} = 3.3 \text{ V} \)

\( C_{IN} = C_{OUT} = 1 \text{ } \mu\text{F} \)
\( I_{OUT} = 0 \text{ mA} \)
\( V_{OUT(NOM)} = 3.3 \text{ V} \)

\( T_A = 125^\circ \text{C} \)
\( T_A = 25^\circ \text{C} \)
\( T_A = -40^\circ \text{C} \)

Figure 17. Dropout Voltage vs. Output Current
\( V_{OUT} = 1.2 \text{ V} \)

\( C_{IN} = C_{OUT} = 1 \text{ } \mu\text{F} \)
\( V_{OUT(NOM)} = 1.2 \text{ V} \)

\( T_A = 25^\circ \text{C} \)
\( T_A = -40^\circ \text{C} \)
Figure 24. Short−Circuit Limit vs. Temperature
\[ V_{OUT} = 1.5 \, \text{V} \]

Figure 25. Short−Circuit Limit vs. Temperature
\[ V_{OUT} = 1.85 \, \text{V} \]

Figure 26. Short−Circuit Limit vs. Temperature
\[ V_{OUT} = 2.85 \, \text{V} \]

Figure 27. Short−Circuit Limit vs. Temperature
\[ V_{OUT} = 3.0 \, \text{V} \]

Figure 28. Short−Circuit Limit vs. Temperature
\[ V_{OUT} = 3.1 \, \text{V} \]

Figure 29. Short−Circuit Limit vs. Temperature
\[ V_{OUT} = 3.3 \, \text{V} \]
Figure 30. Line Regulation vs. Temperature

\( V_{\text{OUT}} = 1.2 \, \text{V} \)

\( \text{JUNCTION TEMPERATURE (°C)} \)

\( -40 \quad -20 \quad 0 \quad 140 \quad 120 \quad 100 \quad 80 \quad 60 \quad 40 \quad 20 \quad 0 \quad 20 \quad 40 \quad 60 \quad 80 \quad 100 \quad 120 \quad 140 \)

\( 0.0 \quad 0.5 \quad 1.0 \quad 1.5 \quad 2.0 \quad 2.5 \quad 3.0 \quad 3.5 \quad 4.0 \quad 4.5 \quad 5.0 \)

Line Regulation from \( V_{\text{IN}} = 2 \, \text{V} \) to 5.5 V

Figure 31. Line Regulation vs. Temperature

\( V_{\text{OUT}} = 1.5 \, \text{V} \)

\( \text{JUNCTION TEMPERATURE (°C)} \)

\( -40 \quad -20 \quad 0 \quad 140 \quad 120 \quad 100 \quad 80 \quad 60 \quad 40 \quad 20 \quad 0 \quad 20 \quad 40 \quad 60 \quad 80 \quad 100 \quad 120 \quad 140 \)

\( 0.0 \quad 0.5 \quad 1.0 \quad 1.5 \quad 2.0 \quad 2.5 \quad 3.0 \quad 3.5 \quad 4.0 \quad 4.5 \quad 5.0 \)

Line Regulation from \( V_{\text{IN}} = 2 \, \text{V} \) to 5.5 V

Figure 32. Line Regulation vs. Temperature

\( V_{\text{OUT}} = 1.85 \, \text{V} \)

\( \text{JUNCTION TEMPERATURE (°C)} \)

\( -40 \quad -20 \quad 0 \quad 140 \quad 120 \quad 100 \quad 80 \quad 60 \quad 40 \quad 20 \quad 0 \quad 20 \quad 40 \quad 60 \quad 80 \quad 100 \quad 120 \quad 140 \)

\( 0.0 \quad 0.5 \quad 1.0 \quad 1.5 \quad 2.0 \quad 2.5 \quad 3.0 \quad 3.5 \quad 4.0 \quad 4.5 \quad 5.0 \)

Line Regulation from \( V_{\text{IN}} = 2.35 \, \text{V} \) to 5.5 V

Figure 33. Line Regulation vs. Temperature

\( V_{\text{OUT}} = 2.85 \, \text{V} \)

\( \text{JUNCTION TEMPERATURE (°C)} \)

\( -40 \quad -20 \quad 0 \quad 140 \quad 120 \quad 100 \quad 80 \quad 60 \quad 40 \quad 20 \quad 0 \quad 20 \quad 40 \quad 60 \quad 80 \quad 100 \quad 120 \quad 140 \)

\( 0.0 \quad 0.5 \quad 1.0 \quad 1.5 \quad 2.0 \quad 2.5 \quad 3.0 \quad 3.5 \quad 4.0 \quad 4.5 \quad 5.0 \)

Line Regulation from \( V_{\text{IN}} = 3.35 \, \text{V} \) to 5.5 V

Figure 34. Line Regulation vs. Temperature

\( V_{\text{OUT}} = 3.0 \, \text{V} \)

\( \text{JUNCTION TEMPERATURE (°C)} \)

\( -40 \quad -20 \quad 0 \quad 140 \quad 120 \quad 100 \quad 80 \quad 60 \quad 40 \quad 20 \quad 0 \quad 20 \quad 40 \quad 60 \quad 80 \quad 100 \quad 120 \quad 140 \)

\( 0.0 \quad 0.5 \quad 1.0 \quad 1.5 \quad 2.0 \quad 2.5 \quad 3.0 \quad 3.5 \quad 4.0 \quad 4.5 \quad 5.0 \)

Line Regulation from \( V_{\text{IN}} = 3.5 \, \text{V} \) to 5.5 V

Figure 35. Line Regulation vs. Temperature

\( V_{\text{OUT}} = 3.1 \, \text{V} \)
Figure 36. Line Regulation vs. Temperature
\[ V_{OUT} = 3.3 \text{ V} \]

Figure 37. Load Regulation vs. Temperature
\[ V_{OUT} = 1.2 \text{ V} \]

Figure 38. Load Regulation vs. Temperature
\[ V_{OUT} = 1.5 \text{ V} \]

Figure 39. Load Regulation vs. Temperature
\[ V_{OUT} = 1.8 \text{ V} \]

Figure 40. Load Regulation vs. Temperature
\[ V_{OUT} = 2.85 \text{ V} \]

Figure 41. Ground Current vs Output Current
\[ V_{IN} = V_{OUT(NOM)} + 0.5 \text{ V} \]

\[ T_A = 25^\circ \text{C} \]

\[ T_A = -40^\circ \text{C} \]
Figure 42. Ground Current vs. Temperature

Figure 43. Stability vs. Output Capacitor ESR

Figure 44. PSRR vs. Frequency

Figure 45. PSRR vs. Frequency

Figure 46. PSRR vs. Frequency

Figure 47. PSRR vs. Frequency
Figure 48. Output Noise Density vs. Frequency
V<sub>OUT</sub> = 1.5 V

Figure 49. Output Noise Density vs. Frequency
V<sub>OUT</sub> = 3.1 V

Figure 50. Enable Input Current vs. Enable Voltage

Figure 51. Enable Threshold Voltage vs. Temperature

Figure 52. Shutdown Current vs. Temperature

Figure 53. V<sub>OUT</sub> Turn–on Time vs. Temperature
The NCP717 is a high performance, small package size, 300 mA LDO voltage regulator. This device delivers very good noise and dynamic performance. Thanks to its adaptive ground current feature the device consumes only 25 µA of quiescent current at no−load condition. The regulator features very−low noise of 22 µVRMS, PSRR of typ. 70 dB at 1 kHz and very good load/line transient response. The device is an ideal choice for space constrained portable applications.

A logic EN input provides ON/OFF control of the output voltage. When the EN is low the device consumes as low as typ. 10 nA from the IN pin.

The device is fully protected in case of output overload, output short circuit condition and overheating, assuring a very robust design.

Input Capacitor Selection (CIN)

It is recommended to connect a minimum of 1 µF Ceramic X5R or X7R capacitor close to the IN pin of the device. Larger input capacitors may be necessary if fast and large load transients are encountered in the application. There is no requirement for the min./max. ESR of the input capacitor but it is recommended to use ceramic capacitors for their low ESR and ESL.

Output Capacitor Selection (COUT)

The NCP717 is designed to be stable with small 1.0 µF and larger ceramic capacitors on the output. The minimum effective output capacitance for which the LDO remains stable is 100 nF. The safety margin is provided to account for capacitance variations due to DC bias voltage, temperature, initial tolerance. There is no requirement for the minimum value of Equivalent Series Resistance (ESR) for the COUT but the maximum value of ESR should be less than 700 mΩ.

Larger output capacitors could be used to improve the load transient response or high frequency PSRR characteristics. It is not recommended to use tantalum capacitors on the output due to their large ESR. The equivalent series resistance of tantalum capacitors is also strongly dependent on the temperature, increasing at low temperature. The tantalum capacitors are generally more costly than ceramic capacitors.

No−load Operation

The regulator remains stable and regulates the output voltage properly within the ±2% tolerance limits even with no external load applied to the output.

Enable Operation

The NCP717 uses the EN pin to enable/disable its output and to control the active discharge function. If the EN pin voltage is < 0.4 V the device is guaranteed to be disabled. The pass transistor is turned−off so that there is virtually no current flow between the IN and OUT. In case of the option equipped with active discharge – the active discharge transistor is turned−on and the output voltage VOUT is pulled to GND through a 1.2 kΩ resistor (A version) or 120 Ω (C version). In the disable state the device consumes as low as typ. 10 nA from the VIN. If the EN pin voltage > 0.9 V the device is guaranteed to be enabled. The NCP717 regulates the output voltage and the active discharge transistor is turned−off. The EN pin has an internal pull−down current source with typ. value of 180 nA which assures that the device is turned−off when the EN pin is not connected. A build in 56 mV of hysteresis and de-glitch time in the EN block prevent from periodic on/off oscillations that can occur due to noise on EN line. In the case that the EN function isn’t required the EN pin should be tied directly to IN.

Reverse Current

The PMOS pass transistor has an inherent body diode which will be forward biased in the case that VOUT > VIN. Due to this fact in cases where the extended reverse current condition is anticipated the device may require additional external protection.

Output Current Limit

Output Current is internally limited within the IC to a typical 379 mA. The NCP717 will source this amount of current measured with the output voltage 100 mV lower than the nominal VOUT. If the Output Voltage is directly shorted to ground (VOUT = 0 V), the short circuit protection will limit the output current to 390 mA (typ). The current limit and short circuit protection will work properly up to VIN =5.5 V at TA = 25°C. There is no limitation for the short circuit duration.

Thermal Shutdown

When the die temperature exceeds the Thermal Shutdown threshold (TSD − 160°C typical), Thermal Shutdown event is detected and the device is disabled. The IC will remain in this state until the die temperature decreases below the Thermal Shutdown Reset threshold (TSDU − 140°C typical). Once the IC temperature falls below the 140°C the LDO is enabled again. The thermal shutdown feature provides protection from a catastrophic device failure due to accidental overheating. This protection is not intended to be used as a substitute for proper heat sinking.

Power Dissipation

As power dissipated in the NCP717 increases, it might become necessary to provide some thermal relief. The maximum power dissipation supported by the device is dependent upon board design and layout. Mounting pad configuration on the PCB, the board material, and the ambient temperature affect the rate of junction temperature rise for the part. The maximum power dissipation the NCP717 can handle is given by:

\[ P_{D(MAX)} = \frac{125 - T_A}{\theta_{JA}} \]  

(eq. 1)
For reliable operation junction temperature should be limited to +125°C.

The power dissipated by the NCP717 for given application conditions can be calculated as follows:

\[ P_{D\text{(MAX)}} = \frac{V_{IN}I_{GND} + I_{OUT}(V_{IN} - V_{OUT})}{JC} \]  

(eq. 2)

Figure 54 shows the typical values of \( \theta_{JA} \) vs. heat spreading area.

**Load Regulation**

The NCP717 features very good load regulation of typical 3.6 mV in the 0 mA to 300 mA range. In order to achieve this very good load regulation a special attention to PCB design is necessary. The trace resistance from the OUT pin to the point of load can easily approach 100 mΩ which will cause a 68 mV voltage drop at full load current, deteriorating the excellent load regulation.

**Line Regulation**

The IC features very good line regulation of 0.4 mV/V measured from \( V_{IN} = V_{OUT} + 0.5 \text{ V} \) to 5.5 V.

**Power Supply Rejection Ratio**

At low frequencies the PSRR is mainly determined by the feedback open–loop gain. At higher frequencies in the range 100 kHz – 10 MHz it can be tuned by the selection of \( C_{OUT} \) capacitor and proper PCB layout.

![Figure 54. Thermal Parameters vs. Copper Area](image_url)

**Output Noise**

The IC is designed for very–low output voltage noise. The typical noise performance of 22 μV_{RMS} makes the device suitable for noise sensitive applications.

**Internal Soft Start**

The Internal Soft–Start circuitry will limit the inrush current during the LDO turn–on phase. Please refer to typical characteristics section for typical inrush current values. The soft–start function prevents from any output voltage overshoots and assures monotonic ramp–up of the output voltage.

**PCB Layout Recommendations**

To obtain good transient performance and good regulation characteristics place \( C_{IN} \) and \( C_{OUT} \) capacitors close to the device pins and make the PCB traces wide. In order to minimize the solution size use 0402 capacitors. Larger copper area connected to the pins will also improve the device thermal resistance. The actual power dissipation can be calculated by the formula given in Equation 2.
## ORDERING INFORMATION

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†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.
**PACKAGE DIMENSIONS**

**XDFN4 1.0x1.0, 0.65P**

**CASE 711AJ**

**ISSUE O**

**NOTES:**

2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 mm FROM THE TERMINAL TIPS.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

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*For additional information on our Pb−Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.*