



Test Procedure for the NCP4306LLC60GEVB Evaluation Board

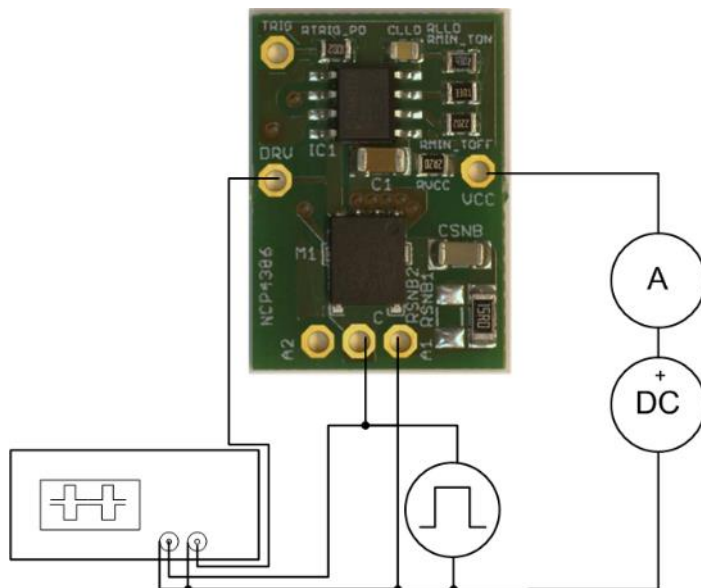


Figure 1: Test Setup

The following steps describe the test procedure for all these boards:

Required Equipment:

DC voltage source (e.g. STATRON 2229)	1pc
DC Amp-Meter (e.g. KEITHLEY 2000).....	1pc
Function generator (e.g. AFG3252)	1pc
2 channel oscilloscope	1pc

Test Procedure:

1. Connect the test setup as shown in figure 1.
2. Apply an supply voltage, $V_{CC} = 12\text{ V}$
3. Apply pulse from generator (pulse, $f = 100\text{ kHz}$, $DC = 50\%$, $V_{LOW} = -1\text{ V}$, $V_{HIGH} = 4\text{ V}$, output impedance = high Z)
4. Check that $I_{CC} = 6 - 8\text{ mA}$, waveforms look like in figure 2 (DRV pulse length may oscillate between 330 ns and 5 us, in case of short time there may be two pulses – like it is shown in figure)
5. Apply pulse from generator (pulse, $f = 100\text{ kHz}$, $DC = 50\%$, $V_{LOW} = +1\text{ V}$, $V_{HIGH} = 4\text{ V}$, output impedance = high Z)
6. Check that $I_{CC} = \sim 60\text{ }\mu\text{A}$, waveforms look like in figure 3 (no DRV pulses)
7. Turn off V_{CC}
8. End of the test

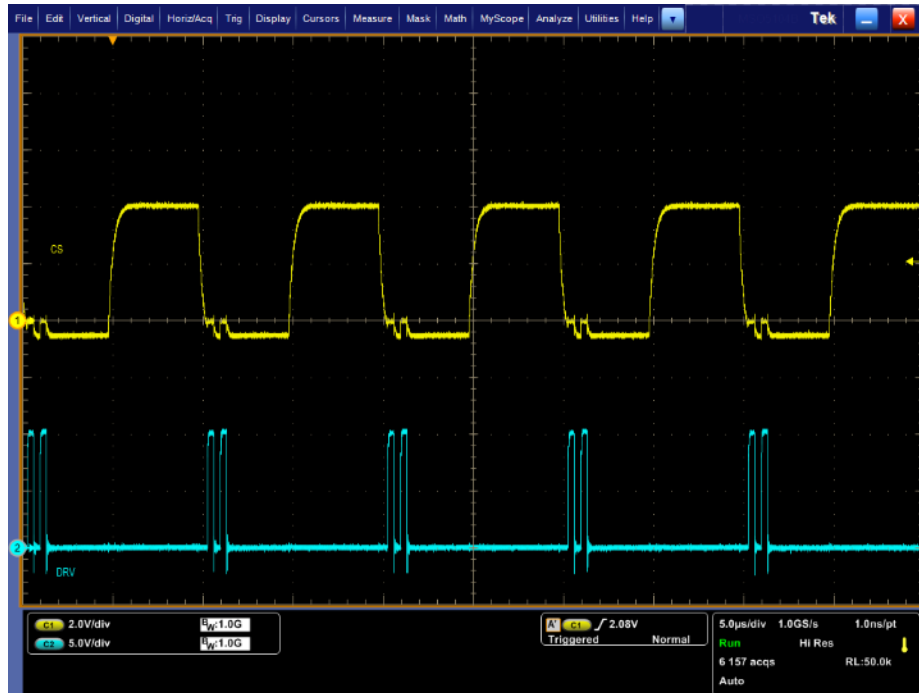


Figure 2: $V_{CC} = 12\text{ V}$, $f = 100\text{ kHz}$, $DC = 50\%$, $V_{LOW} = -1\text{ V}$, $V_{HIGH} = 4\text{ V}$

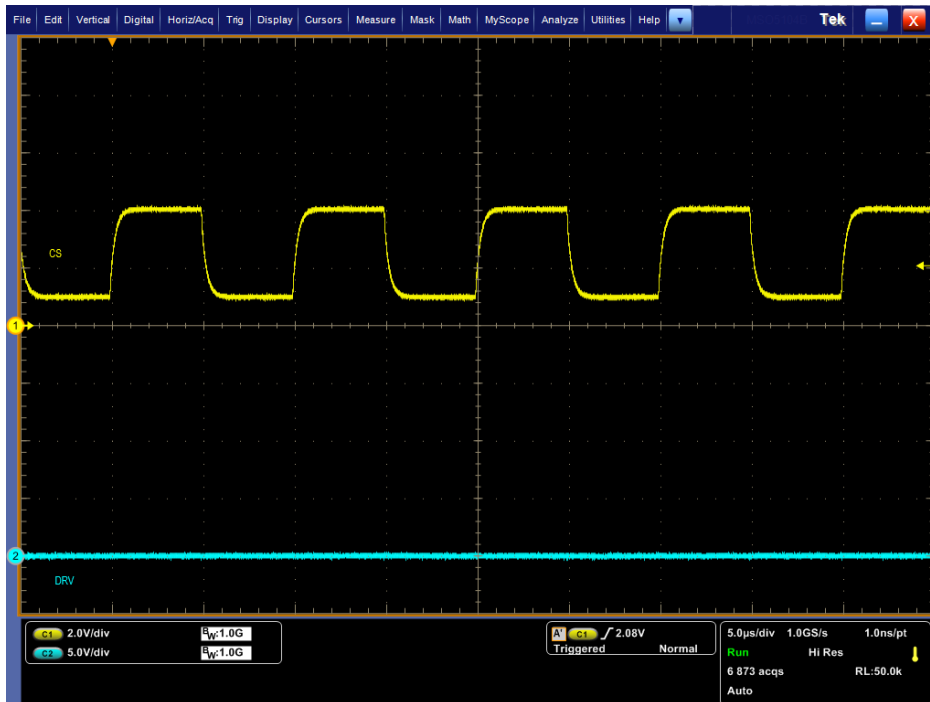


Figure 3: $V_{CC} = 12\text{ V}$, $f = 100\text{ kHz}$, $DC = 50\%$, $V_{LOW} = +1\text{ V}$, $V_{HIGH} = 4\text{ V}$