ON Semiconductor®

Test Procedure for the NCP4306FLY150GEVB Evaluation Board

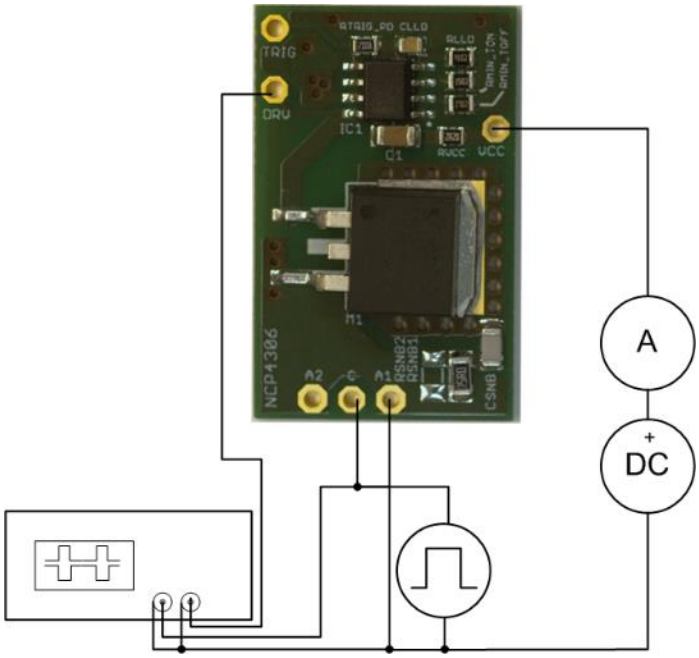

Figure 1: Test Setup

The following steps describe the test procedure for all these boards:

Required Equipment:

DC voltage source (e.g. STATRON 2229) ……...…… 1pc
DC Amp-Meter (e.g. KEITHLEY 2000)…… 1pc
Function generator (e.g. AFG3252) ……... 1pc
2 channel oscilloscope ……... 1pc

Test Procedure:

1. Connect the test setup as shown in figure 1.
2. Apply an supply voltage, V_{CC} = 12 V
3. Apply pulse from generator (pulse, f = 100 kHz, DC = 50%, V_{LOW} = -1 V, V_{HIGH} = 4 V, output impedance = high Z)
4. Check that I_{CC} = 5 - 7 mA, waveforms look like in figure 2 (DRV pulse length may oscillate between 1.5 us and 5 us)
5. Apply pulse from generator (pulse, f = 100 kHz, DC = 50%, V_{LOW} = +1 V, V_{HIGH} = 4 V, output impedance = high Z)
6. Check that I_{CC} = ~60 µA, waveforms look like in figure 3 (no DRV pulses)
7. Turn off V_{CC}
8. End of the test

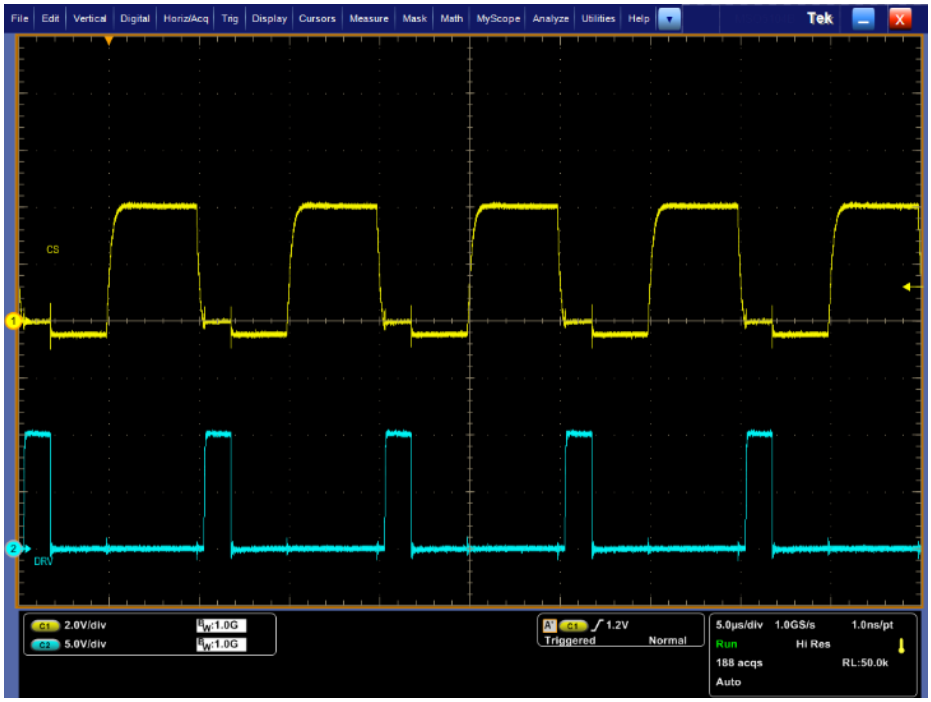

Figure 2: V_{CC} = 12 V, f = 100 kHz, DC = 50 %, V_{LOW} = -1 V, V_{HIGH} = 4 V

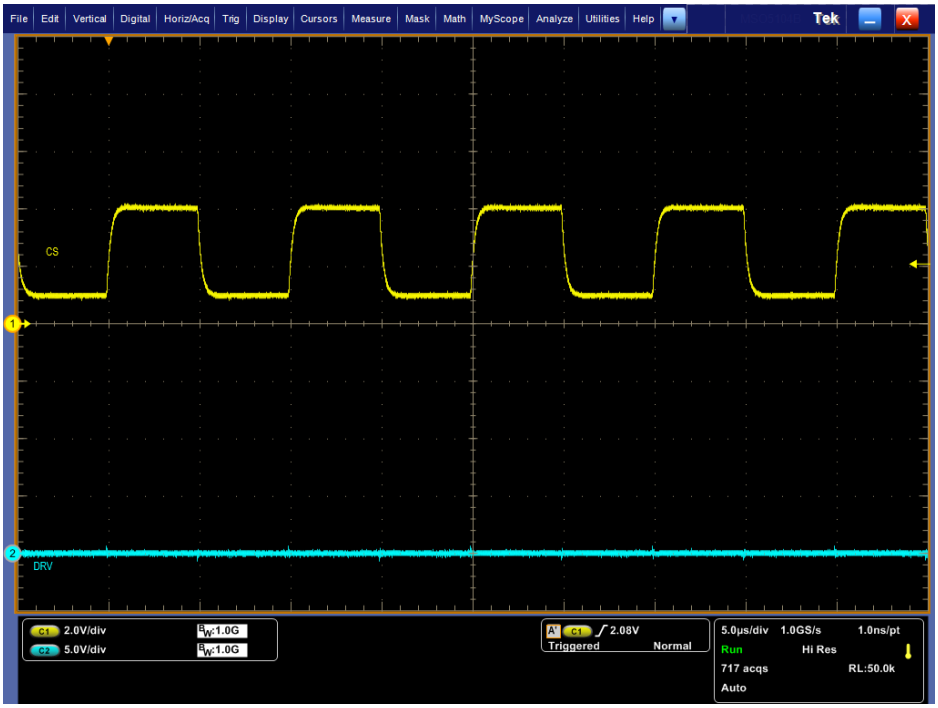

Figure 3: V_{CC} = 12 V, f = 100 kHz, DC = 50 %, V_{LOW} = +1 V, V_{HIGH} = 4 V