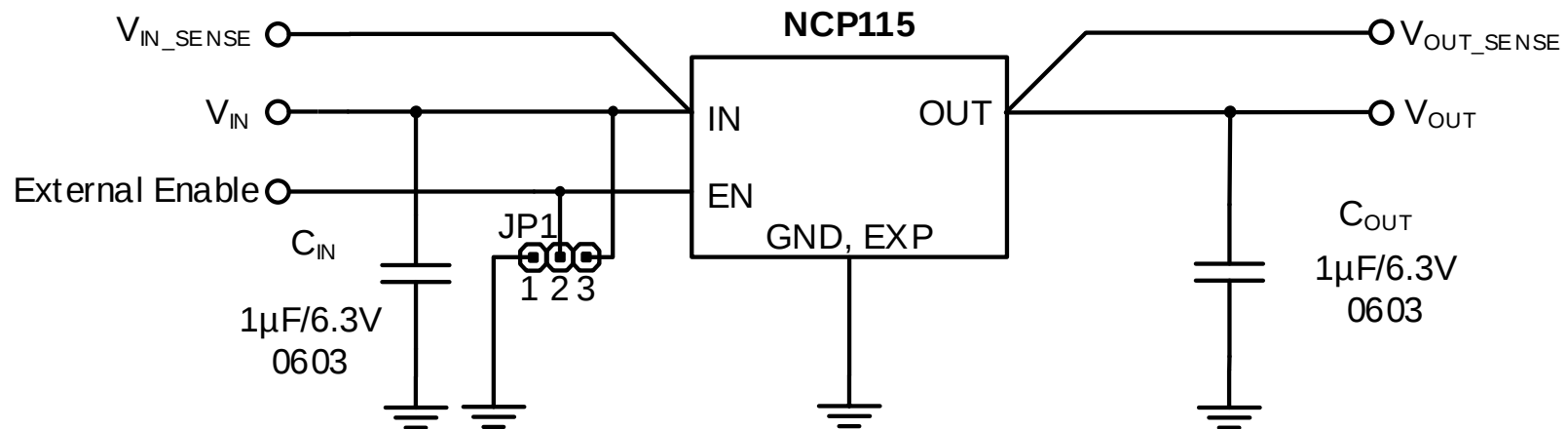




NCP115 Evaluation Board Schematic

**JP1 positions:**2-- --> **EXTERNAL ENABLE** pin is select2 – 3 -> LDO is **ENABLED**2 – 1-> LDO is **DISABLED**

The NCP115 is 300mA high performance LDO with low quiescent current and high PSRR. The NCP115 also offers excellent load/line transients.