NCN5121, NCN5130 ERRATA

Errata

NCN5121, NCN5130 Revision 4 Hardware Errata

2016−January−4

ON Semiconductor®

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Problem Identifier	Problem Description	Impact	Workaround
Use of external clock source prevents startup of the digital state machine	When an external clock source is applied to pin XTAL2 and the clock oscillator circuit is disabled by pulling high XSEL, the digital part of the transceiver is stuck in Power−Up state. This problem occurs only on silicon where the first 3 bits of the Revision ID register read out as 0x04. The device marking of the affected silicon is 21420−001 (NCN5130) or 21420−002 (NCN5121)	It is not possible to send or receive KNX frames to/from the KNX bus. Communication between microcontroller and transceiver is possible, and all analog functions of the transceiver still work as expected.	Connect the external clock source to the XTAL1 and leave XTAL2 floating. XSEL must be pulled low. This workaround will work on all revisions of ON Semiconductor KNX transceivers, including later versions no longer suffering from this bug. Current consumption on VDD will be ~ 0.5 mA higher with this workaround.

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