ON Semiconductor

Is Now



To learn more about onsemi™, please visit our website at www.onsemi.com

onsemi and ONSEMI. and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/ or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use onsemi products for any such unintended or unauthorized application,

NB6L14MMNGEVB Assembly and Test Manual

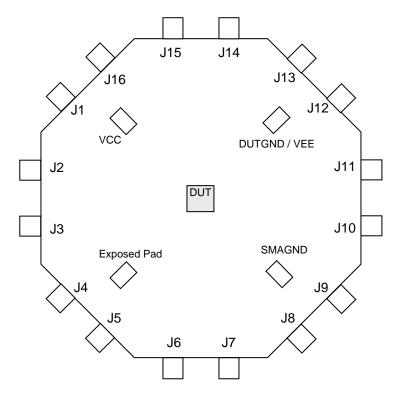
For more detailed information on the QFN16 evaluation board, see the QFN16EVB application note.

NB6L14MMNGEVB Assembly

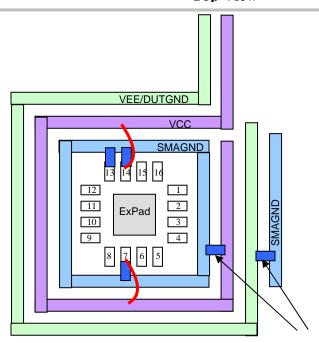
Table 1. Configuration for Device: NB6L14M

	J1	J2	J3	J4	J5	J6	J7	J8	J9	J10	J11	J12	J13	J14	J15	J16
Device Pin #	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
SMA Connector	Yes	Yes	Yes	Yes	Yes	Yes	No	Yes	Yes	Yes	Yes	Yes	No	No	Yes	Yes
Wire	No	No	No	No	No	No	VCC	No	No	No	No	No	GND	VCC	No	No

Note: DUTGND / VEE = Exposed Pad and must be tied to DUTGND / VEE



Top View



Bottom View

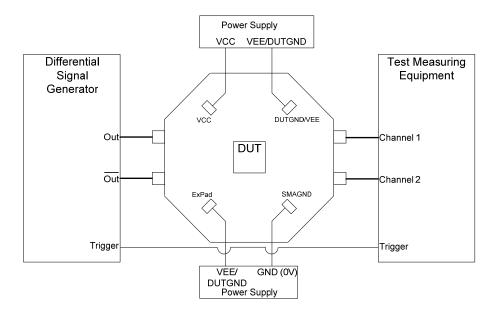
Install 0.1uF Decoupling Capacitors here and at package pins

Table 2. Bill of Materials

Components	Manufacturer	Description	Part Number	QTY	Web Site
SMA Connector	Rosenberger	SMA Connector,	32K243-40ME3	13	http://www.rosenberger.de
		Side Launch, Gold			http://www.rosenbergerna.com
		Plated			
Surface Mount	Keystone*	SMT Miniature Test	5015	4	http://www.keyelco.com
Test Points		Point			
Chip Capacitor	AVC	$0603\ 0.01 \mu F \pm 10\%$	06035C103KAT2A	na	http://www.avxcorp.com
	Corporation*	$0603~0.1 \mu F \pm 10\%$	0603C104KAT2A	5	
Chip Resistor	Panasonic*	$0402\ 50\ \Omega \pm 1\%$	ERJ-2RKF49R9X	na	http://www.panasonic.com
		Precision Thick Film			
		Chip Resistor			
Evaluation	ON	QFN-16 Evaluation	QFN16EVB	1	http://www.onsemi.com
Board	Semiconductor	Board			
Device Samples	ON	QFN 16 Package	NB6L14MMNG	1	http://www.onsemi.com
	Semiconductor	Device			

 $^{{}^{\}star}\text{Components are available through most distributors, i.e. www.newark.com,}\,\,\underline{\text{www.Digikey.com}}$

NB6L14MMNGEVB Test



Basic Lab Setup (typical)

- 1. Connect the appropriate power supplies to VCC, VEE/DUTGND, SMAGND, and ExPad (See Table 2.).
- 2. Connect a signal generator to the input SMA connectors. Setup input signal according to the device data sheet.
- 3. Connect a test measurement device to the device's output SMA connectors. NOTE: The test measurement device must contain $50-\Omega$ termination.

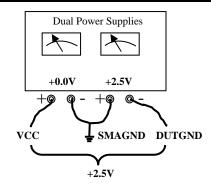
Table 3. Power Supply Levels

Outputs	Power Supply	V _{CC}	V _{EE} / DUTGND	SMAGND	ExPad (typ.)
CML	2.5V	0.0V	-2.5V	0.0V	VEE / DUTGND
CML	3.3V	0.0V	-3.3V	0.0V	VEE / DUTGND

"Split" or Dual Power Supply Connections

Table 3. NB6xxxM, CML Outputs "Split" Power Supply Configuration

Device Pin	"Spilt" Power Supply				
Power Supply Connector					
VCC	VCC = 0.0V				
SMAGND	VTT = 0V				
DUTGND	DUTGND = -2.5V or -3.3V				



Offset / "Split" Power Supply Configuration