

MOSFET – N-Channel, QFET

200 V, 19 A, 170 mΩ

FQP19N20C, FQPF19N20C

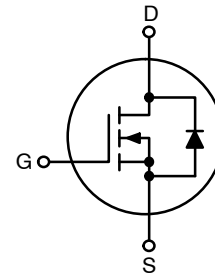
Description

This N-Channel enhancement mode power MOSFET is produced using onsemi's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

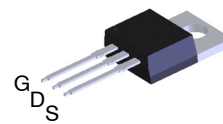
Features

- 19 A, 200 V, $R_{DS(on)} = 170 \text{ m}\Omega$ (Max) @ $V_{GS} = 10 \text{ V}$, $I_D = 9.5 \text{ A}$
- Low Gate Charge (Typ. 40.5 nC)
- Low C_{RSS} (Typ. 85 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

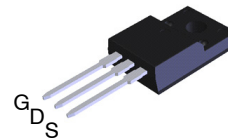
V_{DS}	$R_{DS(ON)}$ MAX	I_D MAX
200 V	170 mΩ @ 10 V	19 A



N-Channel MOSFET

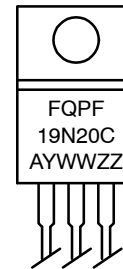


TO-220-3LD
CASE 340AT



TO-220 Fullpack, 3-Lead
/ TO-220F-3SG
CASE 221AT

MARKING DIAGRAM



FQP19N20C
 FQPF19N20C = Device Code
 A = Assembly Location
 YWW = Date Code (Year & Week)
 ZZ = Assembly Lot

ORDERING INFORMATION

Device	Package	Shipping
FQP19N20C	TO-220	1000 Units / Tube
FQPF19N20C	TO-200F	1000 Units / Tube

FQP19N20C, FQPF19N20C

MOSFET MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	FQP19N20C	FQPF19N20C	Unit	
V_{DSS}	Drain to Source Voltage	200		V	
I_D	Drain Current	- Continuous ($T_C = 25^\circ\text{C}$)	19.0	19.0*	A
		- Continuous ($T_C = 100^\circ\text{C}$)	12.1	12.1*	
I_{DM}	Drain Current	- Pulsed (Note 1)	76.0	76.0*	A
V_{GSS}	Gate to Source Voltage	± 30		V	
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	433		mJ	
I_{AR}	Avalanche Current (Note 1)	19.0		A	
E_{AR}	Repetitive Avalanche Energy (Note 1)	13.9		mJ	
dv/dt	Peak Diode Recovery dv/dt (Note 3)	5.5		V/ns	
P_D	Power Dissipation	($T_C = 25^\circ\text{C}$)	139	43	W
		-Derate above 25°C	1.11	0.34	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150		$^\circ\text{C}$	
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds	300		$^\circ\text{C}$	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

*Drain current limited by maximum junction temperature.

1. Repetitive rating: pulse-width limited by maximum junction temperature.
2. $L = 1.8\text{ mH}$, $I_{AS} = 19.0\text{ V}$, $V_{DD} = 50\text{ V}$, $R_G = 25\ \Omega$, starting $T_J = 25^\circ\text{C}$.
3. $I_{SD} \leq 19.0\text{ A}$, $di/dt \leq 300\text{ A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, starting $T_J = 25^\circ\text{C}$.

THERMAL CHARACTERISTICS

Symbol	Parameter	FCP190N60	FCPF190N60	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.9	2.89	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	62.5	

FQP19N20C, FQPF19N20C

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	200	-	-	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	-	0.24	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 200 V, V _{GS} = 0 V	-	-	10	μA
		V _{DS} = 160 V, T _C = 125°C	-	-	100	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V	-	-	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V	-	-	-100	nA

ON CHARACTERISTICS

V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	2.0	-	4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 9.5 A	-	0.14	0.17	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 9.5 A	-	10.8	-	S

DYNAMIC CHARACTERISTICS

C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz	-	830	1080	pF
C _{oss}	Output Capacitance		-	195	255	pF
C _{rss}	Reverse Transfer Capacitance		-	85	110	pF

SWITCHING CHARACTERISTICS

t _{d(on)}	Turn-On Delay Time	V _{DD} = 100 V, I _D = 19.0 A, R _G = 25 Ω (Note 4)	-	15	40	ns
t _r	Turn-On Rise Time		-	150	310	ns
t _{d(off)}	Turn-Off Delay Time		-	135	280	ns
t _f	Turn-Off Fall Time		-	115	240	ns
Q _g	Total Gate Charge	V _{DS} = 160 V, I _D = 19.0 A, V _{GS} = 10 V (Note 4)	-	40.5	53.0	nC
Q _{gs}	Gate-Source Charge		-	6.0	-	nC
Q _{gd}	Gate-Drain Charge		-	22.5	-	nC

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I _S	Maximum Continuous Drain-Source Diode Forward Current	-	-	19.0	A	
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current	-	-	76.0	A	
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 19.0 A	-	-	1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 19.0 A, dI _F /dt = 100 A/μs	-	208	-	ns
Q _{rr}	Reverse Recovery Charge		-	1.63	-	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially Independent of Operating Temperature.

FQP19N20C, FQPF19N20C

TYPICAL CHARACTERISTICS

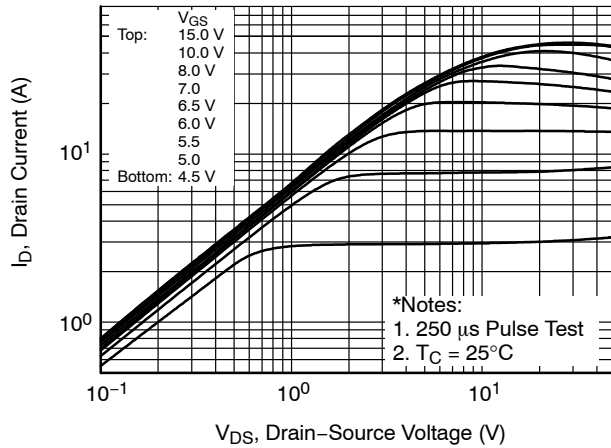


Figure 1. On-Region Characteristics

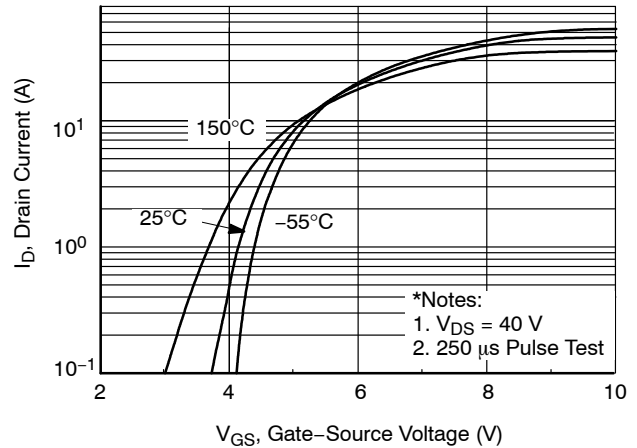


Figure 2. Transfer Characteristics

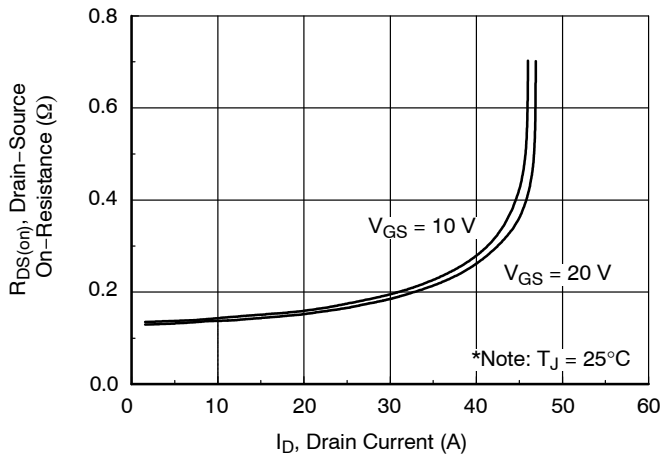


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

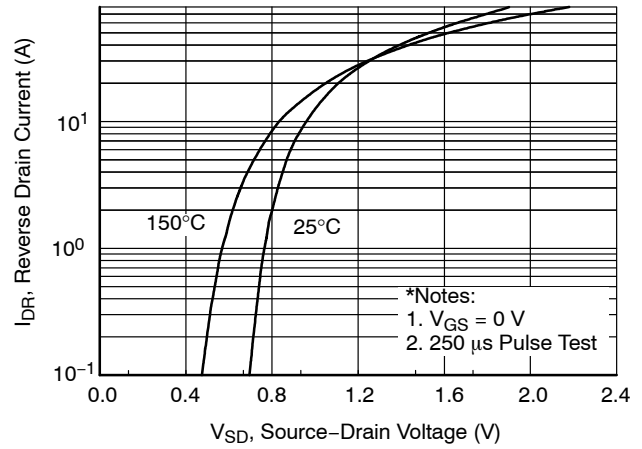


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

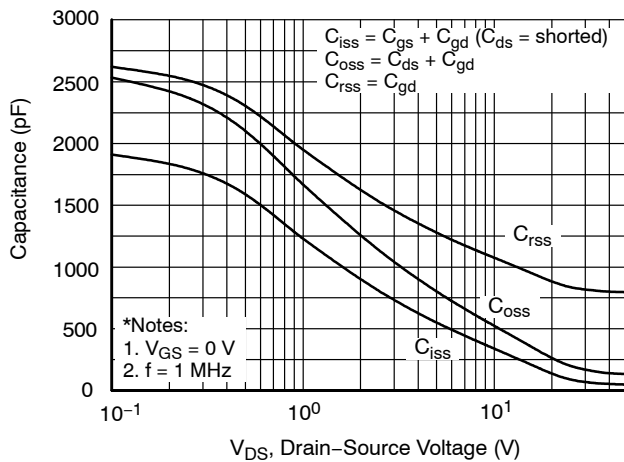


Figure 5. Capacitance Characteristics

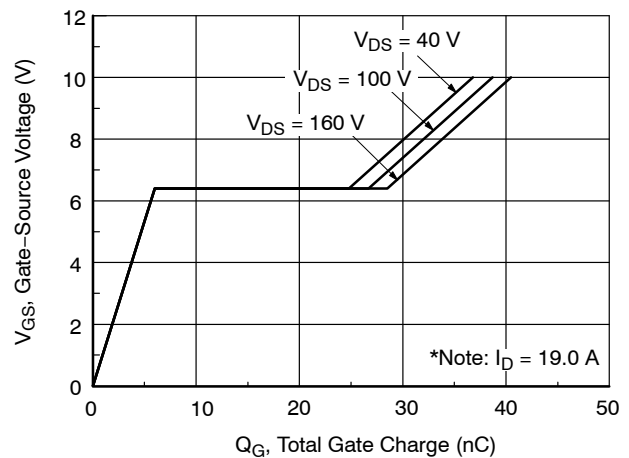
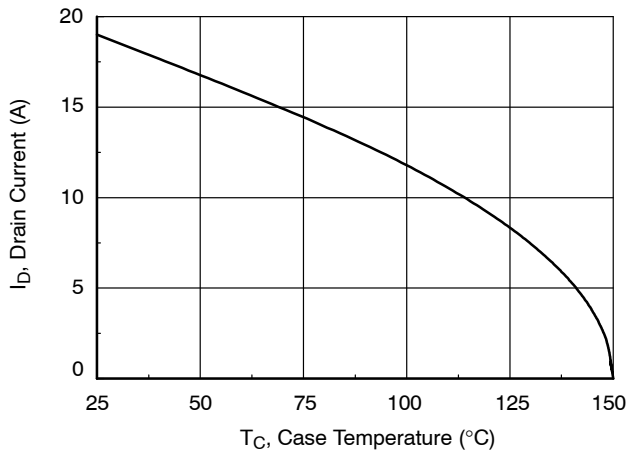
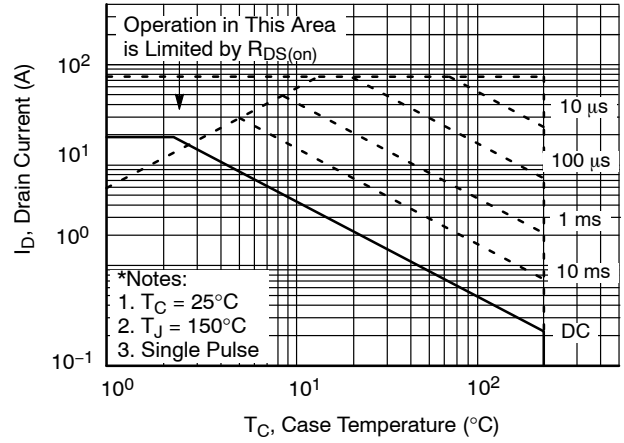
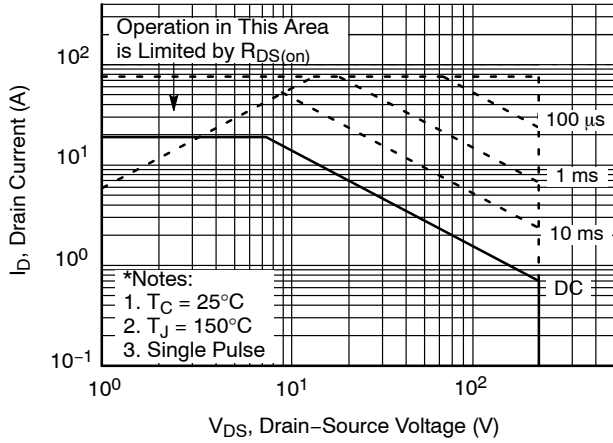
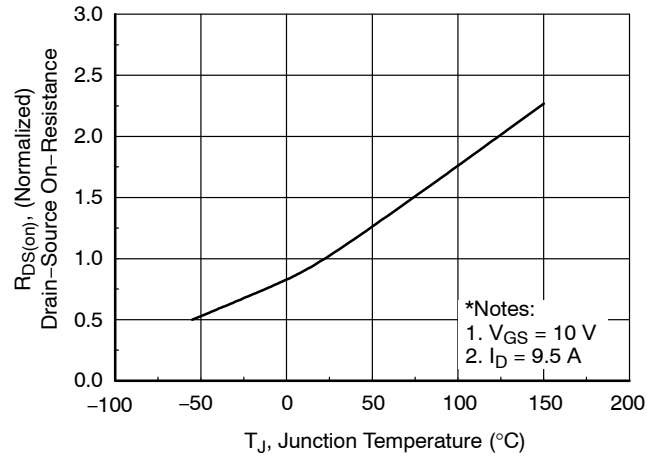
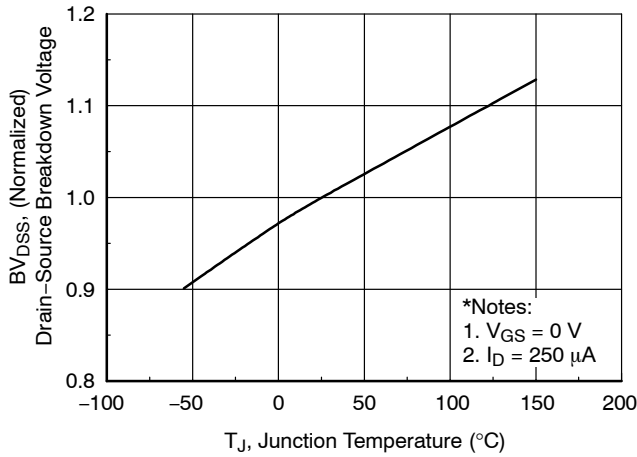


Figure 6. Gate Charge Characteristics

FQP19N20C, FQPF19N20C

TYPICAL CHARACTERISTICS (continued)



FQP19N20C, FQPF19N20C

TYPICAL CHARACTERISTICS (continued)

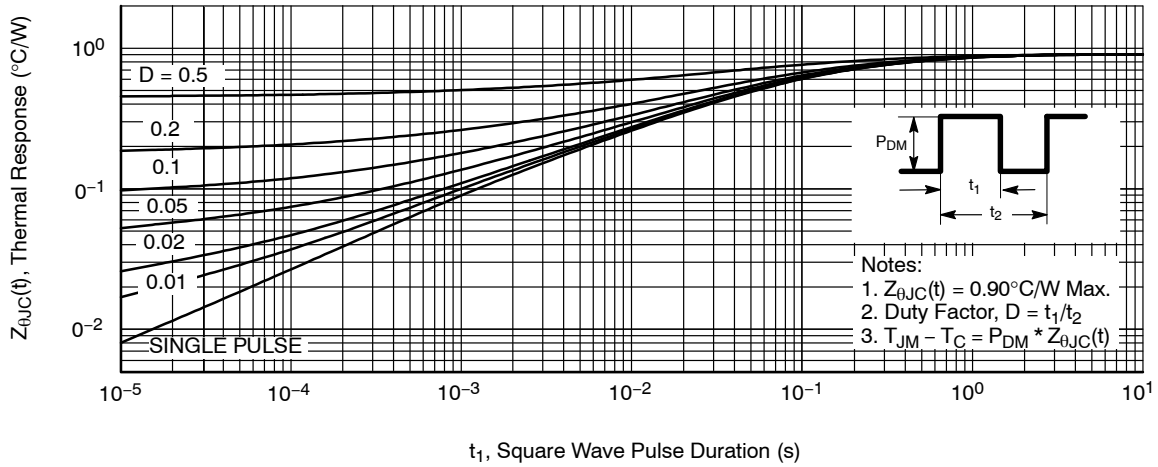


Figure 11.1. Transient Thermal Response Curve for FQP19N20C

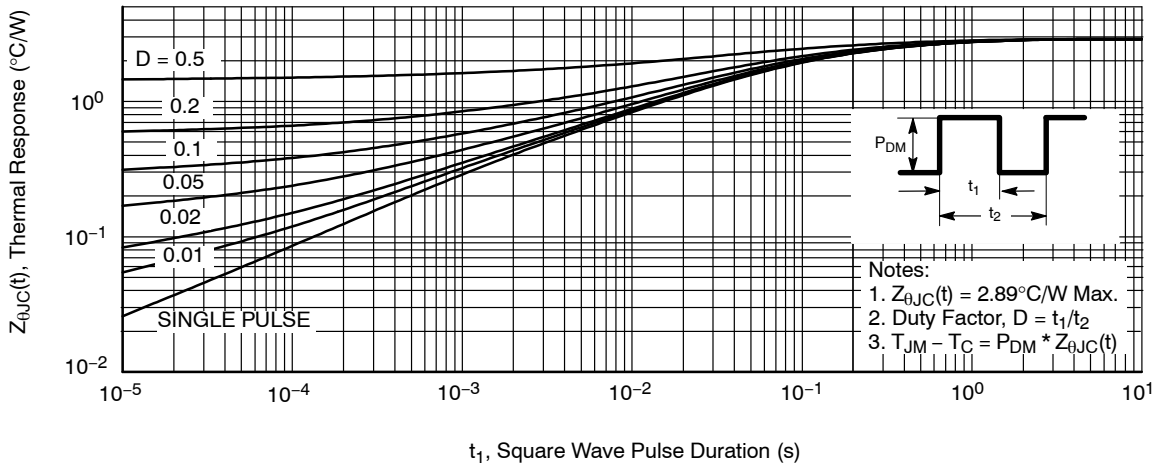


Figure 11.2. Transient Thermal Response Curve for FQPF19N20C

FQP19N20C, FQPF19N20C

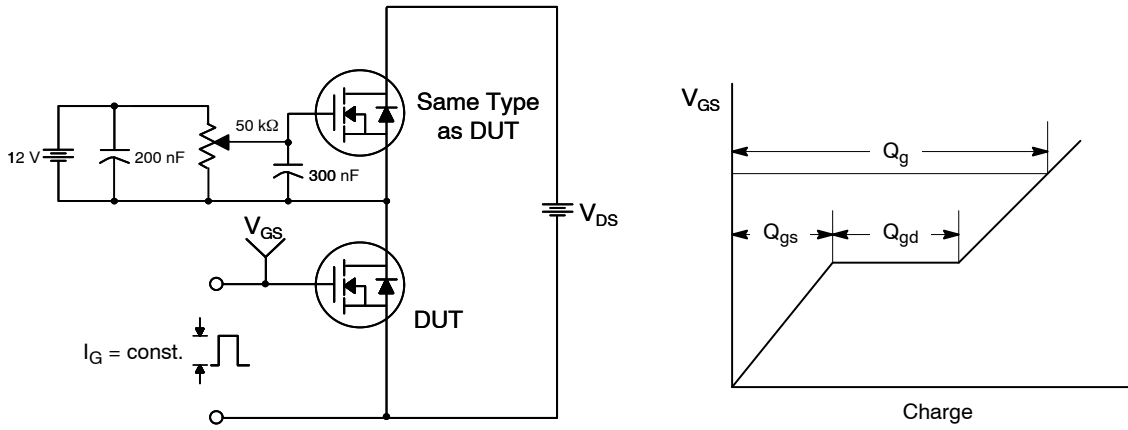


Figure 12. Gate Charge Test Circuit & Waveform

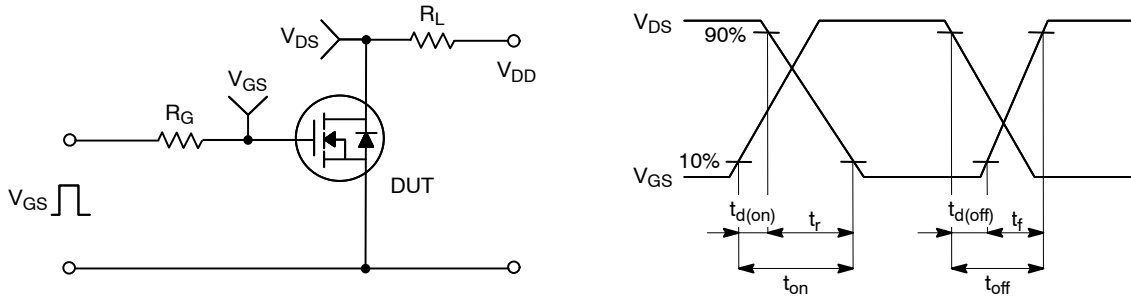


Figure 13. Resistive Switching Test Circuit & Waveforms

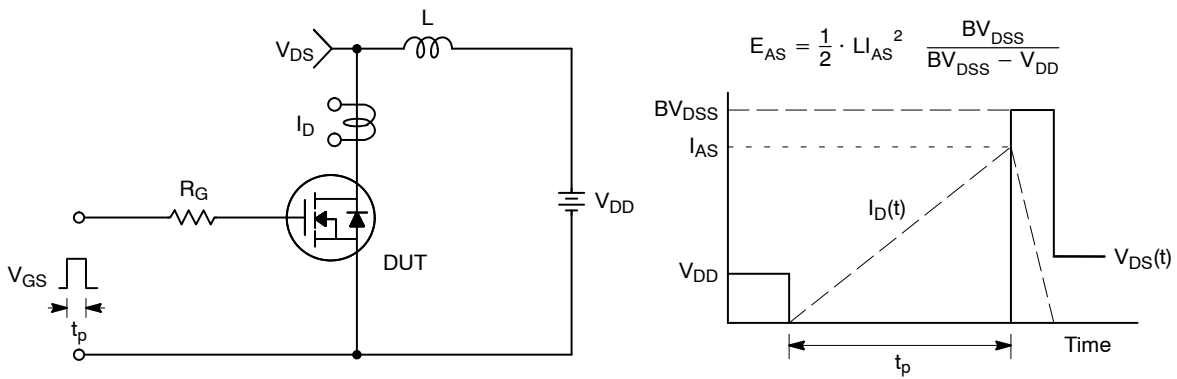


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

FQP19N20C, FQPF19N20C

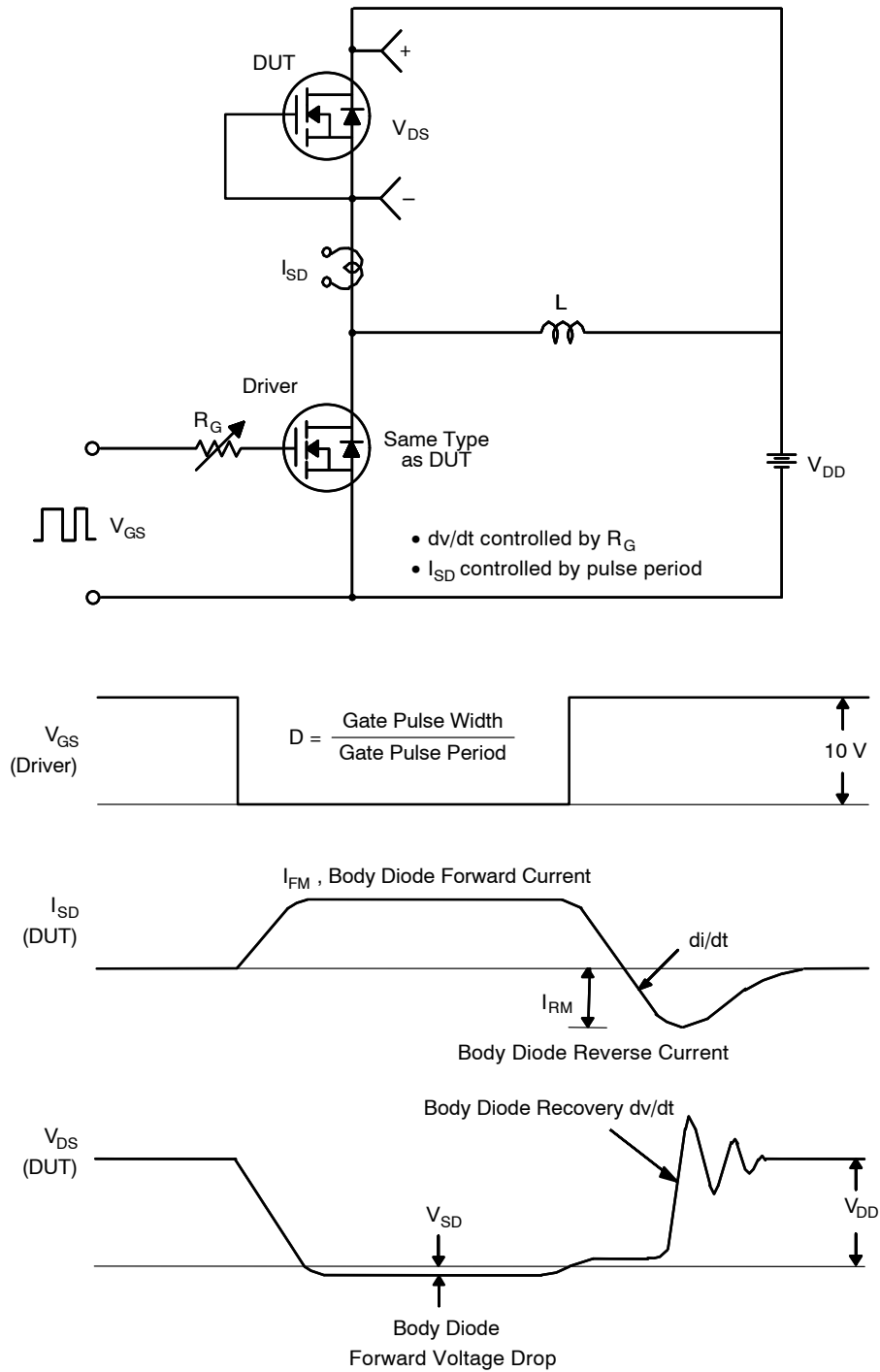


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

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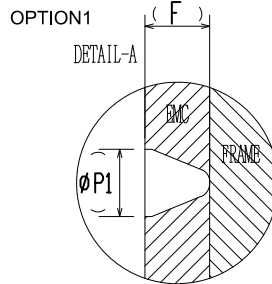
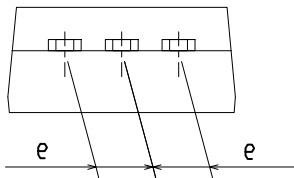


TO-220 Fullpack, 3-Lead / TO-220F-3SG CASE 221AT ISSUE B

DATE 19 JAN 2021



Scale 1:1



DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.50	4.70	4.90
A1	2.56	2.76	2.96
A2	2.34	2.54	2.74
b	0.70	0.80	0.90
b2	~	~	1.47
c	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.60	15.80	16.00
E	9.96	10.16	10.36
e	2.34	2.54	2.74
F	~	0.84	~
H1	6.48	6.68	6.88
L	12.78	12.98	13.18
L1	3.03	3.23	3.43
phi P	2.98	3.18	3.38
phi P1	~	1.00	~
Q	3.20	3.30	3.40

NOTES:

- A. DIMENSION AND TOLERANCE AS ASME Y14.5-2009
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUCTIONS.
- C. OPTION 1 - WITH SUPPORT PIN HOLE
OPTION 2 - NO SUPPORT PIN HOLE

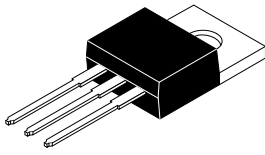
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MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

ON Semiconductor®



Scale 1:1

TO-220-3LD CASE 340AT ISSUE A

DATE 03 OCT 2017



- NOTES:
- A) REFERENCE JEDEC, TO-220, VARIATION AB
 - B) ALL DIMENSIONS ARE IN MILLIMETERS.
 - C) DIMENSIONS COMMON TO ALL PACKAGE SUPPLIERS EXCEPT WHERE NOTED [].
 - D) LOCATION OF MOLDED FEATURE MAY VARY (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF THE PACKAGE)
 - E) DOES NOT COMPLY JEDEC STANDARD VALUE.
 - F) "A1" DIMENSIONS AS BELOW:
 SINGLE GAUGE = 0.51 - 0.61
 DUAL GAUGE = 1.10 - 1.45
 - G) PRESENCE IS SUPPLIER DEPENDENT
 - H) SUPPLIER DEPENDENT MOLD LOCKING HOLES IN HEATSINK.

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