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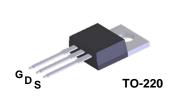
FQP8P10 P-Channel QFET[®] MOSFET -100 V, -8 A, 530 mΩ

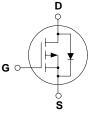
Description

This P-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor®'s proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- -8 A, -100 V, $R_{DS(on)}\text{=}530~m\Omega(Max.)$ @V_GS=-10 V, $I_D\text{=}\text{-}4$ A
- Low Gate Charge (Typ. 12 nC)
- Low Crss (Typ. 30 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating





Absolute Maximum Ratings T_c = 25°C unless otherwise noted

Symbol	Parameter		FQP8P10	Unit
V _{DSS}	Drain-Source Voltage		-100	V
I _D	Drain Current - Continuous ($T_C = 25^{\circ}C$) - Continuous ($T_C = 100^{\circ}C$)		-8.0	A
			-5.7	А
I _{DM}	Drain Current - Pulsed	(Note 1)	-32	А
V _{GSS}	Gate-Source Voltage		± 30	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	150	mJ
I _{AR}	Avalanche Current	(Note 1)	-8.0	А
E _{AR}	Repetitive Avalanche Energy	(Note 1)	6.5	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	-6.0	V/ns
PD	Power Dissipation ($T_C = 25^{\circ}C$)		65	W
	- Derate above 25°C		0.43	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C
ΤL	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300	°C

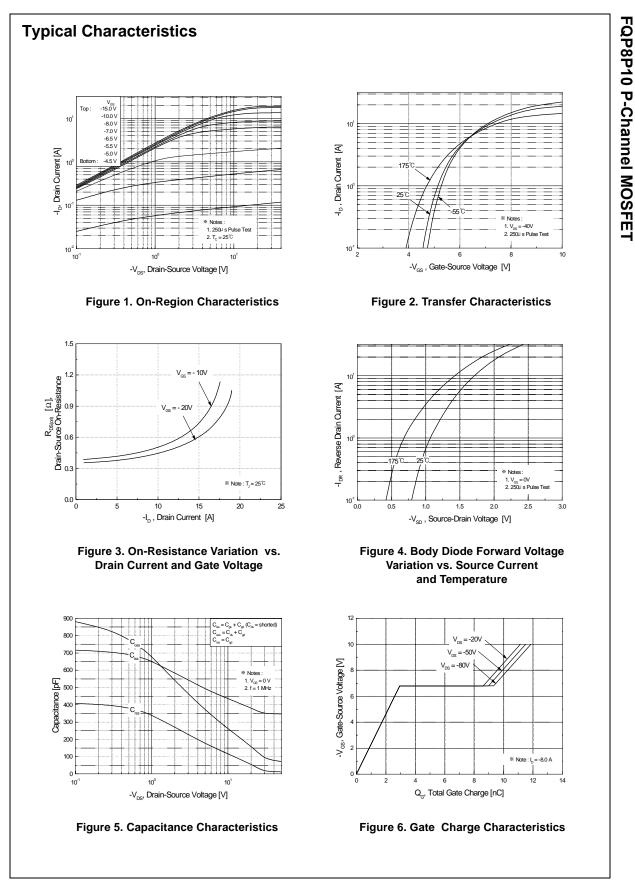
Thermal Characteristics

Symbol	Parameter	Тур	Max	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		2.31	°C/W
R _{0CS}	Thermal Resistance, Case-to-Sink	0.5		°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient		62.5	°C/W

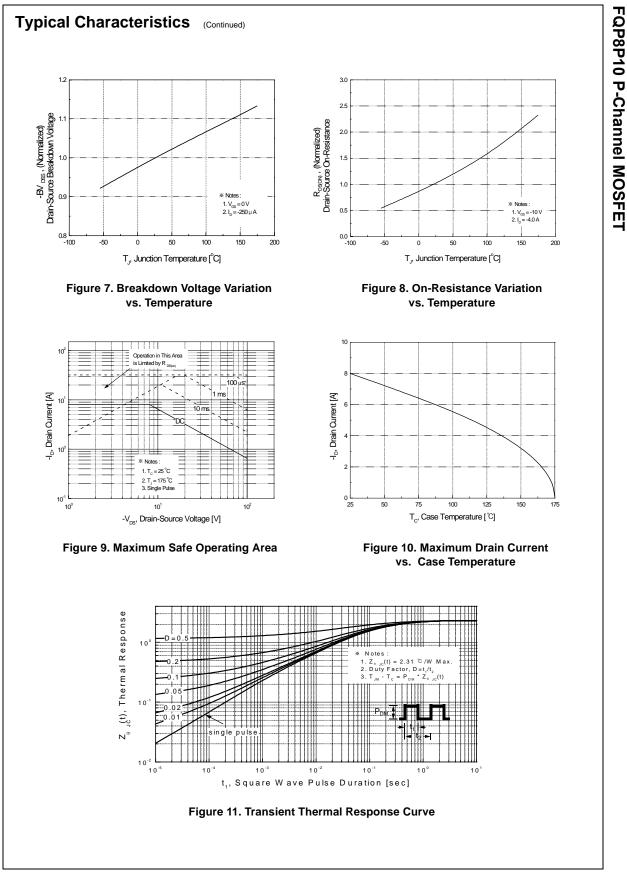
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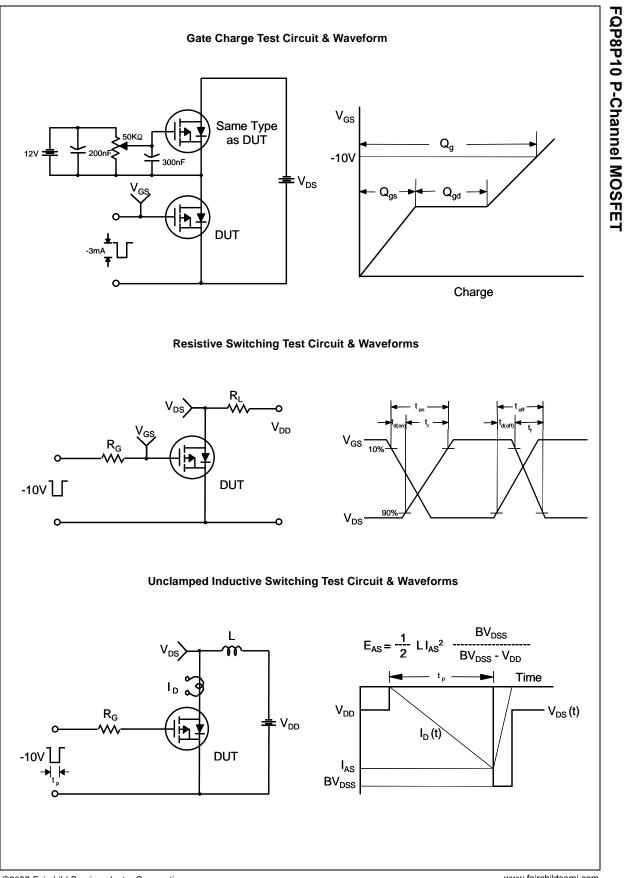
Symbol	Parameter	Test Conditions		Min	Тур	Max	Unit
Off Cha	racteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = -250 μA		-100			V
ΔBV _{DSS} ′ ΔTJ	Breakdown Voltage Temperature Coefficient	$I_D = -250 \mu\text{A}$, Referenced t	to 25°C		-0.1		V/°C
IDSS	Zero Gate Voltage Drain Current	V _{DS} = -100 V, V _{GS} = 0 V				-1	μA
		$V_{DS} = -80 \text{ V}, \text{ T}_{C} = 150^{\circ}\text{C}$			-	-10	μΑ
GSSF	Gate-Body Leakage Current, Forward	V_{GS} = -30 V, V_{DS} = 0 V				-100	nA
GSSR	Gate-Body Leakage Current, Reverse	$V_{GS} = 30 \text{ V}, V_{DS} = 0 \text{ V}$				100	nA
On Cha	racteristics						
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250 \mu A$		-2.0		-4.0	V
₹ _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = -10 V, I _D = -4.0 A			0.41	0.53	Ω
Ĵfs	Forward Transconductance	$V_{DS} = -40 \text{ V}, \text{ I}_{D} = -4.0 \text{ A}$	(Note 4)		4.3		S
_							
	ic Characteristics						_
C _{iss}	Input Capacitance	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V},$			360	470	pF
C _{oss}	Output Capacitance	f = 1.0 MHz			120	155	pF
C _{rss}	Reverse Transfer Capacitance				30	40	pF
d(on)	ng Characteristics Turn-On Delay Time	V _{DD} = -50 V, I _D = -8.0 A,			11	30	ns
r	Turn-On Rise Time	$R_G = 25 \Omega$			110	230	ns
d(off)	Turn-Off Delay Time	-			20	50	ns
f	Turn-Off Fall Time	(1	Note 4, 5)		35	80	ns
ე _g	Total Gate Charge	$V_{DS} = -80 \text{ V}, \text{ I}_{D} = -8.0 \text{ A},$			12	15	nC
ຊ _{gs}	Gate-Source Charge	V _{GS} = -10 V			3.0		nC
ე _{gd}	Gate-Drain Charge	(1	Note 4, 5)		6.4		nC
Drain_9	ource Diode Characteristics a	nd Maximum Patings					
s	Maximum Continuous Drain-Source Dic					-8.0	Α
SM	Maximum Pulsed Drain-Source Diode F	urce Diode Forward Current				-32	Α
/ _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 V, I_{S} = -8.0 A$				-4.0	V
rr	Reverse Recovery Time	V _{GS} = 0 V, I _S = -8.0 A,			98		ns
Q _{rr}	Reverse Recovery Charge	dI _F / dt = 100 A/μs	(Note 4)		0.35		μC
L = 3.5mH, I I _{SD} \leq -8.0A, Pulse Test :	ating : Pulse width limited by maximum junction temper $_{AS} = -8.0A$, $V_{DD} = -25V$, $R_G = 25 \Omega$, Starting $T_J = 25^{\circ}C$ di/dt $\leq 300A/\mu$ s, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^{\circ}C$ Pulse width $\leq 300\mu$ s, Duty cycle $\leq 2\%$ independent of operating temperature						

FQP8P10 P-Channel MOSFET



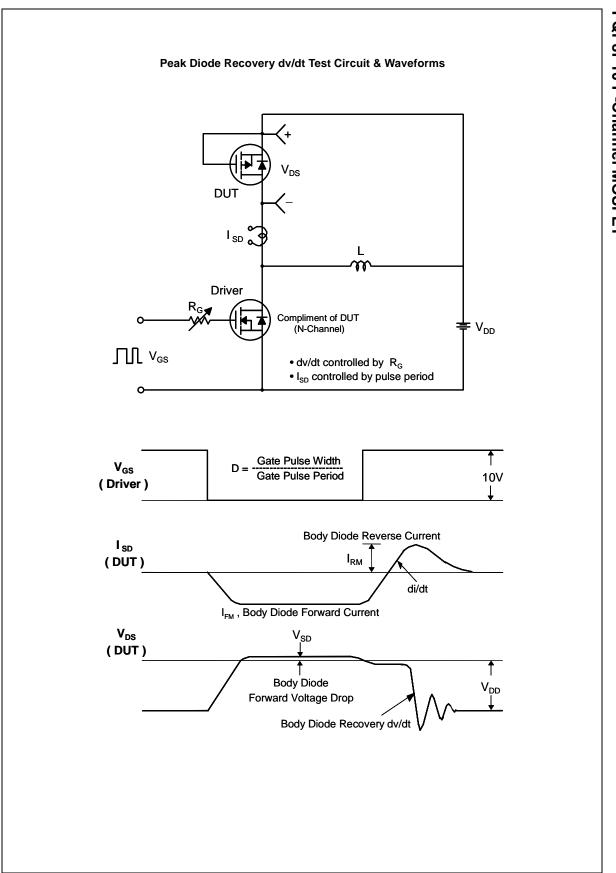
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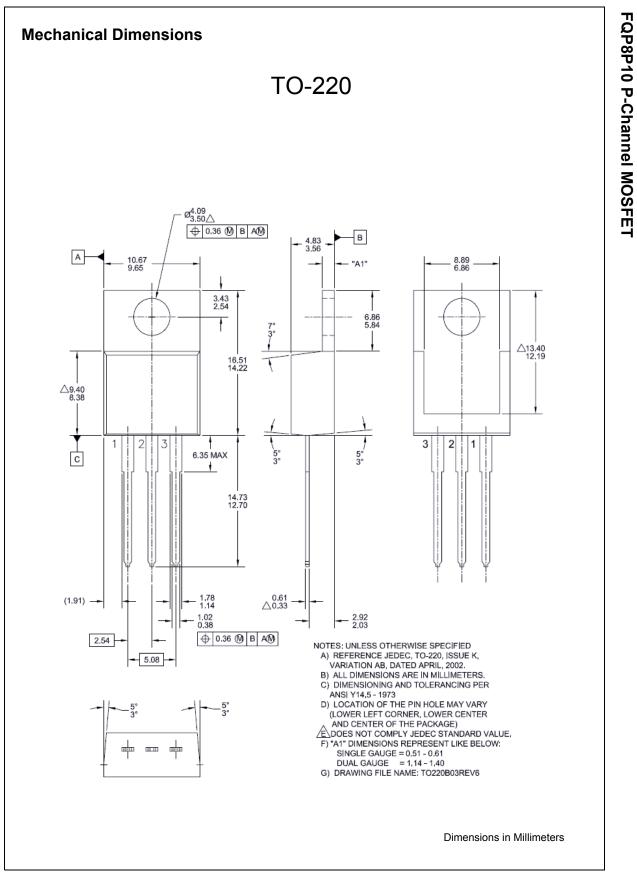




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