

Silicon Carbide (SiC) Schottky Diode - EliteSiC. 10 A, 650 V, D1, TO-220F-2L FFSPF1065A

Description

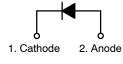
Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size and cost.

Features

- Max Junction Temperature 175°C
- Avalanche Rated 64 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery / No Forward Recovery
- This Device is Pb-Free and is RoHS Compliant

Applications

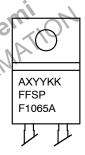
- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits



Schottky Diode



MARKING DIAGRAM



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ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, Unless otherwise specified)

Symbol	Parameter	FFSPF1065A	Unit	
V_{RRM}	Peak Repetitive Reverse Voltage		650	V
E _{AS}	Single Pulse Avalanche Energy (Note 1)		64	mJ
l _F	Continuous Rectified Forward Current	@Tc < 110°C	10	Α
		@ Tc < 135°C	7.2	
I _{F, Max}	Non-Repetitive Peak Forward Surge Current	Tc = 25°C, 10 μs	720	Α
		Tc = 150°C, 10 μs	680	Α
I _{F, SM}	Non-Repetitive Forward Surge Current	Half-Sine Pulse, t _P = 8.3 ms	56	Α
I _{F, RM}	Repetitive Forward Surge Current	Half-Sine Pulse, t _P = 8.3 ms	28	Α
P _{tot}	Power Dissipation	Tc = 25°C	38	W
		Tc = 150°C	6.4	W
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	√ °C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. E_{AS} of 64 mJ is based on starting $T_J = 25^{\circ}C$, L = 0.5 mH, $I_{AS} = 16$ A, V = 50 V.

THERMAL CHARACTERISTICS

Symbol	Parameter	Ratings	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	3.9	°C/W

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
FFSPF1065A	FFSPF1065A	TO-220 FP / TO-220F-2FS	Tube	N/A	N/A	50 Units

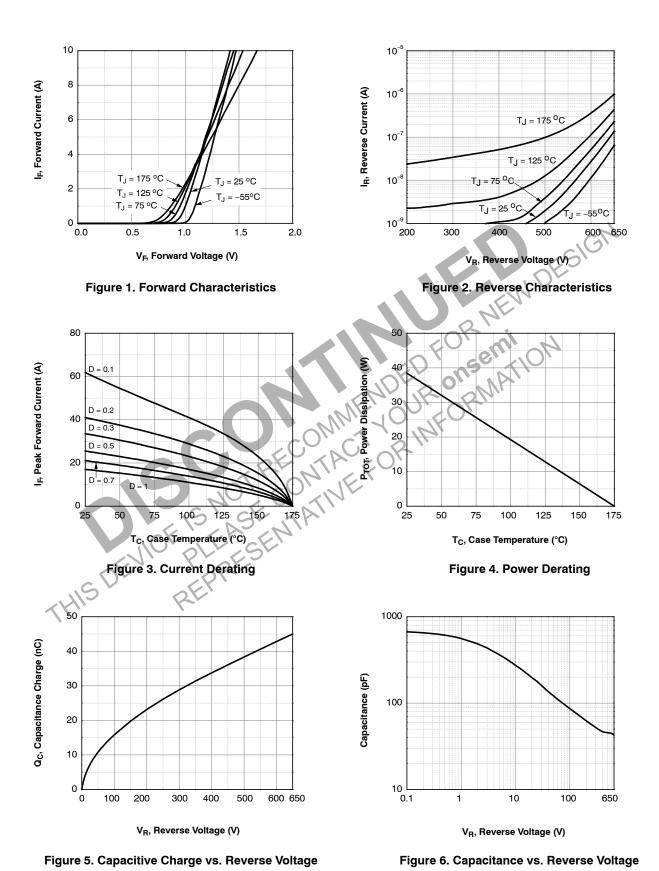
ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
V _F	Forward Voltage	IF = 10 A, Tc = 25°C	-	1.50	1.75	V
	CE ERCE	IF= 10 A, Tc = 125°C	_	1.60	2.0	
	ENIO PLACES	IF = 10 A, Tc = 175°C	_	1.72	2.4	
I _R	Reverse Current	VR = 650 V, Tc = 25°C	_	-	200	μΑ
-14	D RE	VR = 650 V, Tc = 125°C	_	_	400	
71.	*	VR = 650 V, Tc = 175°C	_	_	600	
Q _C	Total Capacitance Charge	V = 400 V	_	34	ı	nC
С	Total Capacitance	VR = 1 V, f = 100 kHz	_	575	_	pF
		VR = 200 V, f = 100 kHz	_	62	_	
		VR = 400 V, f = 100 kHz	_	47	1	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

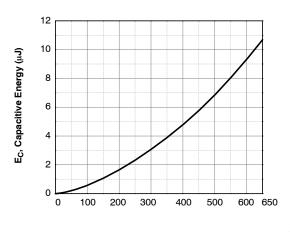
(T_J = 25°C UNLESS OTHERWISE NOTED)



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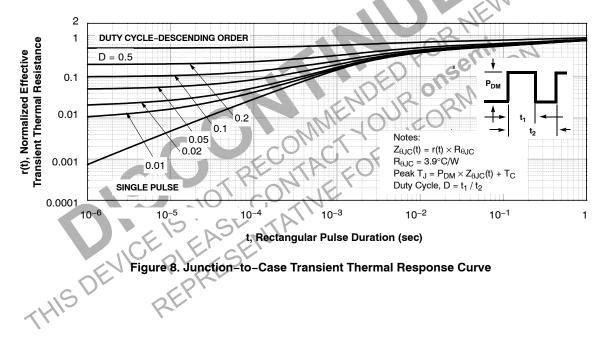
TYPICAL CHARACTERISTICS (CONTINUED)

(T_J = 25°C UNLESS OTHERWISE NOTED)



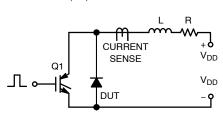
V_R, Reverse Voltage (V)

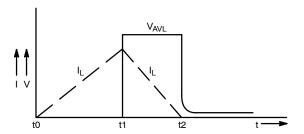
Figure 7. Capacitance Stored Energy



TEST CIRCUIT AND WAVEFORMS

$$\label{eq:local_local_local} \begin{split} L &= 0.5 \text{ mH} \\ R &< 0.1 \ \Omega \\ V_{DD} &= 50 \ V \\ EAVL &= 1/2 Ll2 \left[V_{R(AVL)} / (V_{R(AVL)} - V_{DD}) \right] \\ Q1 &= IGBT \ (BV_{CES} > DUT \ V_{R(AVL)}) \end{split}$$





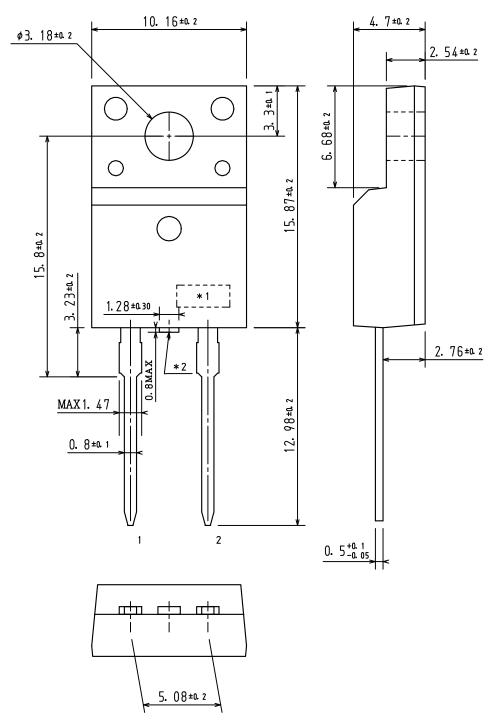
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TO-220 Fullpack, 2-Lead / TO-220F-2FS CASE 221AS ISSUE O

DATE 29 FEB 2012



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DESCRIPTION:	TO-220 FULLPACK, 2-LEAD / TO-220F-2FS		PAGE 1 OF 1	

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