STEALTH Diode 30 A, 300 V

FFH30US30DN

Description

The FFH30US30DN is a STEALTH $^{\text{m}}$ diode optimized for low loss performance in output rectification. The STEALTH family exhibits low reverse recovery current ($I_{\text{RM}(\text{REC})}$), low V_F and soft recovery under typical operating conditions.

This device is intended for use as an output rectification diode in Telecom power supplies and other power switching applications. Lower V_F and $I_{RM(REC)}$ reduces diode losses.

300 V

20 mJ

Formerly developmental type TA49449.

Features

- Soft Recovery $t_b/t_a > 0.45$
- Fast Recovery trr < 50 ns
- High Operating Temperature 175°C
- Reverse Voltage
- Avalanche Energy Rating
- This is a Pb–Free Device

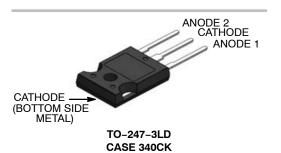
Applications

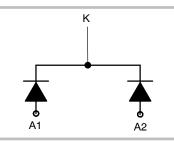
- Switch Mode Power Supplies
- Power Factor Correction
- Uninterruptable Power Supplies
- Motor Drives
- Welders



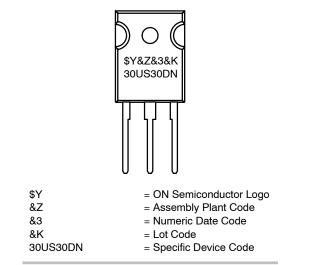
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MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

DEVICE MAXIMUM RATINGS (per leg) ($T_C = 25^{\circ}C$ unless otherwise noted)

Rating	Symbol	Value	Unit	
Repetitive Peak Reverse Voltage	V _{RRM}	300	V	
Working Peak Reverse Voltage	V _{RWM}	300	V	
DC Blocking Voltage	V _R	300	V	
Average Rectified Forward Current ($T_C = 160^{\circ}C$)	I _{F(AV)}	30	А	
Total Device Current (Both Legs)		60		
Repetitive Peak Surge Current (20 kHz Square Wave)	I _{FRM}	70	А	
Non-repetitive Peak Surge Current (Halfwave 1 Phase 60 Hz)	I _{FSM}	325	А	
Power Dissipation	PD	230	W	
Avalanche Energy (1 A, 40 mH)	E _{AVL}	20	mJ	
Operating and Storage Temperature Range	T _J , T _{STG}	–55 to 175	°C	
Maximum Temperature for Soldering Leads at 0.063in (1.6 mm) from Case for 10 s Package Body for 10 s, See Application Note AN–7528	Т _L Т _{РКG}	300 260	°C ℃	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS (T_C = 25° C unless otherwise noted)

Parameter	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	0.65	°C/W
Thermal Resistance, Junction to Ambient	R_{\thetaJA}	30	°C/W

PACKAGE MARKING AND ORDERING INFORMATION

Device	Device Marking	Package	Tape Width	Quantity
FFH30US30DN	30US30DN	TO-247-3LD	N/A	30

ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise noted)

Parameter	Symbol	Test Co	ndition	Min	Тур	Max	Unit
OFF STATE CHARACTERISTICS							
Instantaneous Reverse Current	I _R	V _R = 300 V	$T_{C} = 25^{\circ}C$	-	-	100	μA
			T _C = 125°C	-	-	1	mA
ON STATE CHARACTERISTICS							
Instantaneous Forward Voltage	V _F	I _F = 30 A	$T_{C} = 25^{\circ}C$	-	0.93	1.0	V
			T _C = 125°C	-	0.8	0.87	
DYNAMIC CHARACTERISTICS						-	
Junction Capacitance	CJ	V _R = 10 V, I _F = 0 A		-	410	_	pF
SWITCHING CHARACTERISTICS						-	
Reverse Recovery Time	t _{rr}	t_{rr} I _F = 1 A, dI _F /dt = 100 A/µs, V _R = 15 V	-	29	50	ns	
		I_F = 30 A, dI _F /dt = 100 A/µs, V _R = 15 V		-	32	55	
Reverse Recovery Time	t _{rr}			-	46	-	ns
Maximum Reverse Recovery Current	I _{RM(REC)}			-	5.3	_	Α
Reverse Recovered Charge	Q _{RR}			-	140	_	nC
Reverse Recovery Time	t _{rr}		-	77	_	ns	
Softness Factor (t _b /t _a)	S		-	0.45	-	-	
Maximum Reverse Recovery Current	I _{RM(REC)}		-	9	_	Α	
Reverse Recovered Charge	Q _{RR}		-	400	-	nC	
Reverse Recovery Time	t _{rr}	I _F = 30 A	-	54	-	ns	
Softness Factor (t _b /t _a)	S	dl _F /dt = 1000 A/μs V _R = 195 V, T _C = 125°C		-	0.49	-	-
Maximum Reverse Recovery Current	I _{RM(REC)}			-	32	-	Α
Reverse Recovered Charge	Q _{BB}			-	930	-	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL PERFORMANCE CURVES (per leg)

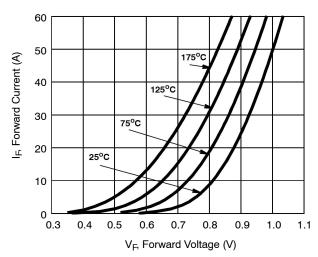


Figure 1. Forward Current vs. Forward Voltage

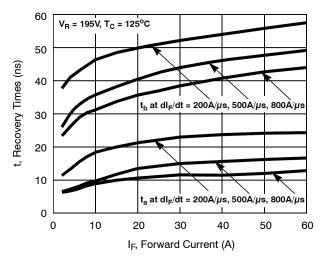
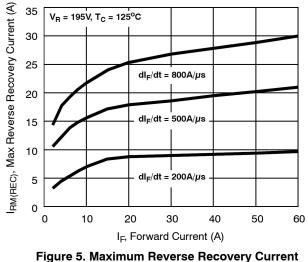


Figure 3. t_a and t_b Curves vs. Forward Current



vs. Forward Current

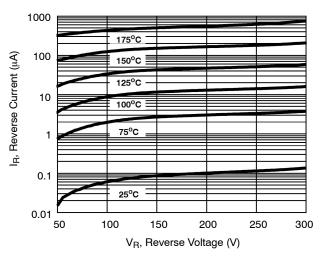


Figure 2. Reverse Current vs. Reverse Voltage

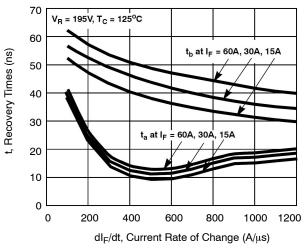
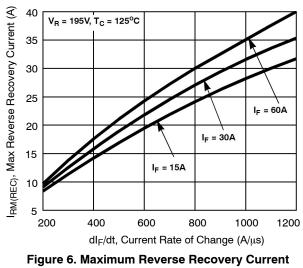


Figure 4. t_a and t_b Curves vs. dI_F/dt



vs. dl_F/dt

TYPICAL PERFORMANCE CHARACTERISTICS (per leg) (continued)

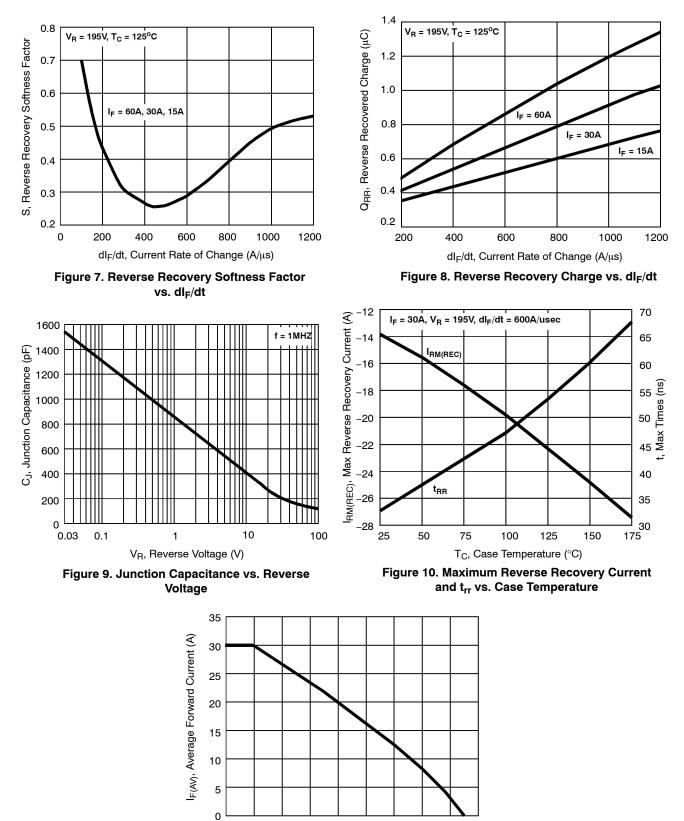
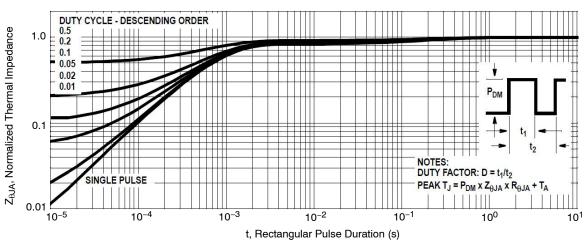




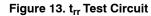
Figure 11. DC Current Derating Curve



TYPICAL PERFORMANCE CHARACTERISTICS (per leg) (continued)



TEST CIRCUITS AND WAVEFORMS



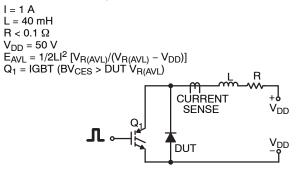
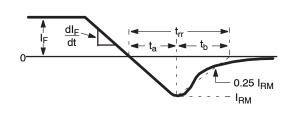


Figure 15. Avalanche Energy Test Circuit





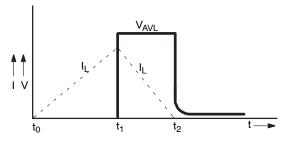


Figure 16. Avalanche Current and Voltage Waveforms

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TO-247-3LD SHORT LEAD CASE 340CK **ISSUE A**

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