# onsemi

## **MOSFET** – Dual N-Channel, Logic Level PWM Optimized POWERTRENCH<sup>®</sup>

### 9.4 A, 20 V

## FDS6898A

#### **General Description**

These N-Channel Logic Level MOSFETs are produced using **onsemi**'s advanced POWERTRENCH process that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

These devices are well suited for low voltage and battery powered applications where low in-line power loss and fast switching are required.

#### Features

- 9.4 A, 20 V
  - $R_{DS(on)} = 14 \text{ m}\Omega \text{ at } V_{GS} = 4.5 \text{ V}$
  - $R_{DS(on)} = 18 \text{ m}\Omega \text{ at } V_{GS} = 2.5 \text{ V}$
- Low Gate Charge (16 nC typical)

MOSFET MAXIMUM RATINGS

- High Performance Trench Technology for Extremely Low RDS(on)
- High Power and Current Handling Capability
- This Device is Pb-Free, Halide Free and is RoHS Compliant

(T <sub>A</sub> = 25°C	unless otherwise noted)		
Symbol	Parameter	Ratings	Unit
V <sub>DSS</sub>	Drain-Source Voltage	20	V
V <sub>GSS</sub>	Gate-Source Voltage	±12	V
۱ <sub>D</sub>	Drain Current – Continuous (Note 1a) – Pulsed	9.4 38	A
PD	Power Dissipation for Dual Operation	2	W
	Power Dissipation for Single Operation (Note 1a) (Note 1b) (Note 1c)	1.6 1 0.9	
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +150	°C

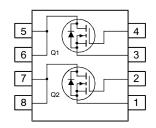
## Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

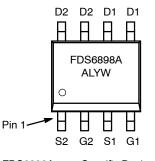
Symbol	Parameter	Ratings	Unit
$R_{\thetaJA}$	Thermal Resistance, Junction-to-Ambient (Note 1a)	78	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case (Note 1)	40	°C/W



SOIC8 CASE 751EB



MARKING DIAGRAM



FDS6898A	= Specific Device Code
А	= Assembly Site
L	= Wafer Lot Number
YW	= Assembly Start Week

#### ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
FDS6898A	SOIC8 (Pb–Free/ Halide Free)	2500 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, <u>BRD8011/D</u>.

#### ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit		
OFF CHARACTERISTICS								
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ = 0 V, $I_D$ = 250 $\mu$ A	20	-	-	V		
$\frac{\Delta \text{BV}_{\text{DSS}}}{\Delta \text{T}_{\text{J}}}$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, Referenced to 25°C	-	21	-	mV/°C		
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 16 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	1	μA		
I <sub>GSSF</sub>	Gate-Body Leakage, Forward	$V_{GS}$ = 12 V, $V_{DS}$ = 0 V	-	-	100	nA		
I <sub>GSSR</sub>	Gate-Body Leakage, Reverse	$V_{GS} = -12 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$	-	-	-100	nA		
ON CHARAC	TERISTICS (Note 2)	-	-	-	-	-		

V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	0.5	1	1.5	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, Referenced to $25^{\circ}$ C	-	-3.5	-	mV/°C
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	$V_{GS}$ = 4.5 V, I <sub>D</sub> = 9.4 A	-	10	14	mΩ
		$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 8.3 \text{ A}$	-	13	18	
		$V_{GS}$ = 4.5 V, $I_D$ = 9.4 A, $T_J$ = 125°C	-	14	21	
I <sub>D(on)</sub>	On-State Drain Current	$V_{GS}$ = 4.5 V, $V_{DS}$ = 5 V	19	-	_	А
9 <sub>FS</sub>	Forward Transconductance	$V_{DS} = 5 V, I_D = 9.4 A$	-	47	-	S

#### DYNAMIC CHARACTERISTICS

C <sub>iss</sub>	Input Capacitance	$V_{DS}$ = 10 V, $V_{GS}$ = 0 V, f = 1.0 MHz	-	1821	-	pF
C <sub>oss</sub>	Output Capacitance		-	440	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	208	-	

#### SWITCHING CHARACTERISTICS (Note 2)

t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ A},$	-	10	20	ns
t <sub>r</sub>	Rise Time	$V_{GS} = 4.5 \text{ V},        $	-	15	27	
t <sub>d(off)</sub>	Turn-Off Delay Time		-	34	55	
t <sub>f</sub>	Fall Time		-	16	29	
Qg	Total Gate Charge	$V_{DS}$ = 10 V, $V_{GS}$ = 4.5 V, $I_{D}$ = 9.4 A	-	16	23	nC
Q <sub>gs</sub>	Gate-Source Charge		-	3	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	4	-	

#### DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

ا <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current		-	-	1.3	А
$V_{SD}$	Drain-Source Diode Forward Voltage	$V_{GS}$ = 0 V, I <sub>S</sub> = 1.3 A (Note 2)	-	0.7	1.2	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### NOTES:

1. R<sub>0JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.  $R_{\theta,JC}$  is guaranteed by design while  $R_{\theta CA}$  is determined by the user's board design.



a) 78°C/W when mounted on a 0.5  $\mbox{in}^2$ pad of 2 oz. copper.



b) 125°C/W when mounted on a 0.02 in<sup>2</sup> pad of 2 oz. copper.

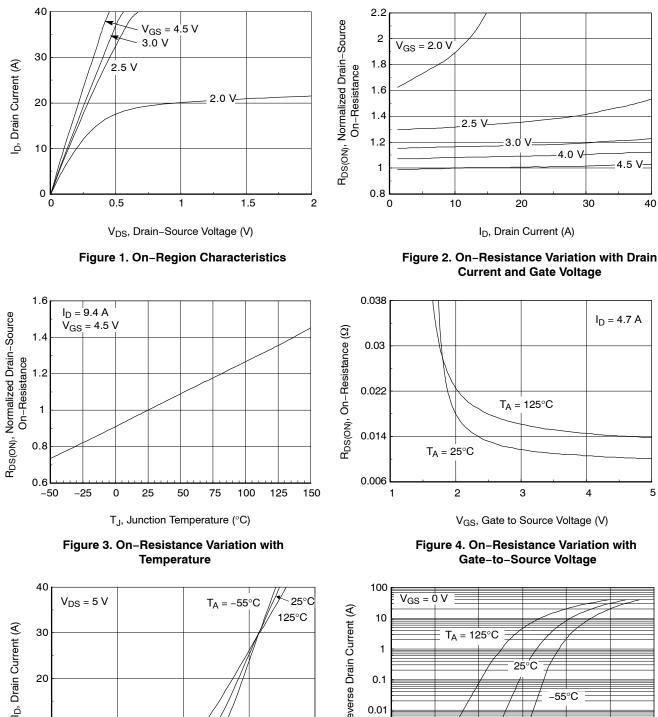


c) 135°C/W when mounted on a minimum mounting pad.

Scale 1:1 on letter size paper

2. Pulse Test: Pulse Width < 300 µs, Duty Cycle < 2.0%

#### **TYPICAL CHARACTERISTICS**



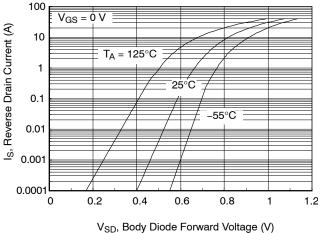


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature

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2.5

10

0

0.5

1

1.5

V<sub>GS</sub>, Gate to Source Voltage (V)

Figure 5. Transfer Characteristics

2

#### TYPICAL CHARACTERISTICS (continued)

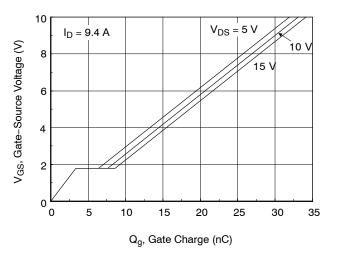
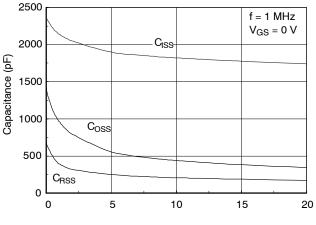
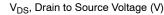


Figure 7. Gate Charge Characteristics







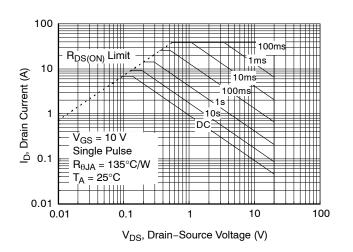
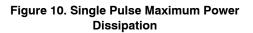


Figure 9. Maximum Safe Operating Area

40 Single Pulse Peak Transient Power (W)  $R_{\theta JA} = 135^{\circ}C/W$ T<sub>A</sub> = 25°C 30 20 10 P(pk), 0 0.1 0.001 0.01 1 10 100 t<sub>1</sub>, Time (s)



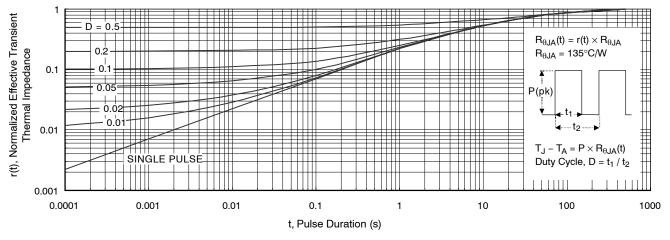


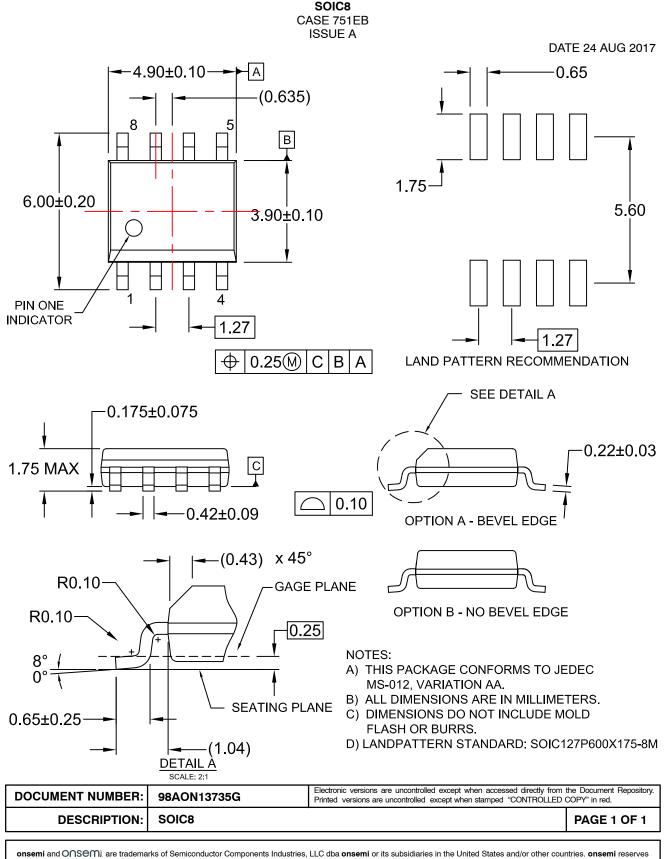
Figure 11. Transient Thermal Response Curve

Thermal characterization performed using the conditions described in Note 1c. Transient thermal response will change depending on the circuit board design.

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