

# MOSFET – N-Channel, UniFET™ II

**600 V, 17 A, 340 mΩ**

## FDPF17N60NT

### Description

UniFET II MOSFET is onsemi's high voltage MOSFET family based on advanced planar stripe and DMOS technology. This advanced MOSFET family has the smallest on-state resistance among the planar MOSFET, and also provides superior switching performance and higher avalanche energy strength. In addition, internal gate-source ESD diode allows UniFET II MOSFET to withstand over 2 kV HBM surge stress. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.

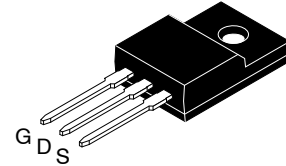
### Features

- $R_{DS(on)} = 290 \text{ m}\Omega$  (Typ.) @  $V_{GS} = 10 \text{ V}$ ,  $I_D = 8.5 \text{ A}$
- Low Gate Charge (Typ. 48 nC)
- Low  $C_{rss}$  (Typ. 23 pF)
- 100% Avalanche Tested
- Improved dv/dt Capability
- RoHS Compliant

### Applications

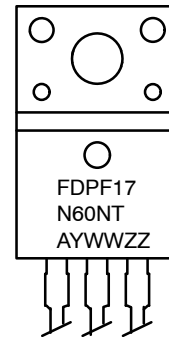
- LCD/LED/PDP TV
- Lighting
- Uninterruptible Power Supply
- AC-DC Power Supply

$V_{DS}$	$R_{DS(on)}$ MAX	$I_D$ MAX
600 V	340 mΩ @ 10 V	17 A



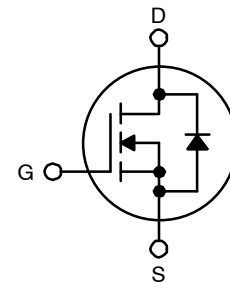
TO-220 Fullpack, 3-Lead / TO-220F-3SG  
CASE 221AT

### MARKING DIAGRAM



FDPF17N60NT = Specific Device Code  
A = Assembly Location  
YWW = Date Code (Year & Week)  
ZZ = Assembly Lot

### N-CHANNEL MOSFET



### ORDERING INFORMATION

Part Number	Package	Shipping
FDPF17N60NT	TO-220F	1000 Units / Tube

# FDPF17N60NT

## MOSFET MAXIMUM RATINGS (T<sub>C</sub> = 25°C, unless otherwise noted\*)

Symbol	Parameter		FDPF17N60NT	Unit
V <sub>DSS</sub>	Drain to Source Voltage		600	V
V <sub>GSS</sub>	Gate to Source Voltage		±30	V
I <sub>D</sub>	Drain Current	– Continuous (T <sub>C</sub> = 25°C)	17*	A
		– Continuous (T <sub>C</sub> = 100°C)	10.2*	
I <sub>DM</sub>	Drain Current	– Pulsed (Note 1)	68*	A
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)		838	mJ
I <sub>AR</sub>	Avalanche Current (Note 1)		17	A
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)		24.5	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		10	V/ns
P <sub>D</sub>	Power Dissipation	(T <sub>C</sub> = 25°C)	62.5	W
		– Derate Above 25°C	0.5	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		–55 to +150	°C
T <sub>L</sub>	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse-width limited by maximum junction temperature.
2. L = 5.8 mH, I<sub>AS</sub> = 17 A, V<sub>DD</sub> = 50 V, R<sub>G</sub> = 25 Ω, starting T<sub>J</sub> = 25°C.
3. I<sub>SD</sub> ≤ 17 A, di/dt ≤ 200 A/μs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, starting T<sub>J</sub> = 25°C.

## THERMAL CHARACTERISTICS

Symbol	Parameter	FDPF17N60NT	Unit
R <sub>θJC</sub>	Thermal Resistance, Junction to Case, Max.	2.0	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction to Ambient, Max.	62.5	

# FDPF17N60NT

## ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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### OFF CHARACTERISTICS

BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	I <sub>D</sub> = 250 $\mu$ A, V <sub>GS</sub> = 0 V, T <sub>C</sub> = 25°C	600	–	–	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250 $\mu$ A, Referenced to 25°C	–	0.8	–	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	–	–	1	$\mu$ A
		V <sub>DS</sub> = 480 V, V <sub>GS</sub> = 0 V, T <sub>C</sub> = 150°C	–	–	10	
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>GS</sub> = $\pm$ 30 V, V <sub>DS</sub> = 0 V	–	–	$\pm$ 100	nA

### ON CHARACTERISTICS

V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 250 $\mu$ A	3.0	–	5.0	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 8.5 A	–	0.29	0.34	$\Omega$
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 8.5 A	–	21	–	S

### DYNAMIC CHARACTERISTICS

C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	–	2285	3040	pF
C <sub>oss</sub>	Output Capacitance		–	310	410	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		–	23	35	pF
Q <sub>g(tot)</sub>	Total Gate Charge at 10 V	V <sub>DS</sub> = 480 V, I <sub>D</sub> = 17 A, V <sub>GS</sub> = 10 V (Note 4)	–	48	65	nC
Q <sub>gs</sub>	Gate to Source Gate Charge		–	13	–	nC
Q <sub>gd</sub>	Gate to Drain “Miller” Charge		–	20	–	nC

### SWITCHING CHARACTERISTICS

t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 300 V, I <sub>D</sub> = 17 A, V <sub>GS</sub> = 10 V, R <sub>G</sub> = 25 $\Omega$ (Note 4)	–	48	106	ns
t <sub>r</sub>	Turn-On Rise Time		–	79	168	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		–	128	266	ns
t <sub>f</sub>	Turn-Off Fall Time		–	62	134	ns

### DRAIN-SOURCE DIODE CHARACTERISTICS

I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		–	–	74	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		–	–	68	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 17 A	–	–	1.4	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 17 A, dI <sub>F</sub> /dt = 100 A/μs	–	575	–	ns
Q <sub>rr</sub>	Reverse Recovery Charge		–	7.2	–	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics

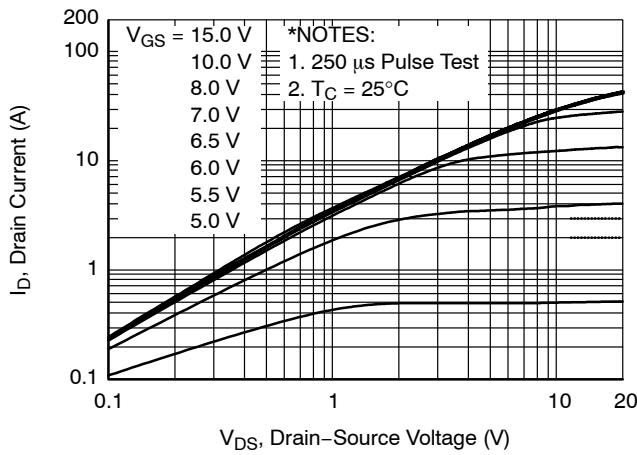


Figure 1. On-Region Characteristics

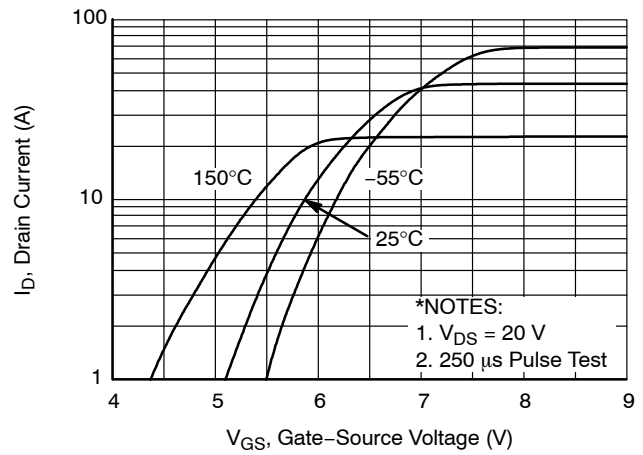


Figure 2. Transfer Characteristics

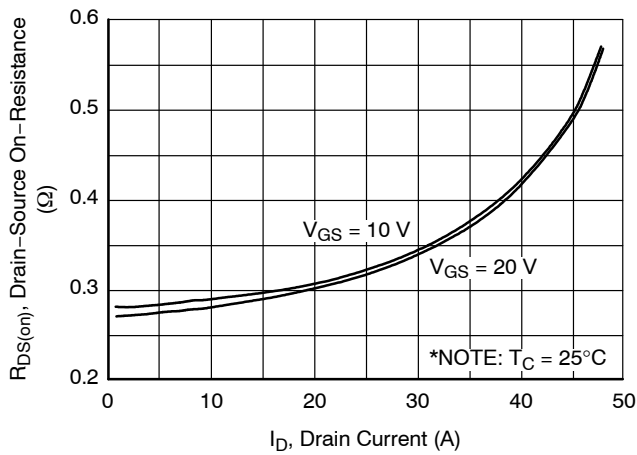


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

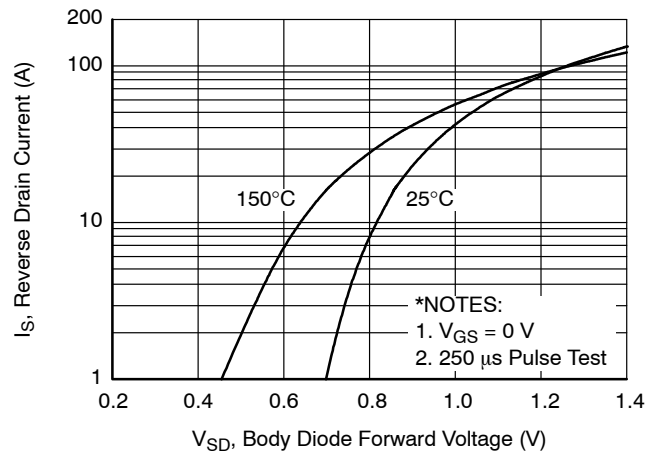


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

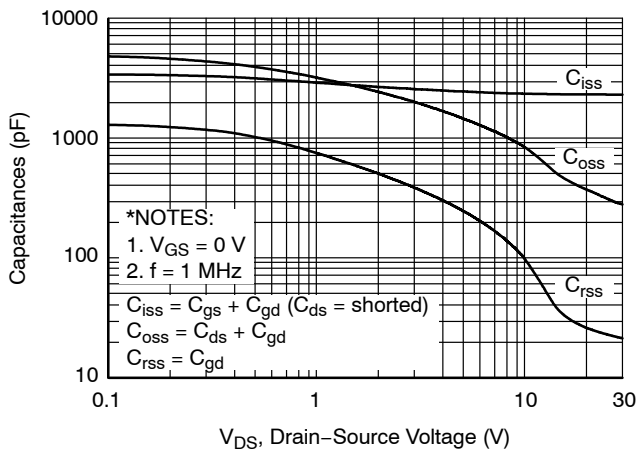


Figure 5. Capacitance Characteristics

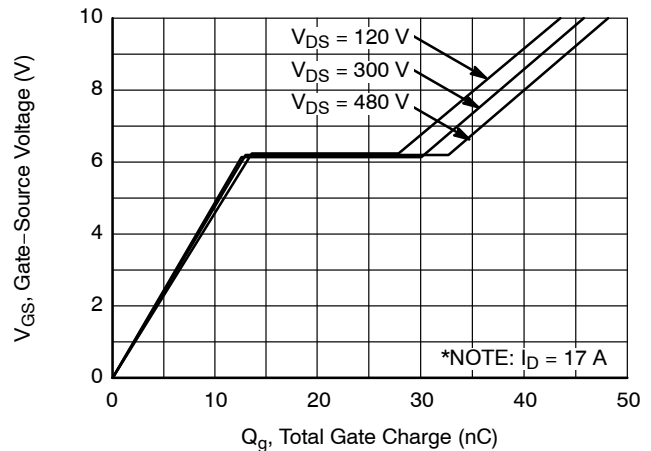
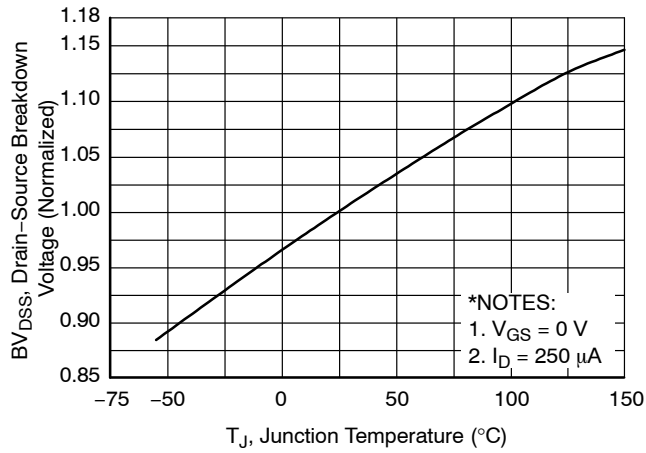


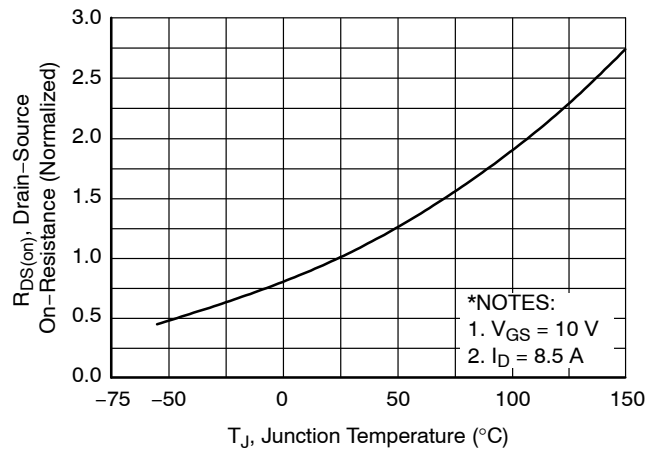
Figure 6. Gate Charge Characteristics

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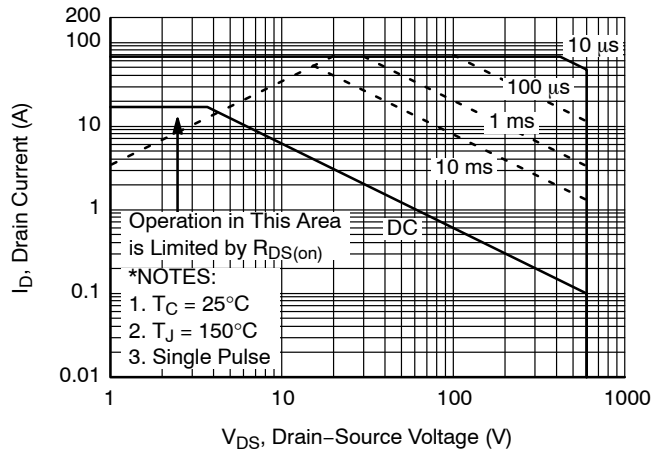
## Typical Performance Characteristics (Continued)



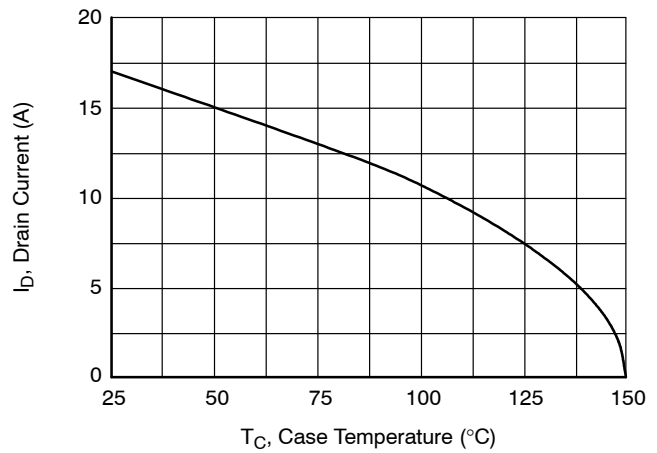
**Figure 7. Breakdown Voltage Variation vs. Temperature**



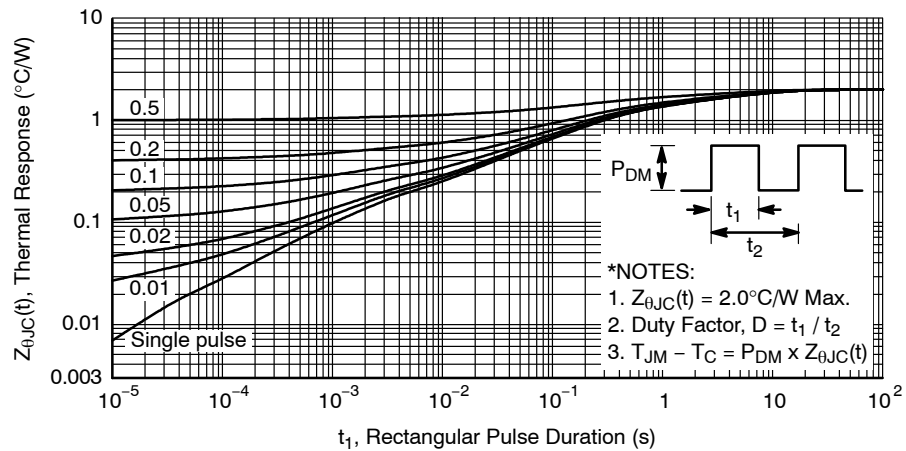
**Figure 8. On-Resistance Variation vs. Temperature**



**Figure 9. Maximum Safe Operating Area**



**Figure 10. Maximum Drain Current vs. Case Temperature**



**Figure 11. Transient Thermal Response Curve**

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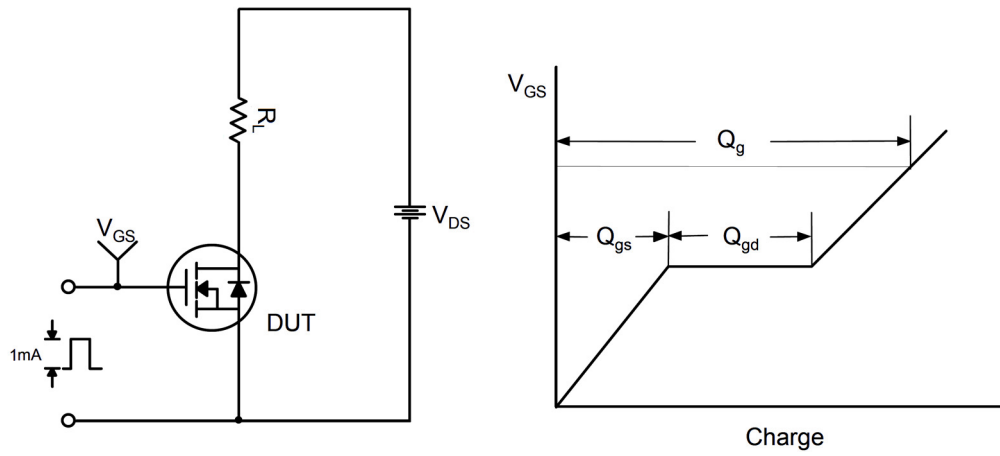


Figure 12. Gate Charge Test Circuit & Waveform

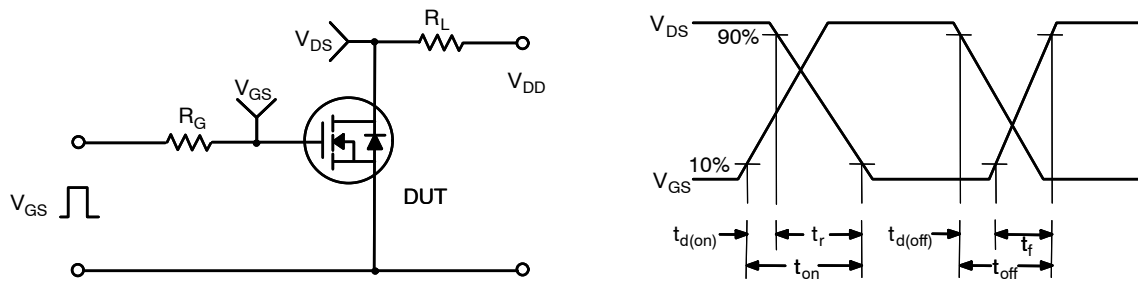


Figure 13. Resistive Switching Test Circuit & Waveforms

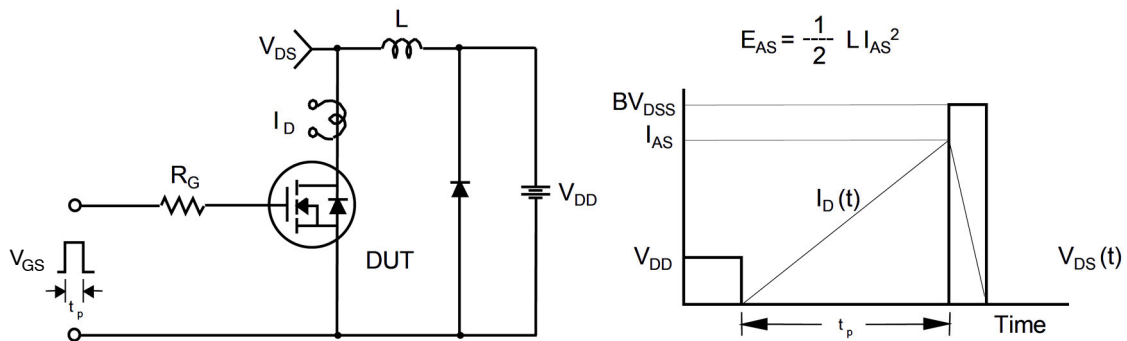
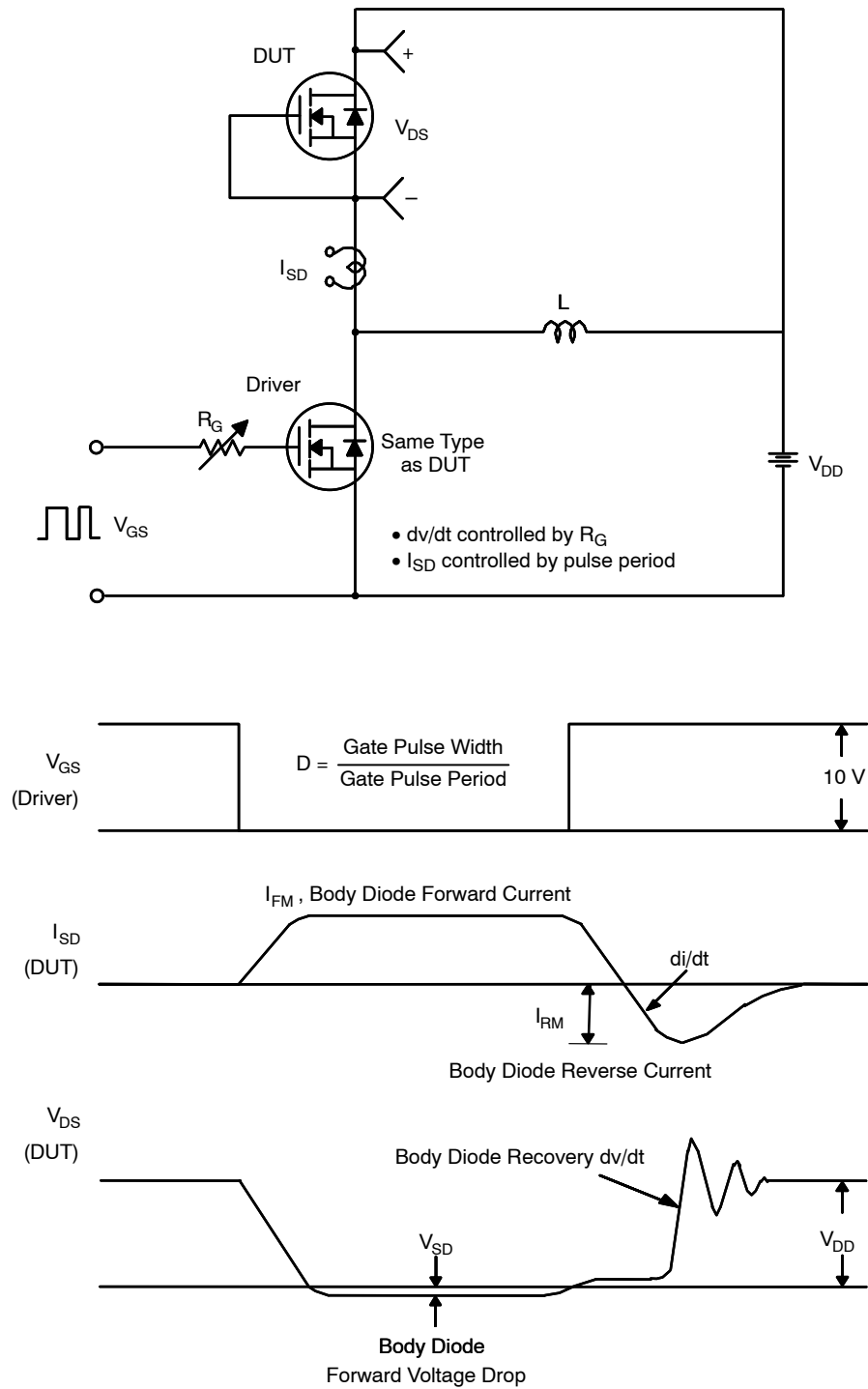


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

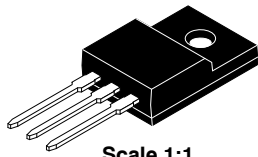
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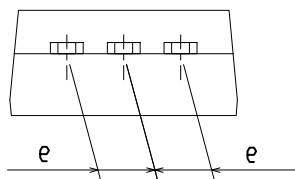
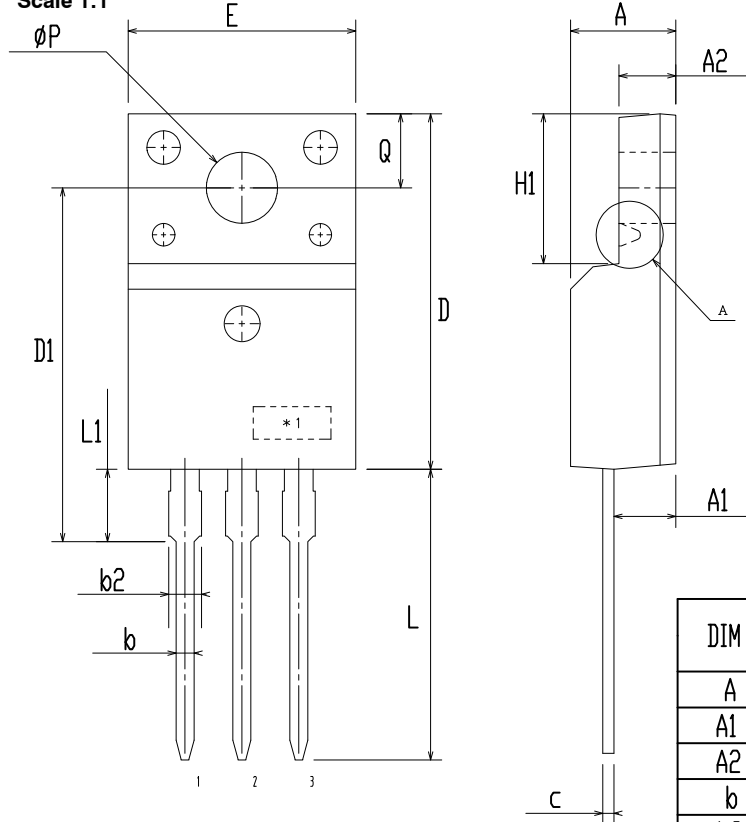
**Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms**

**TO-220 Fullpack, 3-Lead / TO-220F-3SG**  
**CASE 221AT**  
**ISSUE B**

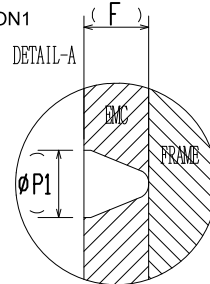
DATE 19 JAN 2021



Scale 1:1



OPTION1



DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.50	4.70	4.90
A1	2.56	2.76	2.96
A2	2.34	2.54	2.74
b	0.70	0.80	0.90
b2	~	~	1.47
c	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.60	15.80	16.00
E	9.96	10.16	10.36
e	2.34	2.54	2.74
F	~	0.84	~
H1	6.48	6.68	6.88
L	12.78	12.98	13.18
L1	3.03	3.23	3.43
Ø P	2.98	3.18	3.38
Ø P1	~	1.00	~
Q	3.20	3.30	3.40

**NOTES:**

A. DIMENSION AND TOLERANCE AS ASME Y14.5-2009

B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUCTIONS.

C. OPTION 1 - WITH SUPPORT PIN HOLE

OPTION 2 - NO SUPPORT PIN HOLE

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<b>DESCRIPTION:</b>	<b>TO-220 FULLPACK, 3-LEAD / TO-220F-3SG</b>	<b>PAGE 1 OF 1</b>

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