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MOSFET - Power, Single

N-Channel

100 V, 10.5 m Ω , 51 A

FDD86069-F085

Features

- Low R_{DS(on)} to Minimize Conduction Losses
- Low Q_G and Capacitance to Minimize Driver Losses
- Wettable Flank for Enhanced Optical Inspection
- AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR–Free and are RoHS Compliant

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	100	V
Gate-to-Source Voltage			V _{GS}	±20	V
Continuous Drain		T _C = 25°C	I _D	51	Α
Current R _{θJC} (Notes 1, 3)	Steady State	T _C = 100°C		36	
Power Dissipation		T _C = 25°C	P_{D}	68.2	W
R _{θJC} (Note 1)		T _C = 100°C		34.1	
Continuous Drain		T _A = 25°C	I _D	10.9	Α
Current R _{0JA} (Notes 1, 2, 3)	Steady State	T _A = 100°C		7.7	
Power Dissipation		T _A = 25°C	P_{D}	3.1	W
R _{θJA} (Notes 1, 2)		T _A = 100°C		1.6	
Pulsed Drain Current	$T_A = 25^{\circ}C, t_p = 10 \mu s$		I _{DM}	328	Α
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +175	°C
Source Current (Body Diode)			I _S	56.8	Α
Single Pulse Drain-to-Source Avalanche Energy (I _{L(pk)} = 3.7 A)			E _{AS}	245	mJ
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State	$R_{ heta JC}$	2.2	°C/W
Junction-to-Ambient - Steady State (Note 2)	$R_{\theta JA}$	48.1	

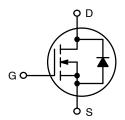
- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Surface-mounted on FR4 board using a 650 mm², 2 oz. Cu pad.
- Maximum current for pulses as long as 1 second is higher but is dependent on pulse duration and duty cycle.



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V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
100 V	10.5 mΩ @ 10 V	51 A

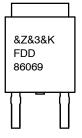


POWER MOSFET



DPAK TO-252 CASE 369AS

MARKING DIAGRAM



&Z = Assembly Plant Code &3 = Data Code (Year & Week)

&K = Lo

FDD86069 = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•		-	•	•	•
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I_D = 250 μA	100			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J			58		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, V _{DS} = 100 V, T _J = 25°C	V _{GS} = 0 V, V _{DS} = 100 V, T _J = 25°C		1	μΑ
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA
ON CHARACTERISTICS (Note 4)						
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D = 109 \mu A$	2	3.1	4.5	V
Threshold Temperature Coefficient	V _{GS(TH)} /T _J			-7.45		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 10 V, I _D = 19.5 A		8.9	10.5	mΩ
CHARGES, CAPACITANCES & GATE	RESISTANCE					
Input Capacitance	C _{iss}			1332		pF
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V, f} = 1 \text{ MHz,} $ $V_{DS} = 50 \text{ V}$		825		
Reverse Transfer Capacitance	C _{rss}	20		12		
Total Gate Charge	Q _{G(TOT)}			19		nC
Threshold Gate Charge	Q _{G(TH)}			2.5		1
Gate-to-Source Charge	Q_{GS}	V _{GS} = 10 V, V _{DS} = 50 V, I _D = 19.5 A		8		
Gate-to-Drain "Miller" Charge	Q_{GD}			4		
Plateau Voltage	V_{GP}			6		V
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{d(on)}			7.7		ns
Turn-On Rise Time	t _r	V _{DD} = 50 V, V _{GS} = 10 V,		15]
Turn-Off Delay Time	t _{d(off)}	$I_D = 19.5 \text{ A}, R_G = 6 \Omega$		15.1]
Turn-Off Fall Time	t _f			11		
DRAIN-SOURCE DIODE CHARACTE	RISTICS					
Source-to-Drain Diode Voltage	V_{SD}	$I_{SD} = 19.5 \text{ A}, V_{GS} = 0 \text{ V}$		0.85	1.2	V
Reverse Recovery Time	T _{RR}			48		ns
Charge Time	ta	$V_{GS} = 0 \text{ V, dI}_{S}/\text{dt} = 100 \text{ A/}\mu\text{s,}$ $I_{S} = 19.5 \text{ A}$		22		1
Discharge Time	t _b	I _S = 19.5 A		26		
Reverse Recovery Charge	Q _{RR}			37		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

DEVICE ORDERING INFORMATION

Device	Marking	Package	Reel Size	Tape Width	Shipping [†]
FDD86069-F085	FDD86069	DPAK (TO-252) (Pb-Free)	13″	16 mm	2500 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{4.} Pulse Test: Pulse Width \leq 300 $\mu s,$ Duty Cycle \leq 2%.

^{5.} Switching characteristics are independent of operating junction temperatures.

TYPICAL CHARACTERISTICS

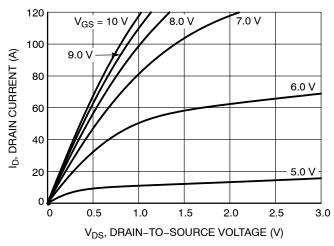


Figure 1. On-Region Characteristics

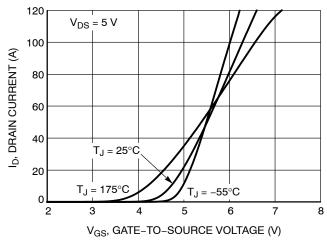


Figure 2. Transfer Characteristics

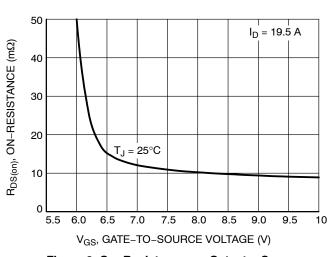


Figure 3. On-Resistance vs. Gate-to-Source Voltage

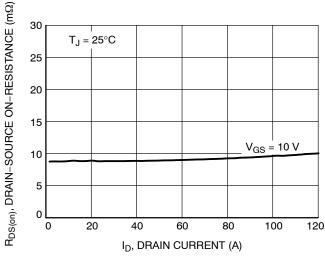


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

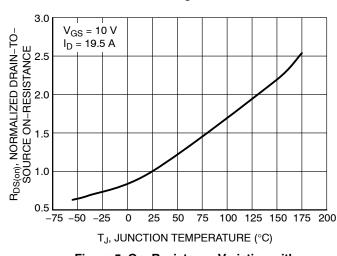


Figure 5. On–Resistance Variation with Temperature

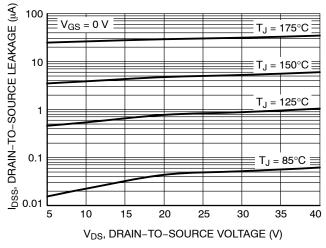


Figure 6. Drain-to-Source Leakage Current vs. Voltage

TYPICAL CHARACTERISTICS

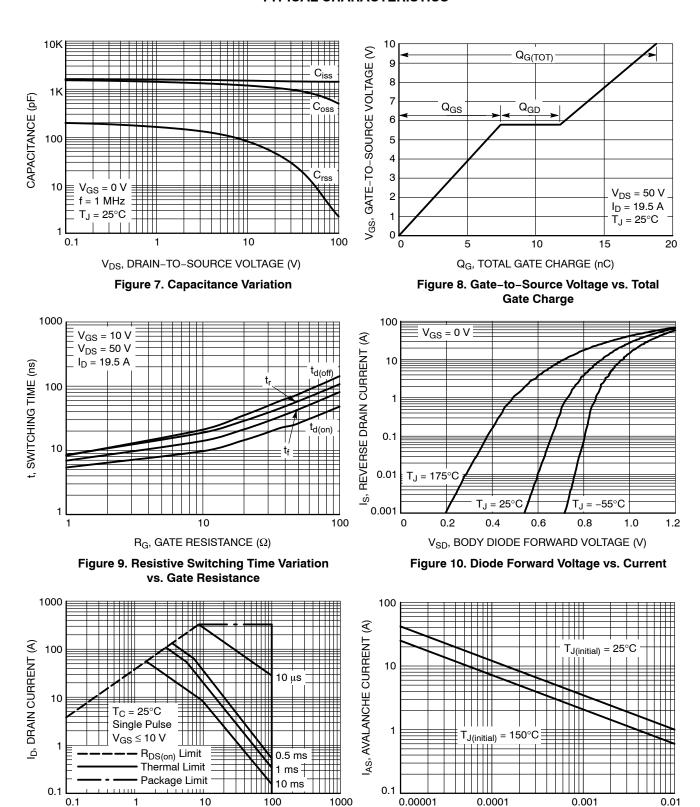


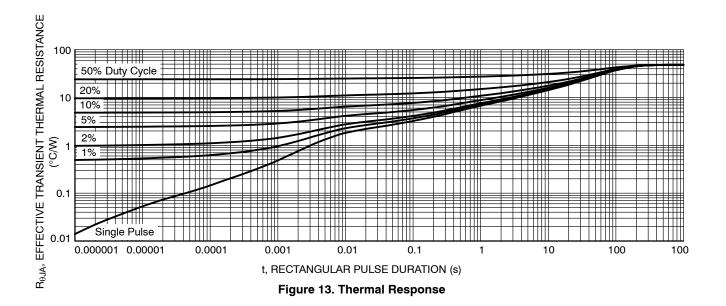
Figure 11. Forward Biased Safe Operating Area

V_{DS}, DRAIN-TO-SOURCE VOLTAGE (V)

t_{AV}, TIME IN AVALANCHE (s)

Figure 12. Avalanche Characteristics

TYPICAL CHARACTERISTICS



PACKAGE DIMENSIONS

DPAK3 (TO-252 3 LD) CASE 369AS ISSUE O 6.73 6.35 5.46 5.21 -5.55 MIN- 6.22 5.97 6.50 MIN 6.40 0.25 MAX PLASTIC BODY Ċ 2 STUB MIN DIODE PRODUCTS VERSION 1.14 0.76 (0.59)3 -1.25 MIN 0.89 2.29 ⊕ 0.25 M AM C 2.28 0.64 -4.56 4.57 LAND PATTERN RECOMMENDATION NON-DIODE PRODUCTS VERSION 2.39 SEE 2.18 4.32 MIN NOTE D 0.58 0.45 5,21 MIN 10.41 9.40 SEE DETAIL A NON-DIODE PRODUCTS VERSION DIODE PRODUCTS VERSION ○ 0.10 B 0.51 **GAGE PLANE** NOTES: UNLESS OTHERWISE SPECIFIED A) THIS PACKAGE CONFORMS TO JEDEC, TO-252. 0.45 ISSUE C, VARIATION AA. 10° 0° (1.54)B) ALL DIMENSIONS ARE IN MILLIMETERS. C) DIMENSIONING AND TOLERANCING PER O) DIMENSIONING AND TOLERANCING FER ASME Y14.5M-2009. D) SUPPLIER DEPENDENT MOLD LOCKING HOLES OR CHAMFERED CORNERS OR EDGE PROTRUSION. E) TRIMMED CENTER LEAD IS PRESENT ONLY FOR DIODE PRODUCTS. 0.127 MAX 1.40 F) DIMENSIONS ARE EXCLUSSIVE OF BURSS, SEATING PLANE (2.90)MOLD FLASH AND TIE BAR EXTRUSIONS. G) LAND PATTERN RECOMENDATION IS BASED ON IPC7351A STD **DETAIL A** TO228P991X239-3N.

(ROTATED -90°) SCALE: 12X

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