

# FSUSB242UCX

## FSUSB242 Evaluation Board User's Manual

### Evaluation Board Description

This board is designed to help the users quickly evaluate the FSUSB242 in the current system. The male/plug connector goes to the existing system and the female/receptacle connector will become the new outward facing port.

### Evaluation Board Operation

The board requires existing system power or an external power supply to power the device. The user can connect power to the VDD test point to power the board. Once the device is powered up the user can use the SEL jumper to configure the switch as desired.

### Jumper Labels

- VDD = Device and Board Power
- GND = Device GND or Supply GND
- SEL = Device Control and Switch Selection

Table 1. SWITCH TRUTH TABLE

VDD	SEL	Switch Configuration
UVLO	X	Switch off High impedance
Valid	0	HSD+ = HSD1+, HSD- = HSD1-
Valid	1	HSD+ = HSD2+, HSD- = HSD2-
Valid	Float/High-Z	Switch Disable High impedance

### BOARD LAYOUT

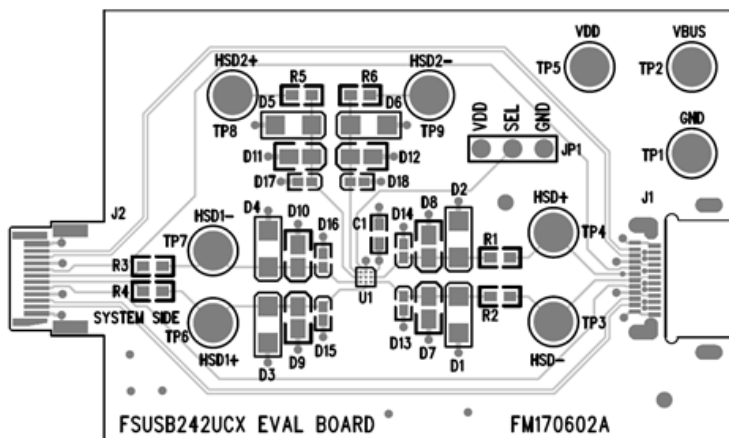


Figure 2. Evaluation Board Layout



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## EVAL BOARD USER'S MANUAL

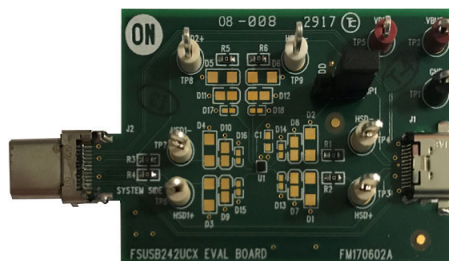


Figure 1. FSUSB242 Evaluation Board

# FSUSB242UCX

## PIN FUNCTION DESCRIPTION

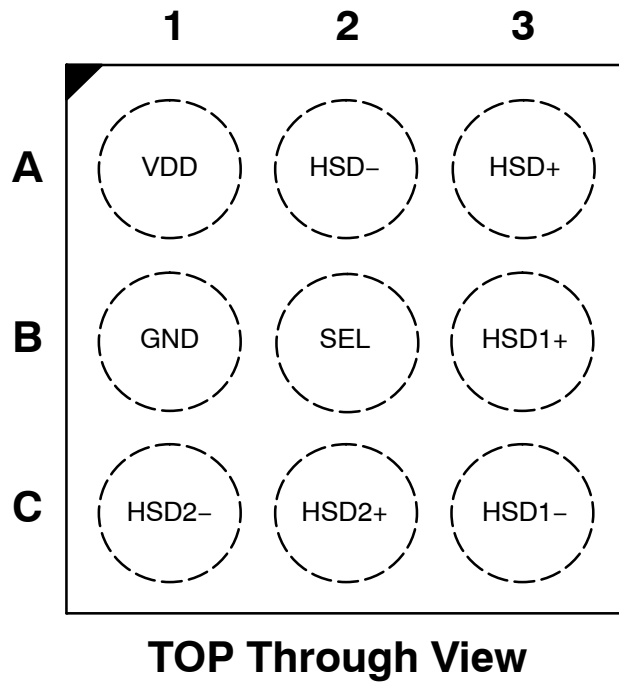


Figure 3. Pin Mapping

Table 2. PIN FUNCTION DESCRIPTION

CSP Bump	Name	Type	Description
A1	VDD	Power	Supply Power
B1	GND	Ground	Ground
A3	HSD+	Data	Common High Speed Data Bus
A2	HSD-	Data	Common High Speed Data Bus
B3	HSD1+	Data	Multiplexed High Speed Data Port 1
C3	HSD1-	Data	Multiplexed High Speed Data Port 1
C2	HSD2+	Data	Multiplexed High Speed Data Port 2
C1	HSD2-	Data	Multiplexed High Speed Data Port 2
B2	SEL	Input	Tri-Input HSD Switch Select

## BOARD SCHEMATIC

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### Figure 4. FM170602A Schematic

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