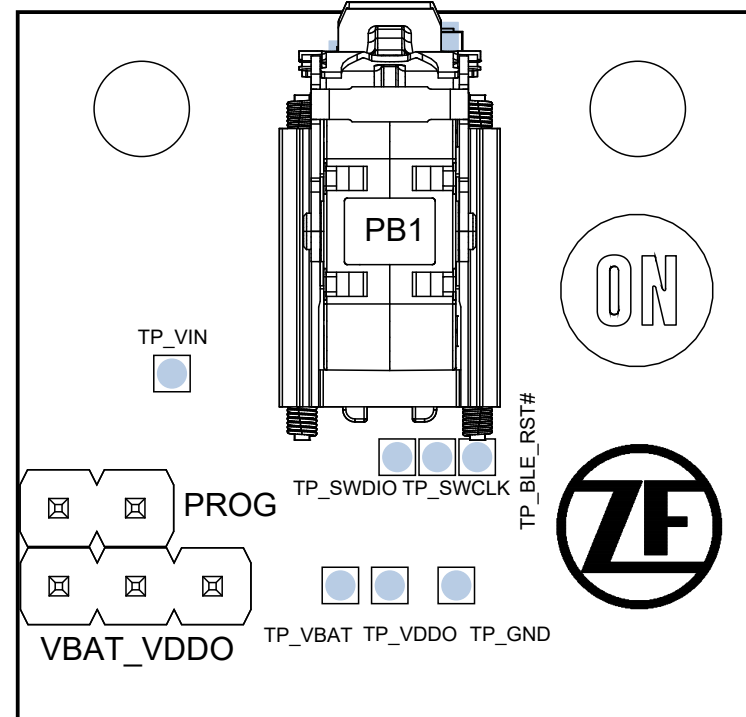


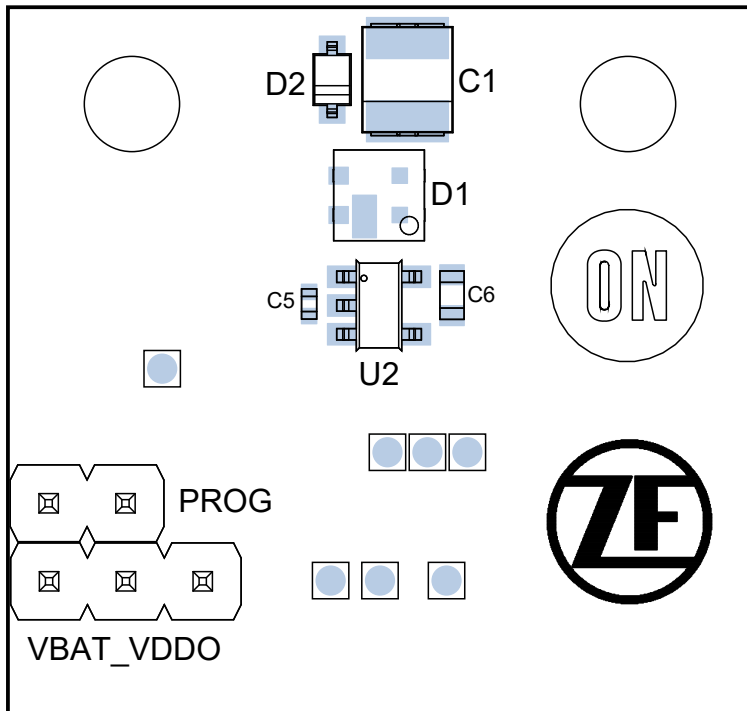
Bill Of Materials - Top side components

Designator	Comment	Quantity
C1	33 uF	1
C3, C4, C7, C8	1 µF	4
C5	100 nF	1
C6	5n6	1
D1	NSR1030QMUTWG	1
D2	MM3Z6V2ST1G	1
PB1	AFIG-0007	1
PROG	613 002 111 21	1
R2, R3	0R	2
R4, R7, R8	68	3
R5	10k	1
R6	100k	1
U1, U3	NCP170BMX330TCG	2
U2	NCP302LSN27	1
U4	RSL10 SiP	1
VBAT_VDDO	613 003 111 21	1

View from Top side (Scale =4:1)



View from Top side (Scale =4:1) - underneath PB1



1 Only components present in this assembly variant are listed in BoM table on the left

2 Components not present in this assembly variant are crossed out by red line

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released

Variant name: standard_board

Assembly
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Engineer: T. Duris

Date: 6. July 2020

PCB File: BLE-SWITCH001-GEVB.PcbDoc

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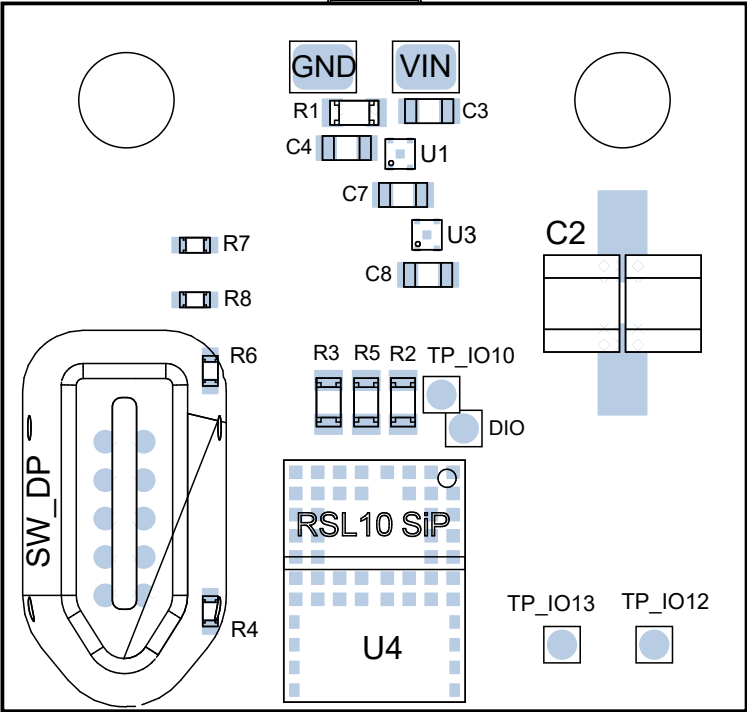


Repository revision: Not in repository control


Bill Of Materials - Bottom side components

Designator	Comment	Quantity
C3, C4, C7, C8	1 μ F	4
R2, R3	0R	2
R4, R7, R8	68	3
R5	10k	1
R6	100k	1
U1, U3	NCP170BMX330TCG	2
U4	RSL10 SiP	1

View from Bottom side (Scale =4:1)



- 3 Only components present in this assembly variant are listed in BoM table on the left
- 4 Components not present in this assembly variant are crossed out by red line

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Engineer: T. Duris	Date: 6. July 2020		
PCB File: BLE-SWITCH001-GEVB.PcbDoc		ON Semiconductor Solution Engineering Center Piestany	
Repository revision: Not in repository control			

1

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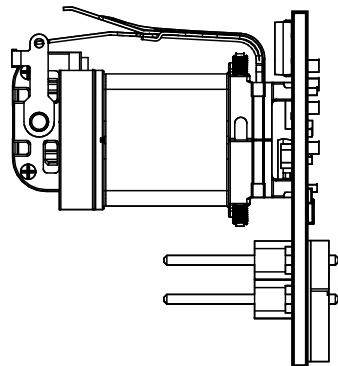
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4

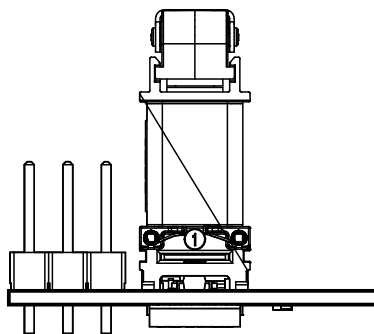
5

A

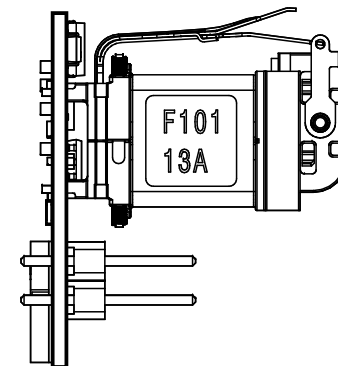
View from Right side (Scale =2:1)



View from Front side (Scale 2)



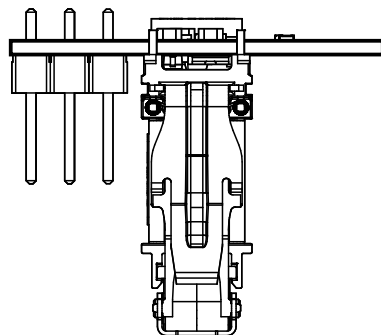
View from Left side (Scale =2:1)



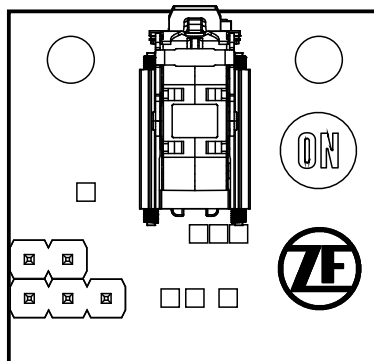
B

B

View from Back side (Scale =2:1)



View from Top side (Scale =2:1)



C

C

D

D

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released

Variant name: standard_board

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Date: 6. July 2020

PCB File: BLE-SWITCH001-GEVB.PcbDoc

Repository revision: Not in repository control

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1

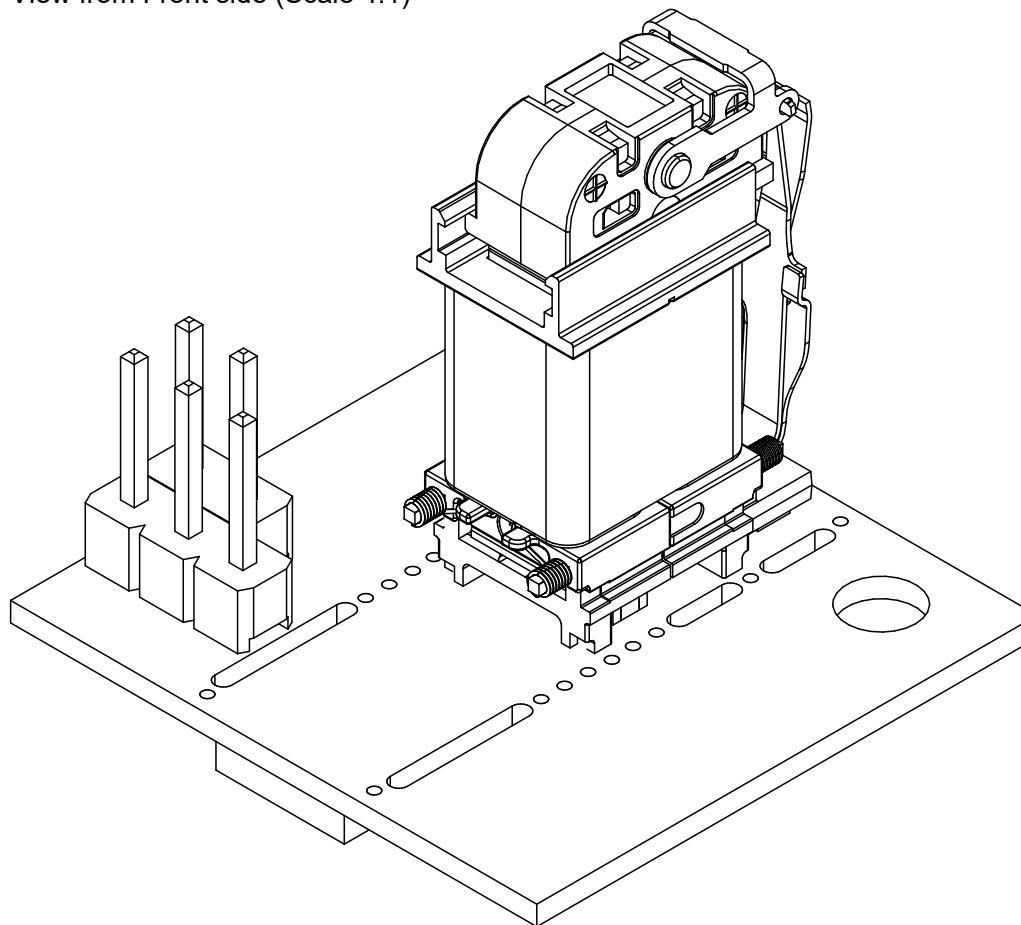
2

3

4

5

View from Front side (Scale 4:1)



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Variant name: standard_board

Engineer: T. Duris

Date: 6. July 2020

PCB File: BLE-SWITCH001-GEVB.PcbDoc

Repository revision: Not in repository control

Revision:
Rev 4

State:
released

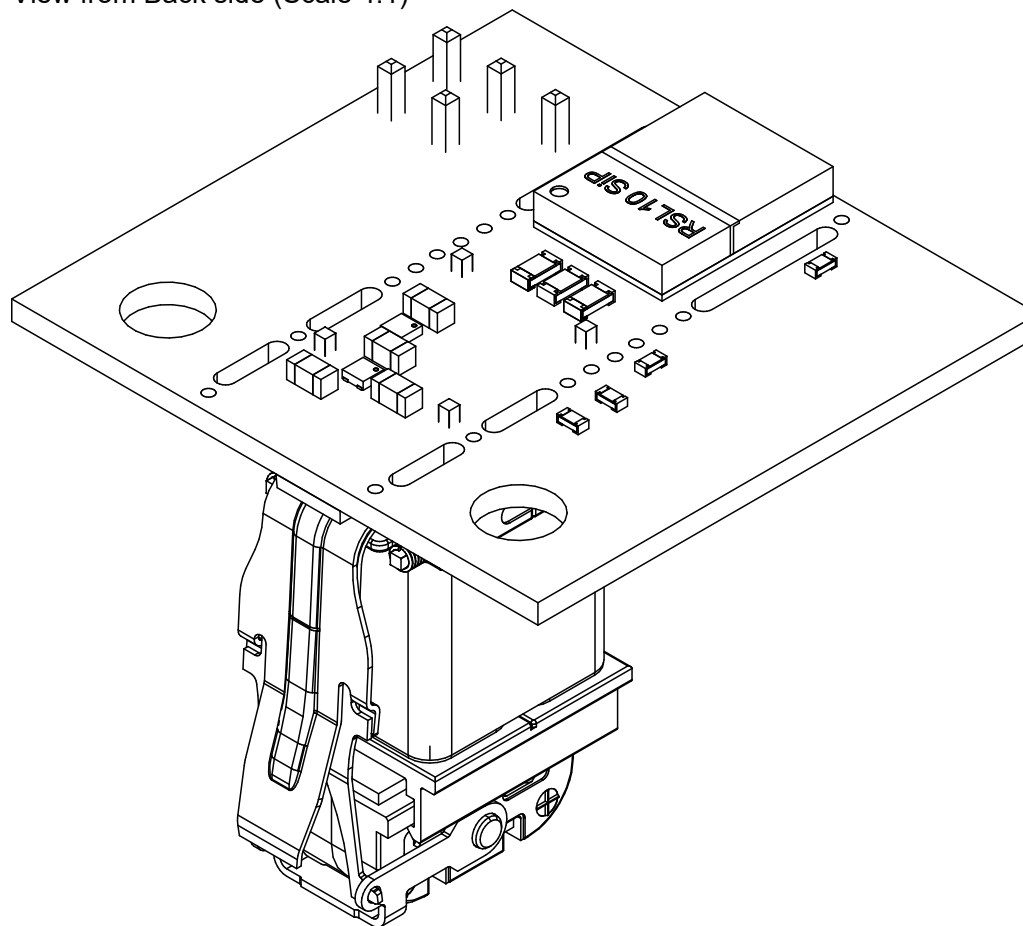
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View from Back side (Scale 4:1)



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Engineer: T. Duris

Date: 6. July 2020

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