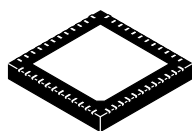


MECHANICAL CASE OUTLINE

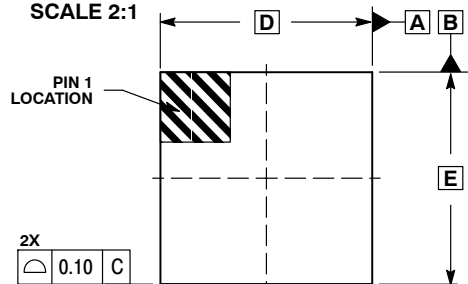
PACKAGE DIMENSIONS

ON Semiconductor®

ON



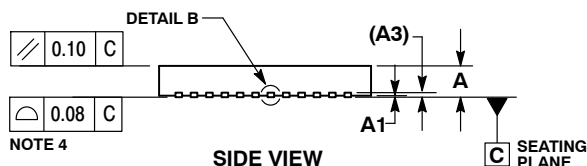
1 48
SCALE 2:1



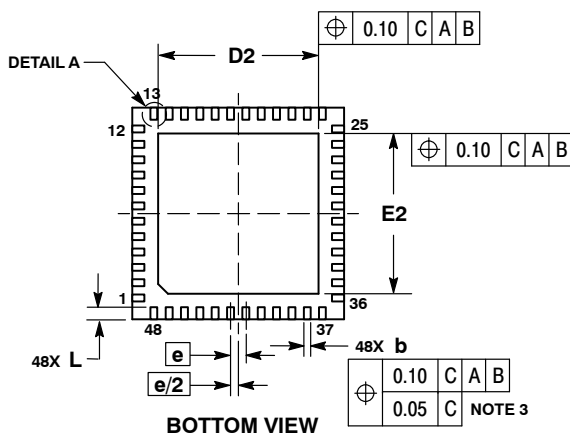
2X
0.10 C

2X
0.10 C

TOP VIEW

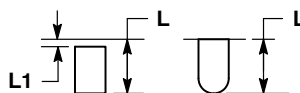


SIDE VIEW

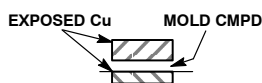


BOTTOM VIEW

QFN48 7x7, 0.5P
CASE 485EB
ISSUE O



DETAIL A
ALTERNATE TERMINAL
CONSTRUCTIONS



DETAIL B
ALTERNATE
CONSTRUCTION

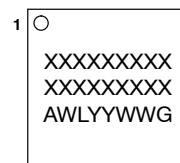
DATE 07 JUL 2015

NOTES:

1. DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO THE PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.25 MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

MILLIMETERS		
DIM	MIN	MAX
A	0.80	0.90
A1	0.00	0.05
A3	0.20	REF
b	0.20	0.30
D	7.00	BSC
D2	5.20	5.40
E	7.00	BSC
E2	5.20	5.40
e	0.50	BSC
L	0.35	0.45
L1	0.00	0.15

GENERIC MARKING DIAGRAM*

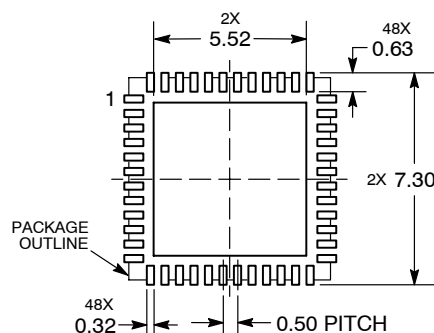


A = Assembly Location
WL = Wafer Lot
YY = Year
WW = Work Week
G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking.

Pb-Free indicator, "G" or microdot "▪", may or may not be present.

RECOMMENDED SOLDERING FOOTPRINT*



DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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DESCRIPTION:	QFN48 7X7, 0.50P	PAGE 1 OF 1

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