

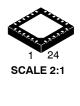
PIN ONE -REFERENCE

0.15 С

0.08 С

С 0.15

С 0.10

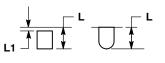


2X 🗀

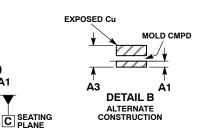
NOTE 4

QFN24 5x5, 0.65P CASE 485CS **ISSUE 0**

DATE 24 OCT 2012



DETAIL A ALTERNATE TERMINAL CONSTRUCTIONS



- 1. DIMENSIONS AND TOLERANCING PER
- ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
 DIMENSION & APPLIES TO PLATED
- TERMINAL AND IS MEASURED BETWEEN
 0.15 AND 0.30 MM FROM THE TERMINAL TIP.
- COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

	MILLIMETERS					
DIM	MIN	MAX				
Α	0.80	0.90				
A1	-	0.05				
А3	0.20 REF					
b	0.25	0.35				
D	5.00 BSC					
D2	3.40	3.60				
E	5.00 BSC					
E2	3.40	3.60				
е	0.65 BSC					
K	0.20 MIN					
L	0.30	0.50				
L1		- 0.15				

GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code Α = Assembly Location

WL = Wafer Lot YY = Year WW = Work Week = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking.

Pb-Free indicator, "G" or microdot " ■", may or may not be present.

			Ф	0.10	(M)	СА	В			
DETAIL A—	- [)2 	-	- K	•	•	•	•		
		000	<u> </u>	13	\oplus	0.10	M	С	Α	В
Ę			님	1						
ŧ		 	堷	- E2	2					
<u>↓1</u> 5		фпг	Ä	₩	_					
A	24	 		_ 24X	b					
	<u> </u>	-			0.1	0 (M)	С	Α	В	
e/2	2 →	←		Ψ	0.0	5 (M	С	NO	TE	3
В	отто	M VIE	W							

D

TOP VIEW

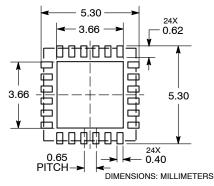
SIDE VIEW

DFTAIL B

ΑВ

E

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

DOCUMENT NUMBER:	98AON84592E	Electronic versions are uncontrolled except when accessed directly from the Document Repository Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	
DESCRIPTION:	QFN24, 5x5, 0.65P		PAGE 1 OF 1

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.