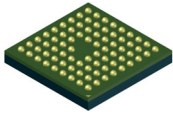


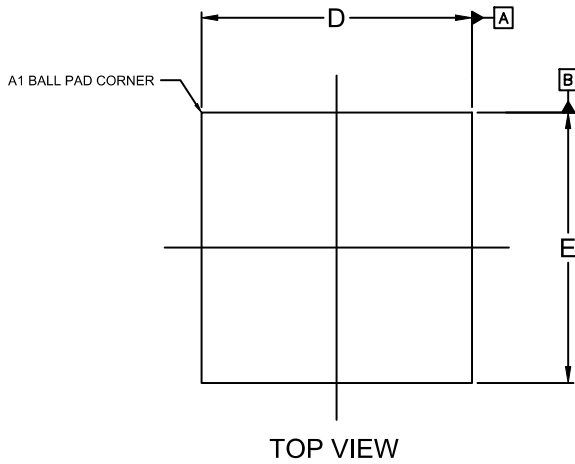
# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

ON Semiconductor®



## VFBGA78 5x5 CASE 138AW ISSUE B

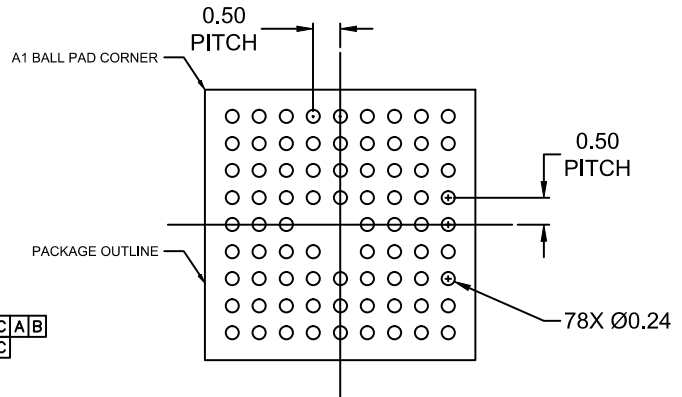
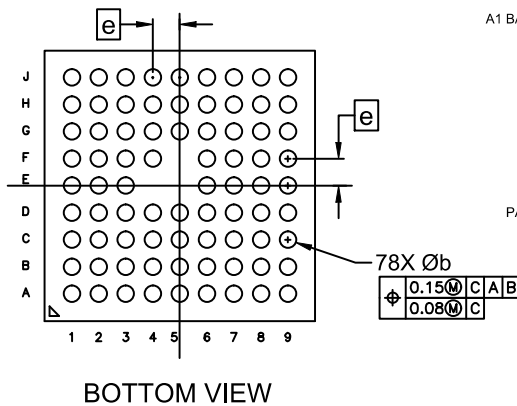
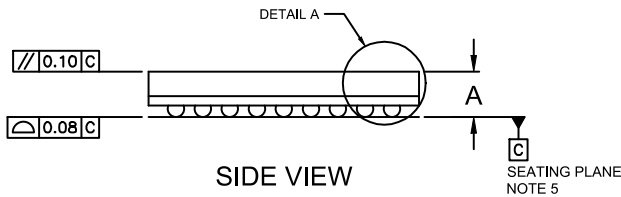
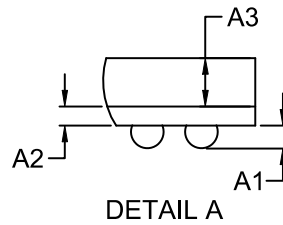
DATE 14 MAY 2020



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION b IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER PARALLEL TO DATUM C.
4. COPLANARITY APPLIES TO THE SPHERICAL CROWNS OF THE SOLDER BALLS.
5. DATUM C, THE SEATING PLANE, IS DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	---	---	0.983
A1	0.16	---	0.26
A2	0.242 REF		
A3	0.450 REF		
b	0.27	---	0.37
D	4.90	5.00	5.10
E	4.90	5.00	5.10
e	0.50 BSC		



### GENERIC MARKING DIAGRAM\*



- XXXX = Specific Device Code
- A = Assembly Location
- WL = Wafer Lot
- YY = Year
- WW = Work Week
- CCC = Country of Origin
- = Pb-Free Package

\* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

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<b>DESCRIPTION:</b>	<b>VFBGA78 5x5</b>	<b>PAGE 1 OF 1</b>

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