

Product Overview

NB7L1008: 2.5 V / 3.3 V 1:8 LVPECL Fanout Buffer

For complete documentation, see the data sheet.

2.5V / 3.3V 1:8 LVPECL high performance differential 1:8 Clock/Data fanout buffer. The NB7L1008 produces eight identical output copies of Clock or Data operating up to 7 GHz or 12 Gb/s, respectively.

Features

- Typical Maximum Input Data Rate > 12 Gb/s Typical
- Data Dependent Jitter < 15 ps
- Low Skew 1:8 LVPECL Outputs, < 20 ps max
- 160 ps Typical Propagation Delay
- Multi-Level Inputs, accepts LVPECL, CML, LVDS
- Internal Input Termination Resistors, 50 ohm
- VREFAC Reference Output

Applications

- Computer server - Data Redundancy
- Line card clocking

Benefits

- High speed clock and data fanout
- Extremely low jitter when transmitting data.
- Very accurate edge placement from output to output
- Provides a 1:8 fanout with almost 0 delay
- Also can provide a level translation function.
- fewer external components needed to provide termination.
- Provides reference voltage for Single Ended Input Capabilities

End Products

- Cloud Servers
- Mini Servers

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Type	Channels	Input / Output Ratio	Input Level	Output Level	V _{CC} Typ (V)	t _{jitter} RMS Typ (ps)	t _{skew(0-1)} Max (ps)	t _{pd} Typ (ns)	t _R & t _F Max (ps)	f _{max} Clock Typ (MHz)	f _{max} Data Typ (Mbps)	Package Type
NB7L1008MNG		Pb-free Halide free non AEC-Q and PPAP	Active	Buffer	1	1:8	CML ECL LVDS	ECL	2.5 3.3	0.2	20	0.16	80	7000	12000	QFN-32
NB7L1008MNTXG		Pb-free Halide free non AEC-Q and PPAP	Active	Buffer	1	1:8	CML ECL LVDS	ECL	2.5 3.3	0.2	20	0.16	80	7000	12000	QFN-32

For more information please contact your local sales support at www.onsemi.com.

Created on: 1/27/2021