

## Product Overview

### NB6L14: Clock / Data Fanout Buffer, 1:4 Differential, 3 GHz, 2.5 V / 3.3 V, with LVPECL Outputs

For complete documentation, see the data sheet.

The NB6L14 is a 3.0 GHz differential 1:4 LVPECL fanout buffer. The differential inputs incorporate internal 50-ohm termination resistors that are accessed through the VT pin. This feature allows the NB6L14 to accept various logic standards, such as LVPECL, LVC MOS, LVTTTL, CML, or LVDS logic levels. The VREF\_AC reference output can be used to rebias capacitor-coupled differential or single-ended input signals. The 1:4 fanout design was optimized for low output skew applications. The NB6L14 is a member of the ECLinPS MAX family of high performance clock and data products.

#### Features

- Input Clock Frequency > 3.0 GHz
- < 20 ps Within Device Output Skew
- Internal Input Termination Resistors, 50-ohm
- VREFAC Reference Output

#### Benefits

- High performance applications
- Low Output-to-output skew
- No external components needed for inputs
- Rebias Capacitor-coupled Input signal

#### Applications

- Clock / Data Distribution

### Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Type	Channels	Input / Output Ratio	Input Level	Output Level	V <sub>CC</sub> Typ (V)	t <sub>Jitter</sub> <sup>R</sup> MS Typ (ps)	t <sub>skew(o)</sub> Max (ps)	t <sub>pd</sub> Typ (ns)	t <sub>R</sub> & t <sub>F</sub> Max (ps)	f <sub>max</sub> Clock Typ (MHz)	f <sub>max</sub> Data Typ (Mbps)	Package Type
NB6L14MNG		Pb-free Halide free non AEC-Q and PPAP	Active	Buffer	1	1:4	CML LVC MOS LVDS LVPECL LVTTTL	LVPECL	2.5 3.3	0.2	20	0.37	200	3000	2500	QFN-16
NB6L14MNR2G		Pb-free Halide free non AEC-Q and PPAP	Active	Buffer	1	1:4	CML LVC MOS LVDS LVPECL LVTTTL	LVPECL	2.5 3.3	0.2	20	0.37	200	3000	2500	QFN-16

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