

NLSF1174

Hex D Flip-Flop with Common Clock and Reset

Product Overview



For complete documentation, see the data sheet.

This device consists of six flip-flops with common Clock and Reset inputs. Each flip-flop is loaded with a low-to-high transition; of the Clock input. Reset is asynchronous and active low. All inputs/outputs are standard CMOS compatible.

Features

- Output Drive Compatibility: 10 LSTTL Loads
- Output Directly Interface to CMOS
- Operation Voltage Range: 2 to 6 V
- Low Input Current: 1.0 μ A
- MSL Level 1
- Chip Complexity: 162 FET
- Pb-Free Package is Available

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Type	Channels	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
NLSF1174MNR2G	0.1693	 	Active	D-Type	2	2	6	20	4	QFN-16