

MM74HC273

Octal D-Type Flip-Flop with Clear

Product Overview

For complete documentation, see the data sheet.

The MM74HC273 edge triggered flip-flops utilize advanced silicon-gate CMOS technology to implement D-type flip-flops. They possess high noise immunity, low power, and speeds comparable to low power Schottky TTL circuits. This device contains 8 master-slave flip-flops with a common clock and common clear. Data on the D input having the specified setup and hold times is transferred to the Q output on the LOW-to-HIGH transition of the CLOCK input. The CLEAR input when LOW, sets all outputs to a low state. Each output can drive 10 low power Schottky TTL equivalent loads. The MM74HC273 is functionally as well as pin compatible to the 74LS273. All inputs are protected from damage due to static discharge by diodes to VCC and ground.

Features

- Typical propagation delay: 18 ns
- Wide operating voltage range
- Low input current: 1 μ A maximum
- Low quiescent current: 80 μ A (74 Series)
- Output drive: 10 LS-TTL loads

Applications

- This product is general usage and suitable for many different applications.

Part Electrical Specifications										
Product	Pricing (\$/Unit)	Compliance	Status	Type	Channels	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MM74HC273MTC	0.2947		Active	Octal D-type w/clear	8	2	6	27	5.2	TSSOP-20
MM74HC273MTCX	0.2184		Active	Octal D-type w/clear	8	2	6	27	5.2	TSSOP-20
MM74HC273WM	0.3383		Active	Octal D-type w/clear	8	2	6	27	5.2	SOIC-20W
MM74HC273WMX	0.3181		Active	Octal D-type w/clear	8	2	6	27	5.2	SOIC-20W