

NLU2G16

Dual Buffer

Product Overview

For complete documentation, see the data sheet.

The NLU2G16 MiniGate™ is an advanced high speed CMOS dual non inverting buffer in ultra small footprint. The NLU2G16 input and output structures provide protection when voltages up to 7.0 volts are applied, regardless of the supply voltage.







Features

- High Speed: $t_{PD} = 3.5 \text{ ns}$ (Typ) at $V_{CC} = 5.0 \text{ V}$
- Low Power Dissipation: $I_{CC} = 1 \mu\text{A}$ (Max) at $T_A = 25^\circ\text{C}$
- Power Down Protection Provided on inputs
- Balanced Propagation Delays
- Overvoltage Tolerant (OVT) Input and Output Pins
- Ultra Small Pb Free Package

Applications

- General Purpose Logic

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Channels	Output	V_{CC} Min (V)	V_{CC} Max (V)	t_{pd} Max (ns)	I_o Max (mA)	Package Type
NLU2G16AMUTCG	0.0853	 	Active	2	CMOS	1.65	5.5	5.5	8	UDFN-6
NLU2G16CMUTCG	0.1169	 	Active	2	CMOS	1.65	5.5	5.5	8	UDFN-6
NLU2G16MUTCG	0.21	 	Active	2	CMOS	1.65	5.5	5.5	8	UDFN-6