

MC74HCT241A

Octal 3-State Noninverting Buffer/Line Driver/Line Receiver with LSTTL-Compatible Inputs

Product Overview

For complete documentation, see the data sheet.

The MC74HCT241A is identical in pinout to the LS241. This device may be used as a level converter for interfacing TTL or NMOS outputs to High Speed CMOS inputs. The HCT241A is an octal noninverting buffer/line driver/line receiver designed to be used with 3 state memory address drivers, clock drivers, and other bus oriented systems. The device has noninverted outputs and two output enables. Enable A is active low and Enable B is active high. The HCT241A is similar in function to the HCT244. See also HCT240.

Features

- Output Drive Capability: 15 LSTTL Loads
- TTL/NMOS Compatible Input Levels
- Outputs Directly Interface to CMOS, NMOS, and TTL
- Operating Voltage Range: 4.5 to 5.5 V
- Low Input Current: 1.0 μ A
- In Compliance with the Requirements Defined by JEDEC Standard No. 7A
- Chip Complexity: 118 FETs or 29.5 Equivalent Gates
- PbFree Packages are Available

Applications

- Desktop

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Channels	Output	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC74HCT241A DTG	0.428		Active	8	3-State	4.5	5.5	23	6	TSSOP-20
MC74HCT241A DTR2G	0.3587		Active	8	3-State	4.5	5.5	23	6	TSSOP-20
MC74HCT241A DWR2G	0.2887		Active	8	3-State	4.5	5.5	23	6	SOIC-20W