

74LCXH16244

Low Voltage 16-Bit Buffer/Line Driver with Bushold

Product Overview

For complete documentation, see the data sheet.

The LCXH16244 contains sixteen non-inverting buffers with 3-STATE outputs designed to be employed as a memory and address driver, clock driver, or bus oriented transmitter/receiver. The device is nibble controlled. Each nibble has separate 3-STATE control inputs which can be shorted together for full 16-bit operation. The LCXH16244 data inputs include active bushold circuitry, eliminating the need for external pull-up resistors to hold unused or floating data inputs at a valid logic level. The LCXH16244 is designed for low voltage (2.5V or 3.3V) VCC applications with capability of interfacing to a 5V signal environment. The LCXH16244 is fabricated with an advanced CMOS technology to achieve high speed operation while maintaining CMOS low power dissipation.

Features

- 5V tolerant control inputs and outputs
- 2.3V-3.6V VCC specifications provided
- 4.5 ns tPD max (VCC = 3.0V), 20 μ A ICC max
- Bushold on inputs eliminates the need for external pull-up/pull-down resistors
- Power down high impedance inputs and outputs
- \pm 24 mA output drive (VCC = 3.0V)
- Implements patented noise/EMI reduction circuitry
- Latch-up performance exceeds 500 mA
- ESD performance: Human body model > 2000V Machine model > 200V
- Also packaged in plastic Fine-Pitch Ball Grid Array (FBGA)

For more features, see the data sheet

Applications

- This product is general usage and suitable for many different applications.

| Part Electrical Specifications | | | | | | | | | | |
|--------------------------------|-------------------|------------|--------|----------|---------|-------------------------|-------------------------|--------------------------|-------------------------|--------------|
| Product | Pricing (\$/Unit) | Compliance | Status | Channels | Output | V _{CC} Min (V) | V _{CC} Max (V) | t _{pd} Max (ns) | I _O Max (mA) | Package Type |
| 74LCXH16244M TDX | 0.7506 | | Active | 16 | 3-State | 2 | 3.6 | 4.5 | 24 | TSSOP-48 |