

Product Overview

CAT24C128: EEPROM Serial 128-Kb I²C

For complete documentation, see the data sheet.

The CAT24C128 is a EEPROM Serial 128-Kb I²C, internally organized as 16,384 words of 8 bits each. It features a 64-byte page write buffer and supports both the Standard (100 kHz), Fast (400 kHz) and Fast-Plus (1 MHz) I²C protocol. Write operations can be inhibited by taking the WP pin High (this protects the entire memory). On-Chip ECC (Error Correction Code) makes the device suitable for high reliability applications.

Features

- Supports Standard, Fast and Fast-Plus I²C Protocol
- 1.8 V to 5.5 V Supply Voltage Range
- 64-Byte Page Write Buffer
- Hardware Write Protection for Entire Memory
- Schmitt Triggers and Noise Suppression Filters on I²C Bus Inputs (SCL and SDA)
- Low Power CMOS Technology
- 1,000,000 Program/Erase Cycles
- 100 Year Data Retention
- Industrial Temperature Range
- 8-lead SOIC and TSSOP and 8-pad UDFN packages

For more features, see the data sheet

Applications

- Alarm Systems
- Audio Players
- Automotive Systems
- Cable Modems
- CDRW

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Type	Density	Organization	Data Transmission Standard	f _{cycle} Max (kHz)	t _{ACC} Max ns	V _{CC} Min (V)	V _{CC} Max (V)	I _{standby} Max (μA)	I _{act} Max (mA)	T Min (°C)	T Max (°C)	Package Type
CAT24C128HU4IGT3	0.4613	Pb-free Halide free non AEC-Q and PPAP	Active	Serial	128 kb	16k x 8	I ² C	1000	400	1.8	5.5	2	3	-40	85	UDFN-8
CAT24C128WI-GT3	0.2053	Pb-free Halide free non AEC-Q and PPAP	Active	Serial	128 kb	16k x 8	I ² C	400	-	1.8	5.5	1	1	-40	85	SOIC-8
CAT24C128YI-GT3	0.26	Pb-free Halide free non AEC-Q and PPAP	Active	Serial	128 kb	16k x 8	I ² C	400	-	1.8	5.5	1	1	-40	85	TSSOP-8

For more information please contact your local sales support at www.onsemi.com.

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