

NB3N5573

Clock Generator, Crystal to 25 MHz, 100 MHz, 125 MHz, 200 MHz, 3.3 V, with Dual HCSL

Product Overview

For complete documentation, see the data sheet.

The NB3N5573 is a high precision, low phase noise clock generator that supports PCI Express and Ethernet requirements. The device is part of the PureEdge™ product family and takes a 25 MHz fundamental mode parallel resonant crystal and generates differential HCSL output at 25 MHz, 100 MHz, 125 MHz or 200 MHz clock frequencies.

Features

- Typical Period RMS jitter of 1.5 ps
- HCSL Differential Output
- Uses 25 MHz Fundamental Mode Parallel Resonant Crystal
- Industrial Temperature Range -40C to +85C
- Operating Range 3.3 V +/- 10%
- PCIe Gen1, Gen2, Gen3, Gen4, QPI, UPI Jitter Compliant

Benefits

- Best in Class Jitter Performance

Applications

- Gigabit Ethernet
- FB DIMM
- PCIe Clock Generation Gen 1, Gen 2, Gen 3, Gen 4

End Products

- Servers
- Networking Equipment
- Set Top Box

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Input Level	Output Level	V _S Typ (V)	f _{in} Typ (MHz)	f _{out} Typ (MHz)	t _{Jitter(Cy-Cy)} Typ (ps)	t _{Jitter(Period)} Typ (ps)	t _{Jitter(Φ)} Typ (ps)	t _{R & t_F} Typ (ps)	t _{R & t_F} Max (ps)	T _A Min (°C)	T _A Max (°C)	Package Type
NB3N5573DTG	2.07		Active		HCSL		25	25-200	2	1.5	0.4	340	700	-40	85	TSSOP-16
NB3N5573DTR2G	1.38		Active		HCSL		25	25-200	2	1.5	0.4	340	700	-40	85	TSSOP-16