

NB3L208K

2.5V, 3.3V Differential 1:8 HCSL Fanout Buffer

Product Overview

For complete documentation, see the data sheet.

The NB3L208K is a differential 1:8 Clock fanout buffer with High-speed Current Steering Logic (HCSL) outputs. Inputs can directly accept differential LVPECL, LVDS, and HCSL signals. Single-ended LVPECL, HCSL, LVCMOS, or LVTTTL levels are accepted with a proper external V_{th} reference supply per Figures 4 and 6. The input signal will be translated to HCSL and provides eight identical copies operating up to 350 MHz. The NB3L208K is optimized for ultra-low phase noise, propagation delay variation and low output-to-output skew, and is DB800H compliant. As such, system designers can take advantage of the NB3L208K's performance to distribute low skew clocks across the backplane or the motherboard making it ideal for Clock and Data distribution applications such as PCI Express, FBDIMM, Networking, Mobile Computing, Gigabit Ethernet, etc. Output drive current is set by connecting a 475 Ω resistor from IREF (Pin 27) to GND per Figure 11. Outputs can also interface to LVDS receivers when terminated per Figure 12.

Features

- Maximum Input Clock Frequency > 350 MHz
- 2.5 V \pm 5% / 3.3 V \pm 10% Supply Voltage Operation
- 8 HCSL Outputs
- DB800H Compliant
- Individual OE Control Pin for Each Bank of 2 Outputs
- 100 ps Max Output-to-Output Skew Performance
- 1 ns Typical Propagation Delay
- 450 ps Typical Rise and Fall Times
- 80 fs Maximum Additive Phase Jitter RMS
- PCIe Gen 3 and Gen 4 Compliant

For more features, see the data sheet

Applications

- Mobile Computing
- Networking
- Gigabit Ethernet
- FBDIMM
- PCI Express Gen 3 and Gen 4

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Type	Channels	Input / Output Ratio	Input Level	Output Level	V_{CC} Typ (V)	$t_{jitter,RMS}$ Typ (ps)	$t_{skew(o-o)}$ Max (ps)	t_{pd} Typ (ns)	t_R & t_F Max (ps)	$f_{max,C}$ lock Typ (MHz)	$f_{max,D}$ ata Typ (Mbps)	Package Type
NB3L208KMNG	2.178		Active	Buffer	1	1:8		HCSL		0.046	20	1	700	350		QFN-32
NB3L208KMNTXG	3.25		Active	Buffer	1	1:8		HCSL		0.046	20	1	700	350		QFN-32