

## NB3F8L3010C

# Clock / Data Fanout Buffer, 3:1:10 Differential, LVCMOS, 3.3 V / 2.5 V / 1.8 V / 1.5 V



## Product Overview

For complete documentation, see the data sheet.

The NB3F8L3010C is a 3:1:10 Clock or Data fanout buffer operating on a 3.3 V or 2.5 V Core VDD and a flexible 3.3 V or 2.5 V or 1.8 V or 1.5 V VDDO supply which must be equal or less than VDD.

## Features

- Ten CMOS / LVTTTL Outputs up to 200 MHz
- Differential Inputs Accept LVPECL, LVDS, HCSL, or SSTL
- Crystal Oscillator Interface
- Crystal Input Frequency Range: 10 MHz to 40 MHz
- Output Skew: 10 ps Typical
- Additive RMS Phase Jitter @ 125 MHz, (12 kHz - 20 MHz): 0.03 ps(Typical)
- Synchronous Output Enable
- Output Defined Level When Input is Floating
- Pure 3.3 V or 2.5 V Operating Mode, 3.3 V Core with 2.5 V/1.8 V/1.5 V Output Supply; or 2.5 V Core with 1.8 V/1.5 V Output Supply.

## Applications

- Wireless and Wired Infrastructure
- Networking and Data Communications
- High-End Computing
- Clock Distribution

## End Products

- Ethernet Switches / Routers
- Servers
- Test and Measurement
- ATE

## Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Type	Channels	Input / Output Ratio	Input Level	Output Level	V <sub>CC</sub> Typ (V)	t <sub>jitter</sub> RMS Typ (ps)	t <sub>skew(o-o)</sub> Max (ps)	t <sub>pd</sub> Typ (ns)	t <sub>R</sub> & t <sub>F</sub> Max (ps)	f <sub>max</sub> Clock Typ (MHz)	f <sub>max</sub> Data Typ (Mbps)	Package Type
NB3F8L3010CMNG	3.25		Active	Buffer	1	3:1:10		LVC MOS			50			100		QFN-32
NB3F8L3010CMNR4G	3.25		Active	Buffer	1	3:1:10		LVC MOS			50			100		QFN-32
NB3F8L3010CMNTWG	3.25		Active													QFN-32