

Product Overview

MM74HC08: Quad 2-Input AND Gate

For complete documentation, see the data sheet.

The MM74HC08 AND gates utilize advanced silicon-gate CMOS technology to achieve operating speeds similar to LS-TTL gates with the low power consumption of standard CMOS integrated circuits. The HC08 has buffered outputs, providing high noise immunity and the ability to drive 10 LS-TTL loads. The 74HC logic family is functionally as well as pin-out compatible with the standard 74LS logic family. All inputs are protected from damage due to static discharge by internal diode clamps to VCC and ground.

Features

- Typical propagation delay: 7 ns (tPHL), 12 ns (tPLH)
- Fanout of 10 LS-TTL loads
- Quiescent power consumption: 2 μ A maximum at room temperature
- Low input current: 1 μ A maximum

Applications

- This product is general usage and suitable for many different applications.

Part Electrical Specifications

Product	Compliance	Status	Type	Channels	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MM74HC08M	Pb-free	Active	AND	4	2	6	12	null	SOIC-14
	Halide free								
MM74HC08MTC	Pb-free	Active	AND	4	2	6	12	null	TSSOP-14 WB
	Halide free								
MM74HC08MTCX	Pb-free	Active	AND	4	2	6	12	null	TSSOP-14 WB
	Halide free								
MM74HC08MX	Pb-free	Active	AND	4	2	6	12	null	SOIC-14
	Halide free								
MM74HC08SJX	Pb-free	Active	AND	4	2	6	12	null	SOP-14

For more information please contact your local sales support at www.onsemi.com.

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