

Product Overview

MC14073B: Triple 3-Input AND Gate

For complete documentation, see the data sheet.

The B Series logic gates are constructed with P and N channel enhancement mode devices in a single monolithic structure (Complementary MOS). Their primary use is where low power dissipation and/or high noise immunity is desired.

Features

- Supply Voltage Range = 3.0 Vdc to 18 Vdc
- All Outputs Buffered
- Capable of Driving Two Low-power TTL Loads or One Low-power Schottky TTL Load Over the Rated Temperature Range.
- Double Diode Protection on All Inputs Except: Triple Diode Protection on MC14011B and MC14081B
- Pin-for-Pin Replacements for Corresponding CD4000 Series B Suffix Devices
- Pb-Free Packages are Available*

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Type	Channels	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC14073BDG	0.2208	Pb-free Halide free non AEC-Q and PPAP	Active	AND	3	3	18	130	null	SOIC-14
MC14073BDR2G	0.196	Pb-free Halide free non AEC-Q and PPAP	Active	AND	3	3	18	130	null	SOIC-14

For more information please contact your local sales support at www.onsemi.com.

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